

GPIB Command Syntax

for

E1962B cdma2000/IS-95/AMPS Mobile Test Application Revision B.03

1000-1756 (to order use part number 5967-5193)



Agilent Technologies

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Diagram Conventions

Description

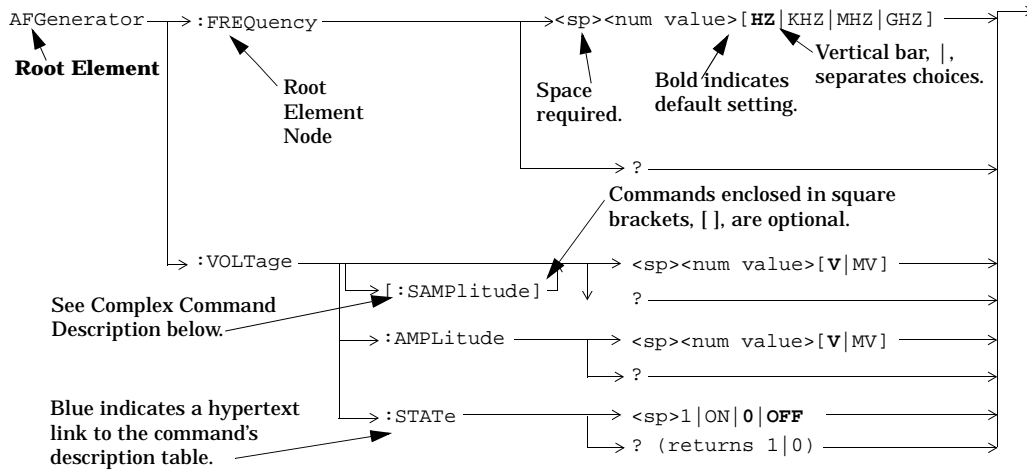


Diagram Conventions

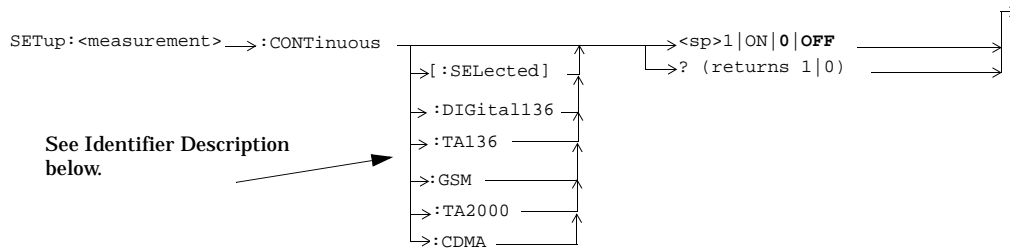


Diagram Description

Statement elements are connected by lines. Each line can be followed in only one direction, as indicated by the arrow at the end of the line. Any combination of statement elements that can be generated by starting at the **Root Element** and following the line the **direction of the arrow** is syntactically correct. The drawings show the proper use of spaces. Where spaces are required they are indicated by **<sp>**, otherwise no spaces are allowed between statement elements.

Complex Command Description

A complex command sets the state of the parameter to ON, and is used to set a value for that parameter. These parameters; amplitude, frequency, gain, number, time, and value can be used as a complex command. Refer to the specific command for the parameter that applies.

Identifier Description

Some test applications are able to test more than one radio format. There may be commands/queries that are shared by more than one radio format in the some of these test applications. Identifiers are used to specify the radio format for the command/query. The command/query is sent to the active radio format if you don't use an identifier. An identifier must be used when sending commands to the inactive radio format.

Developing Code

It is recommended that you set the Test Set's operating environment to debug. To set the Test Set debug mode to "ON" use the following syntax:

```
SYSTem:COMMunicate:GPIB:DEBug ON
```

Units-of-Measure

Amplitude (linear)	V
Frequency	Hz
Power (logarithmic)	dBm
Time	s

ABORt

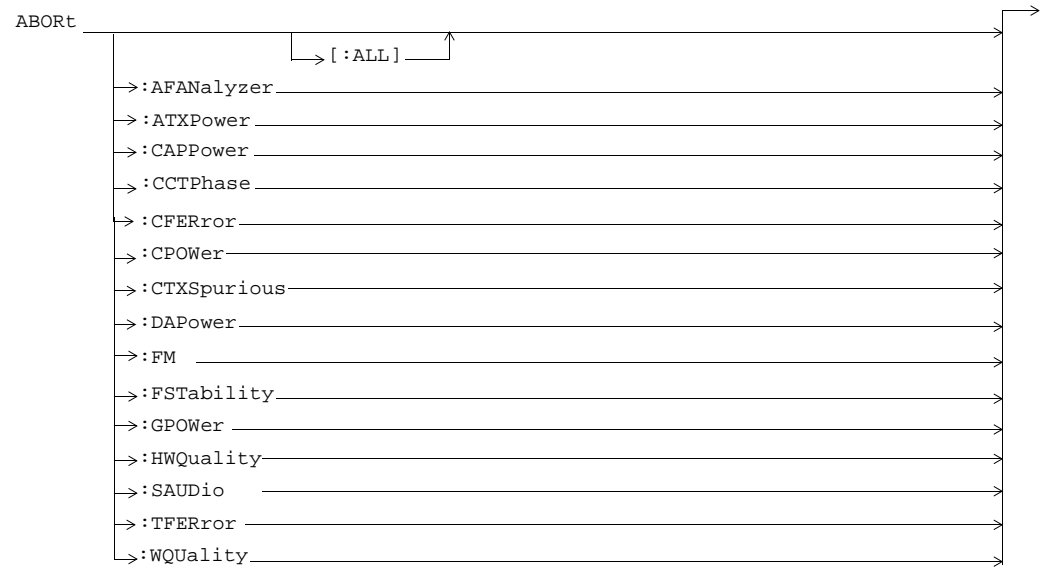
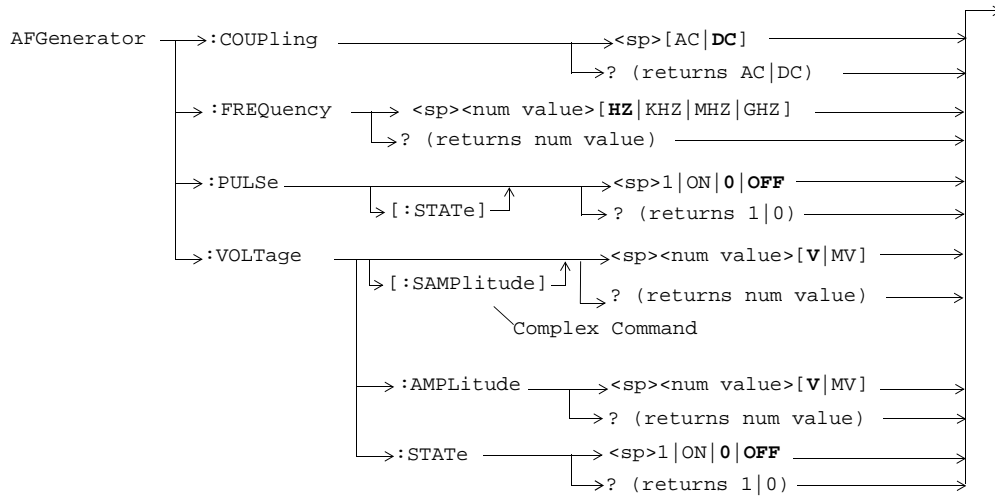
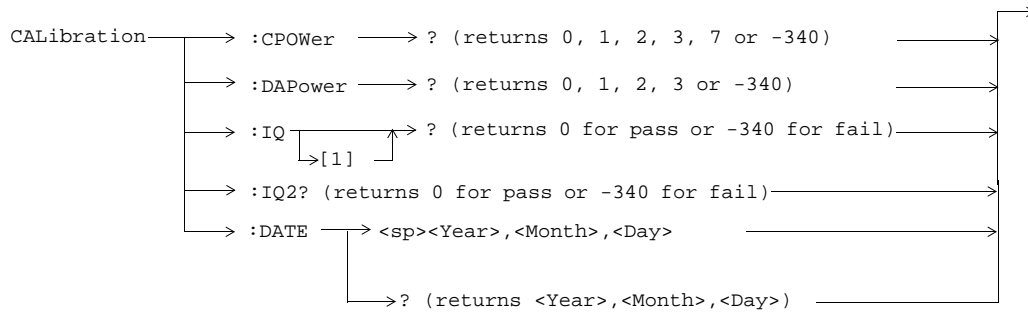


Diagram Conventions

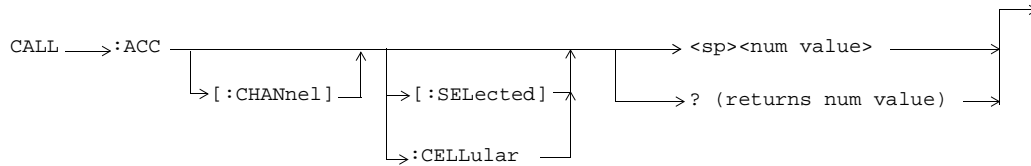
AFGenerator



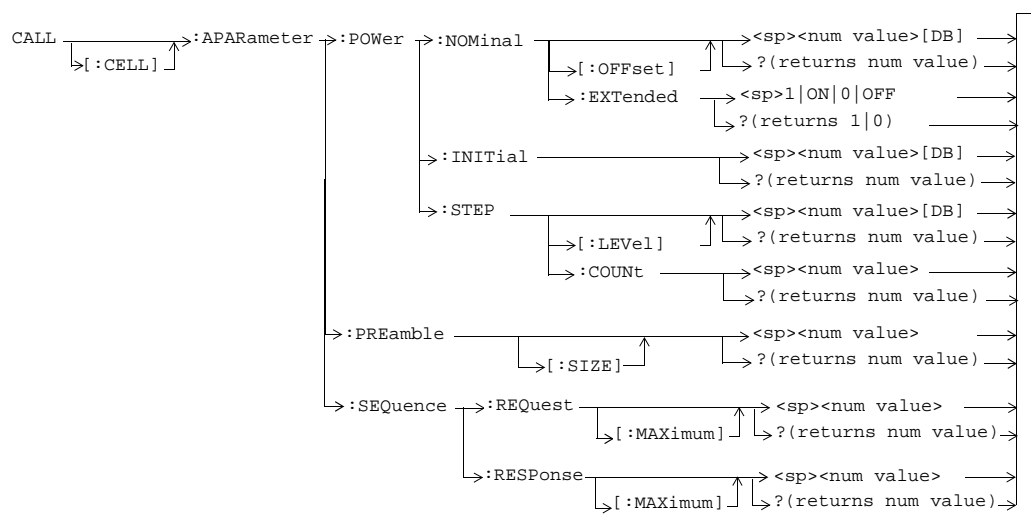
CALibration



CALL:ACC



CALL[:CELL]:APARAmeter



CALL:AVCTest

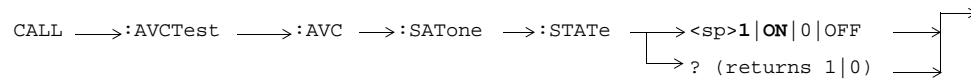
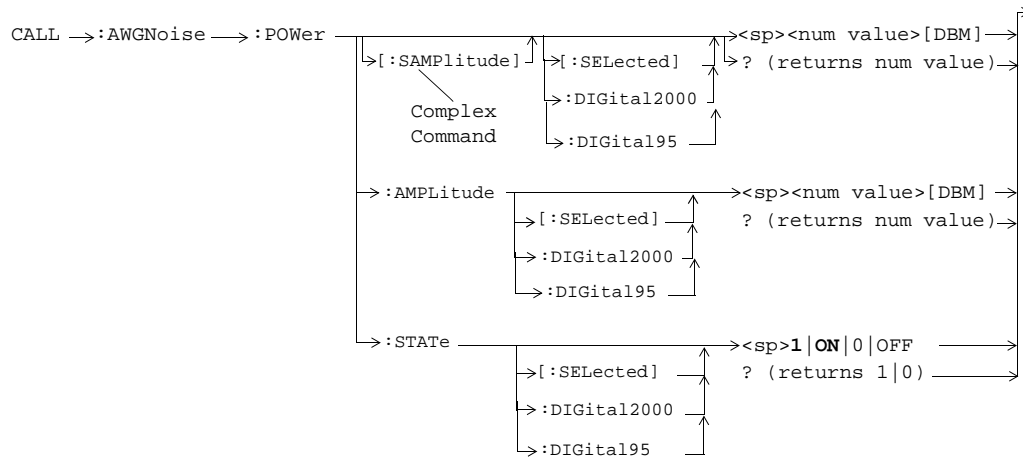
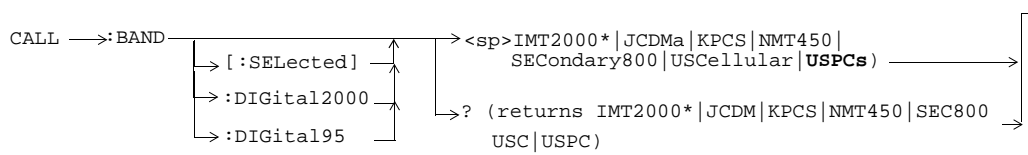


Diagram Conventions

CALL:AWGNoise:POWer

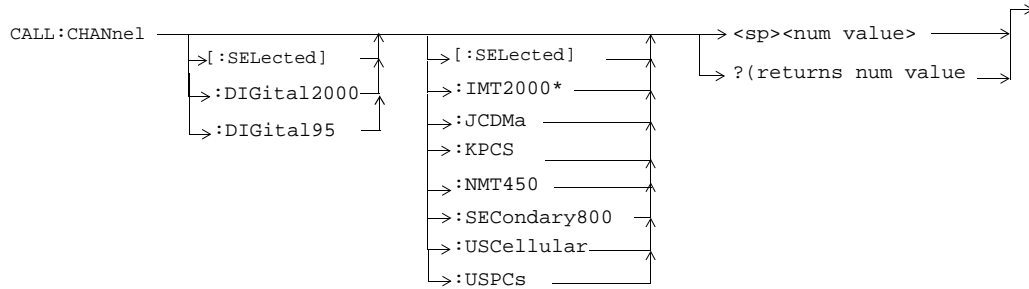


CALL:BAND



* Does not apply to DIGital95

CALL:CHANnel



* Does not apply to DIGital95

CALL[:CELL]:CLPControl

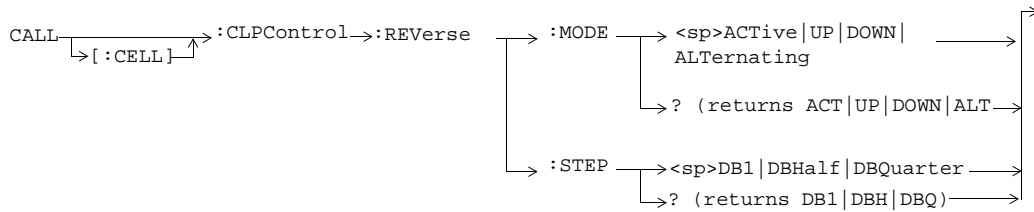
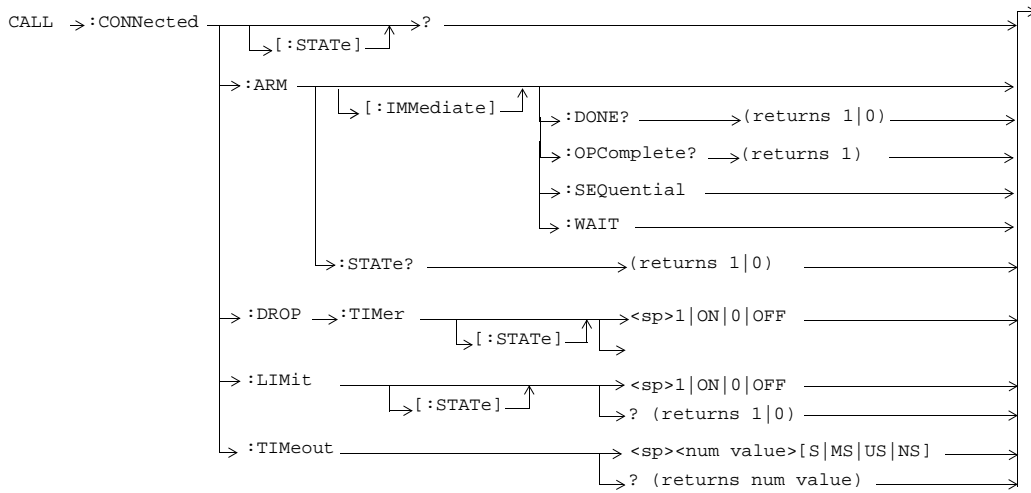
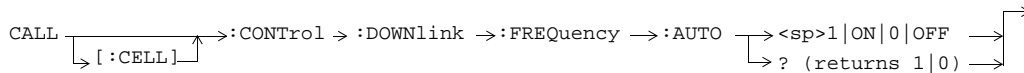


Diagram Conventions

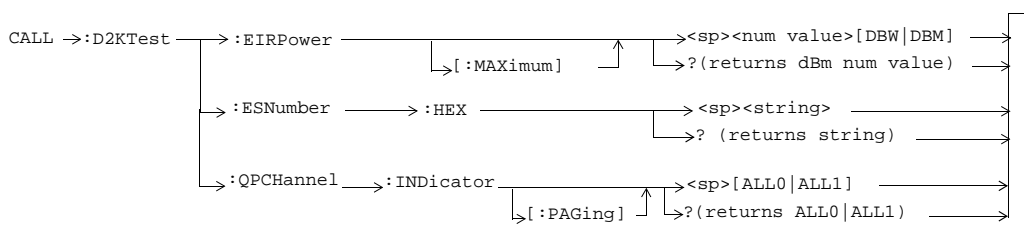
CALL:CONNECTed[:STATe]



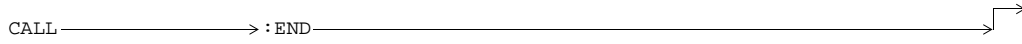
CALL[:CELL]:CONTrol:DOWNlink:FREQuency:AUTO



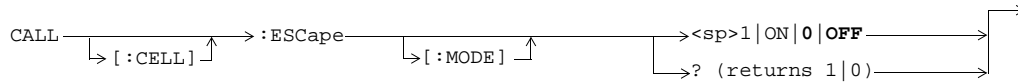
CALL:D2KTest



CALL:END



CALL[:CELL]:ESCAPE[:MODE]



CALL:FCHannel

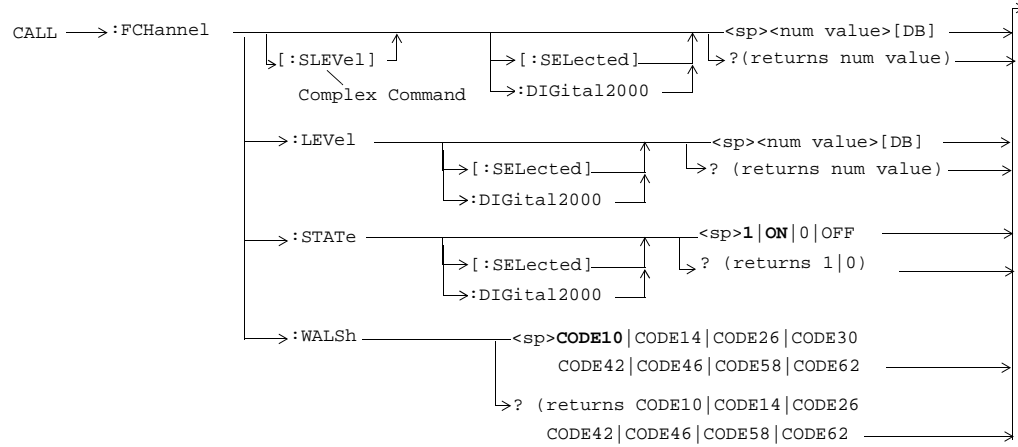
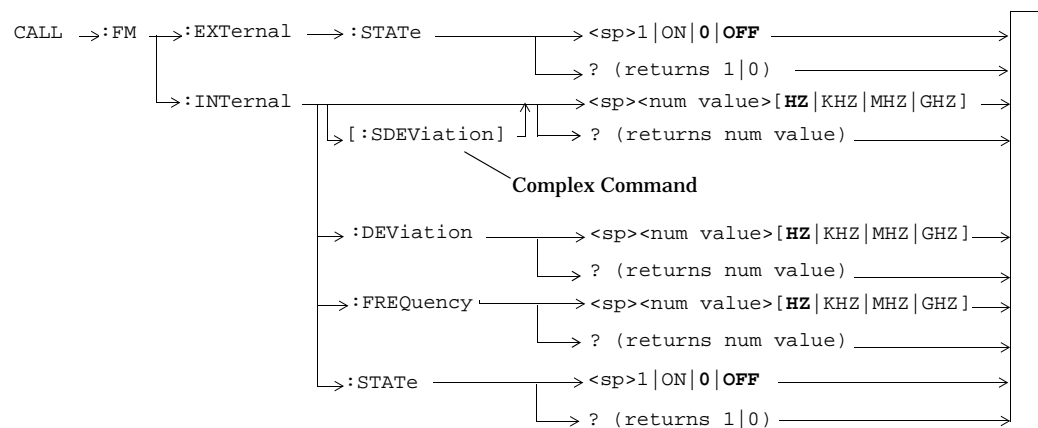
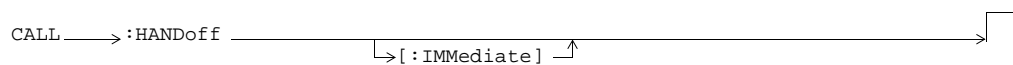


Diagram Conventions

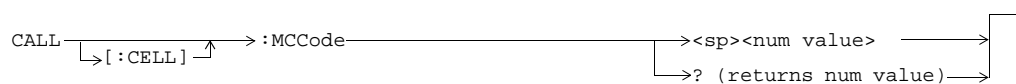
CALL:FM



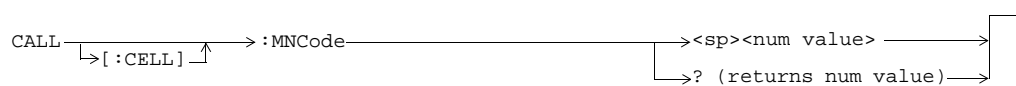
CALL:HANDoff



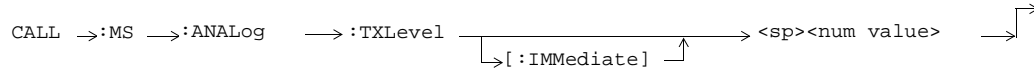
CALL[:CELL]:MCCCode



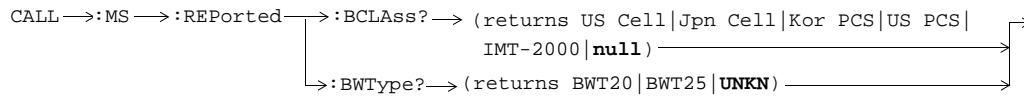
CALL[:CELL]:MNCCode



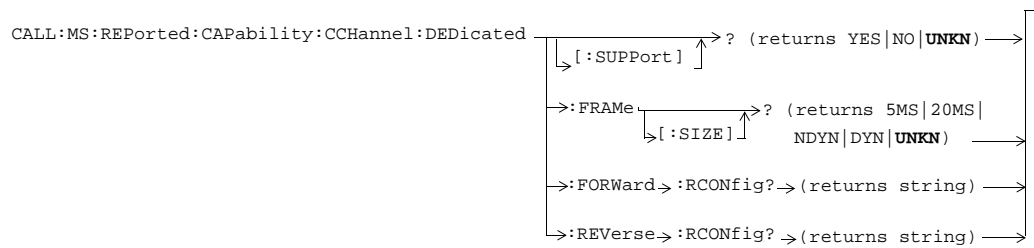
CALL:MS:ANALog



CALL:MS:REPorted:<BCL,BWT>



CALL:MS:REPorted:CAPability:CCHannel:DEDicated



CALL:MS:REPorted:CAPability:CCHannel:DEDicated

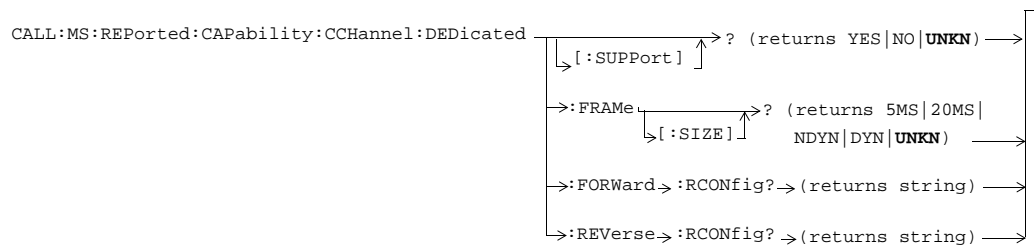
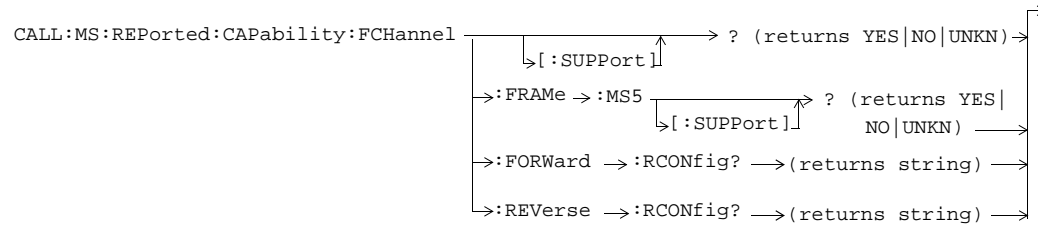
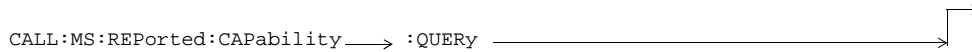


Diagram Conventions

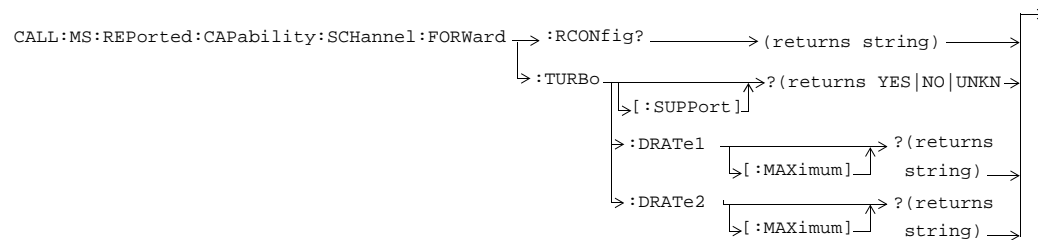
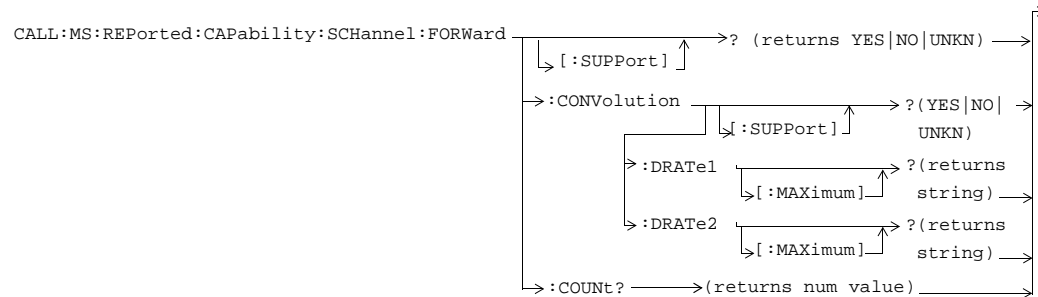
CALL:MS:REPorted:CAPability:FCHannel



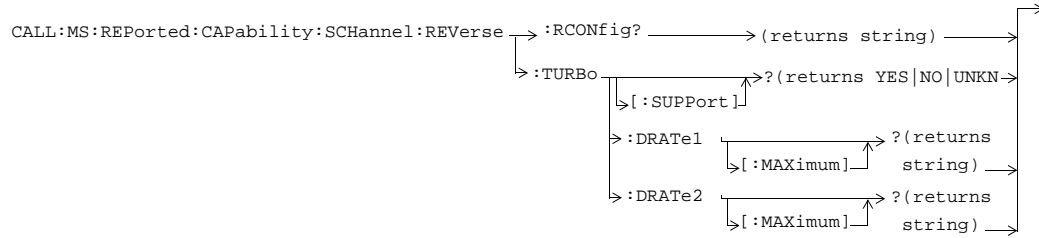
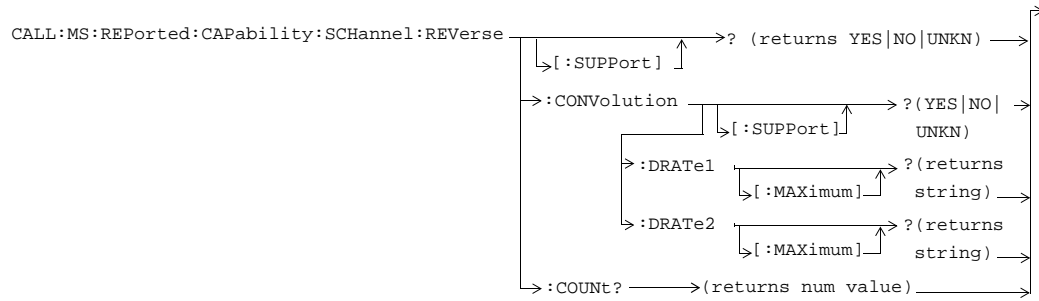
CALL:MS:REPorted:CAPability:QUERy



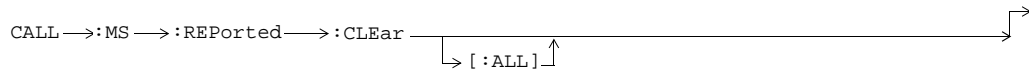
CALL:MS:REPorted:CAPability:SCHannel:FORWARD



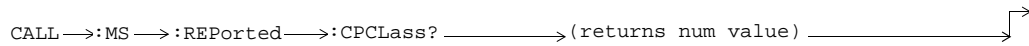
CALL:MS:REPorted:CAPability:SCHannel:REVerse



CALL:MS:REPorted:CLEar



CALL:MS:REPorted:CPCLass



CALL:MS:REPorted:CTXTType

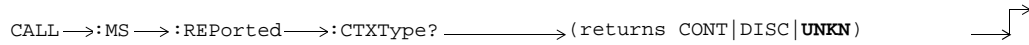
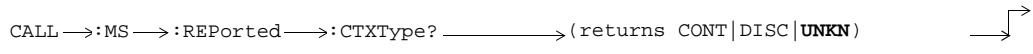
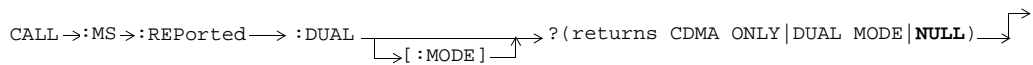


Diagram Conventions

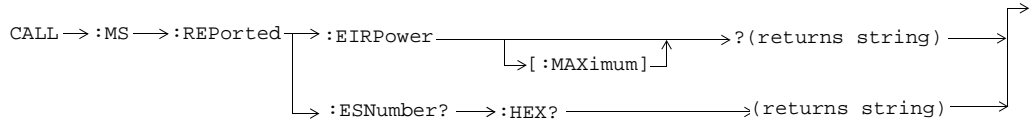
CALL:MS:REPorted:CTXType



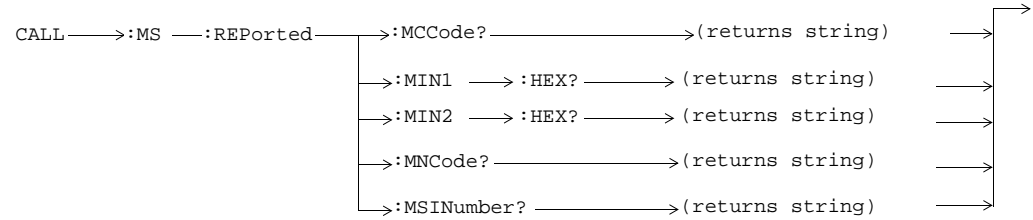
CALL:MS:REPorted:DUAL[:MODE]



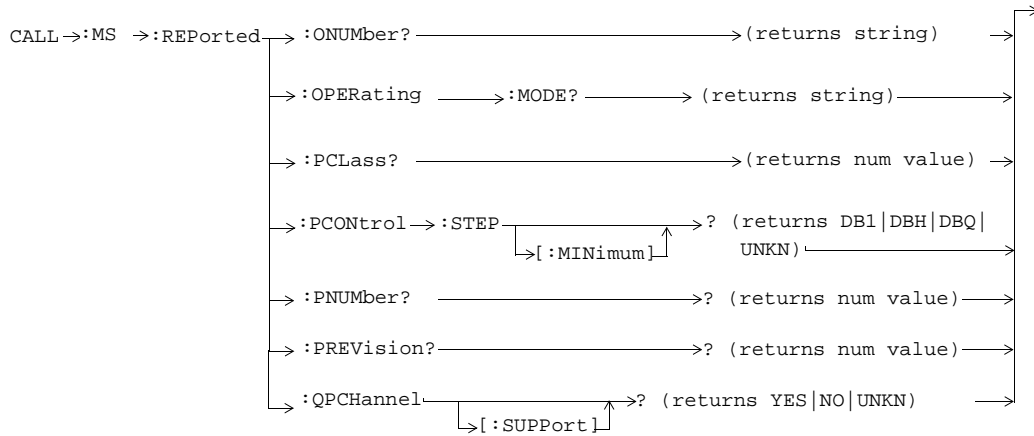
CALL:MS:REPorted:<EIRP,ESN>



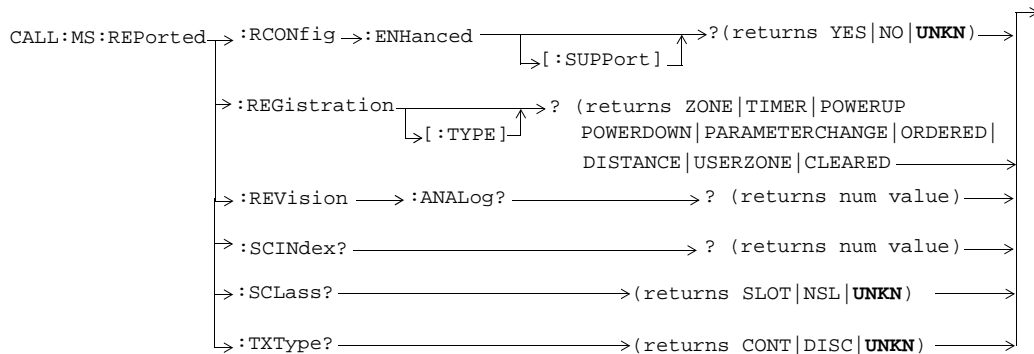
CALL:MS:REPorted:<MCC,MIN1,MIN2,MNC,MSIN>



CALL:MS:REPorted:<ONUM,OPER,PCL,PCON,PNUM,PREV,QPCH>



CALL:MS:REPorted:<RCON,REG,REV,SCIN,SCL,TXT>



CALL[:CELL]:NIDentity

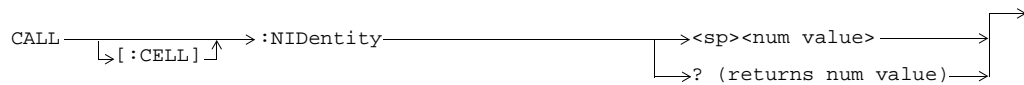
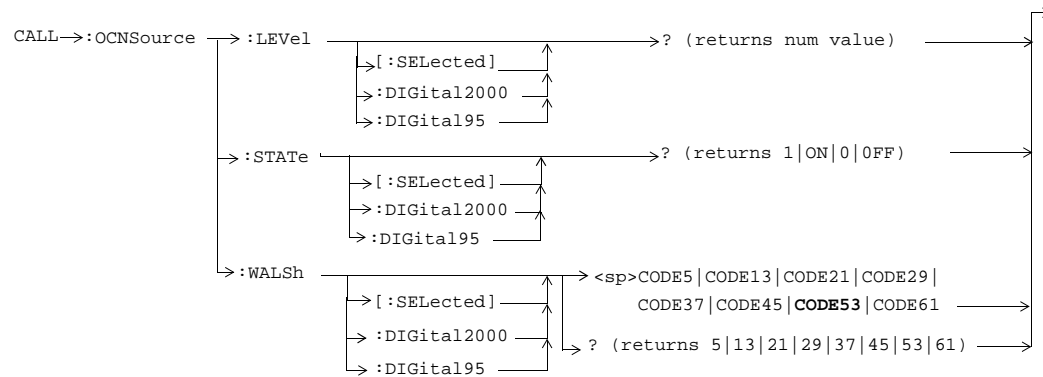
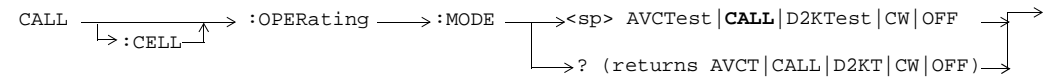


Diagram Conventions

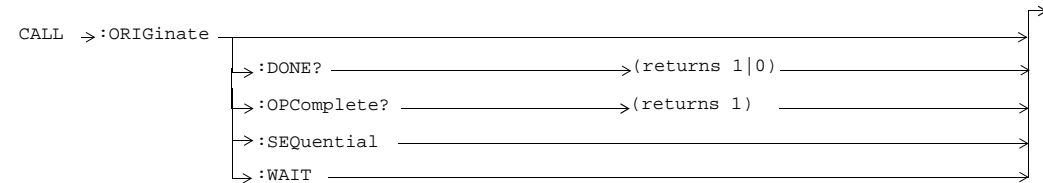
CALL:OCNSource



CALL[:CELL]:OPERating



CALL:ORIGinate



CALL:PAGing

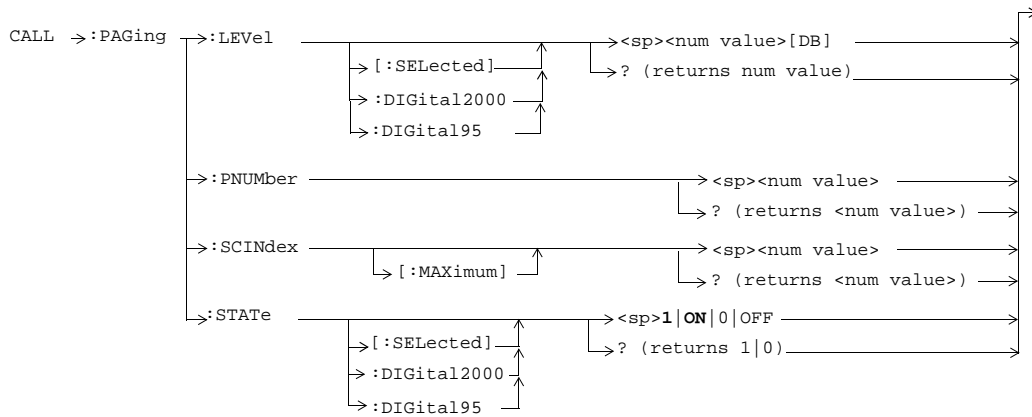
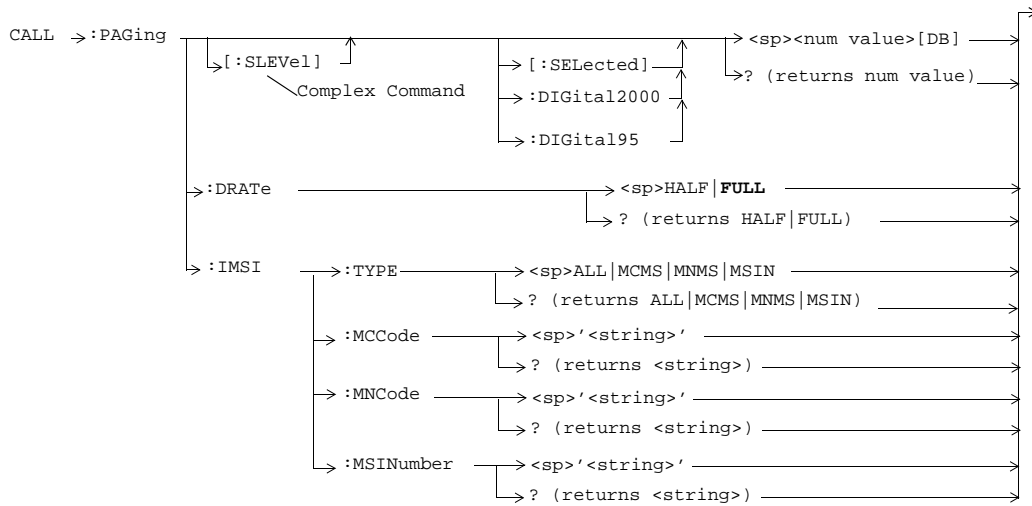
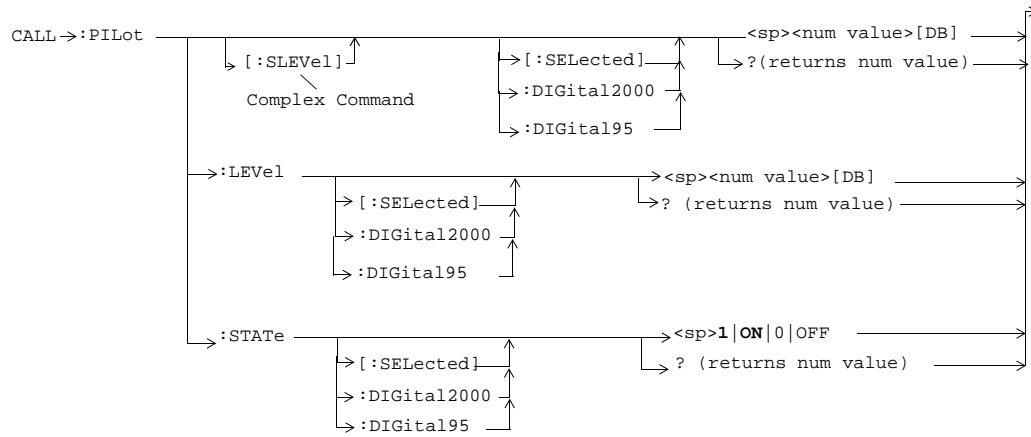
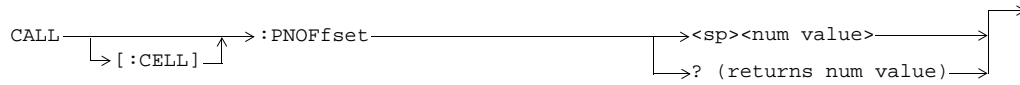


Diagram Conventions

CALL:PILot



CALL[:CELL]:PNOffset



CALL[:CELL]:POWer

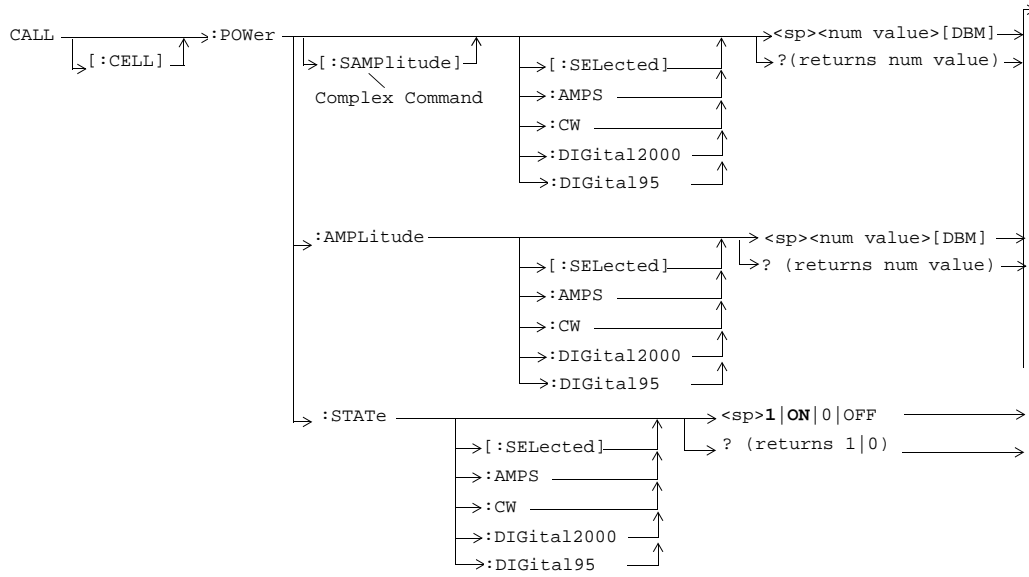
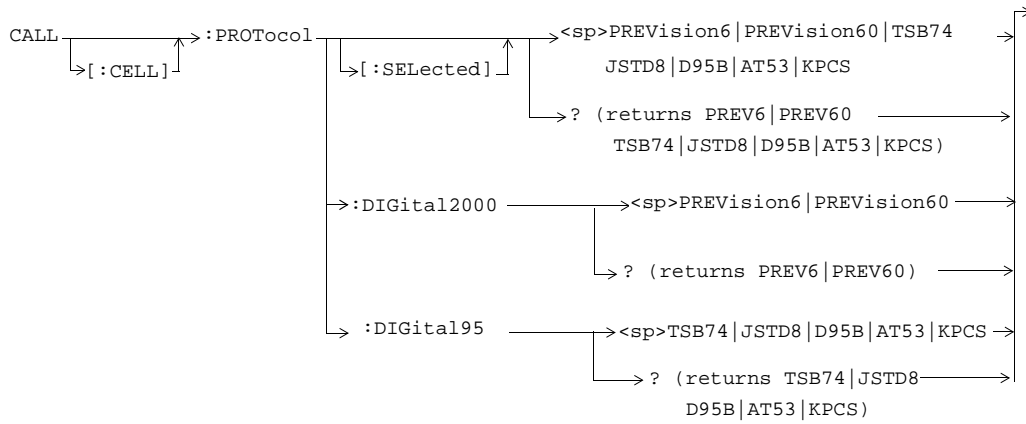
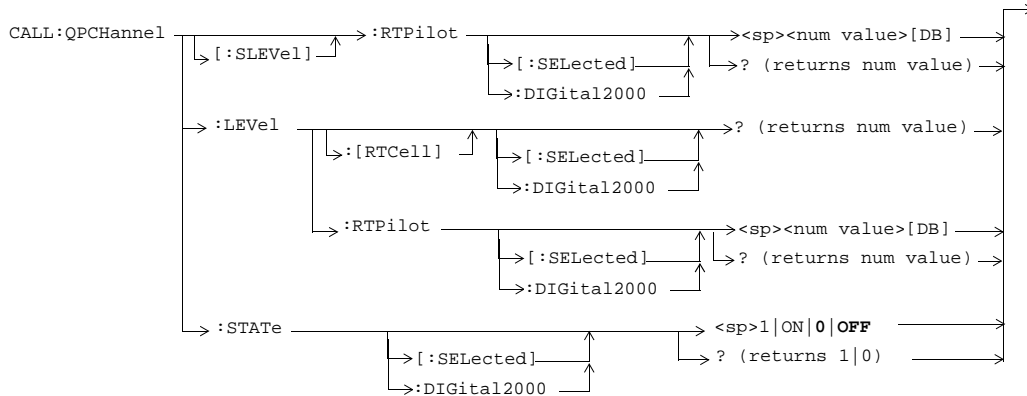


Diagram Conventions

CALL[:CELL]:PROTOcol



CALL:QPCHannel



CALL[:CELL]:RCONfig

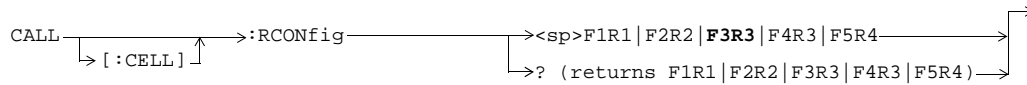
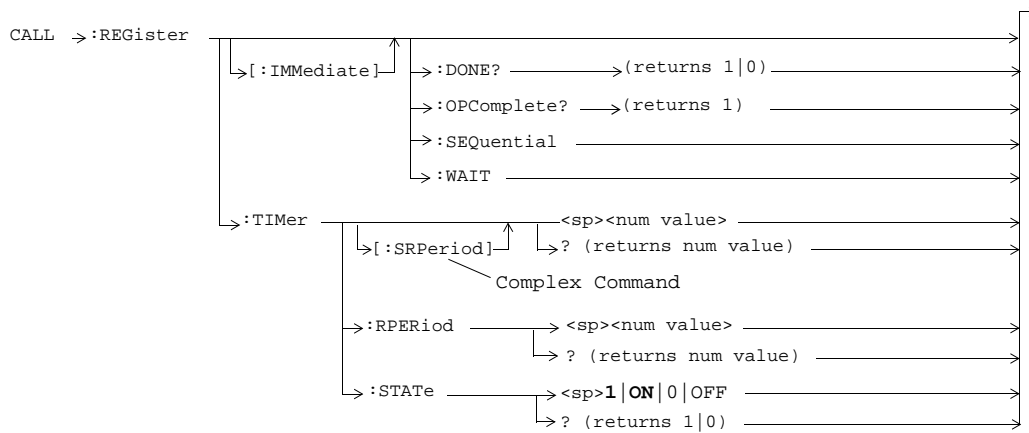
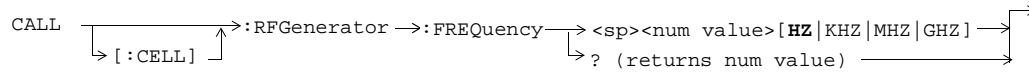


Diagram Conventions

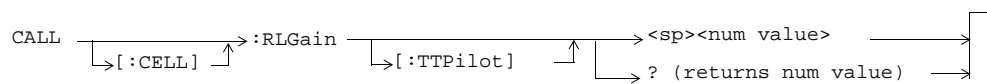
CALL:REGister



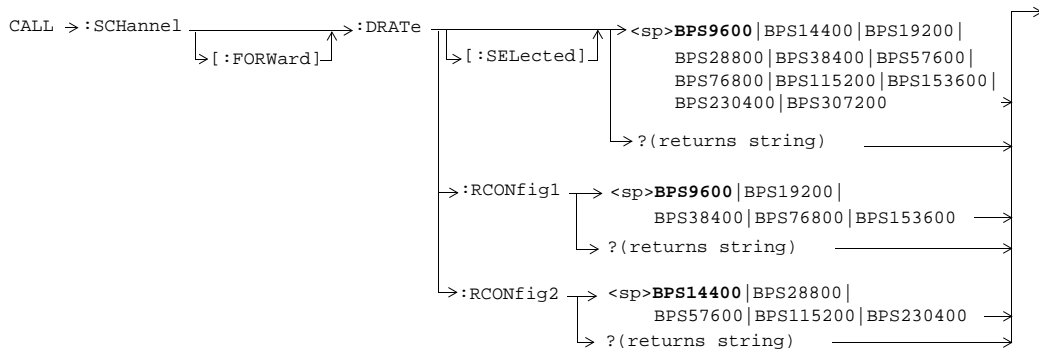
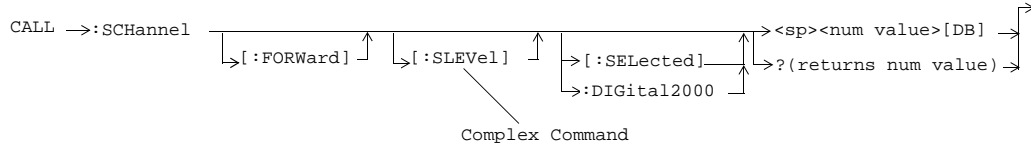
CALL[:CELL]:RFGenerator



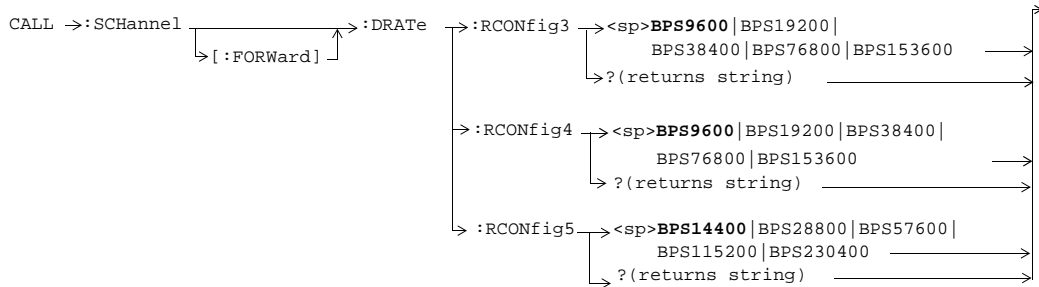
CALL[:CELL]:RLGain



CALL:SCHannel

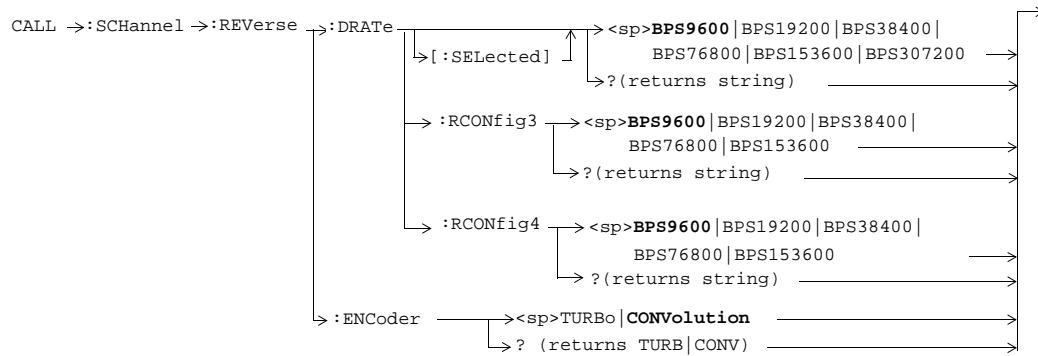
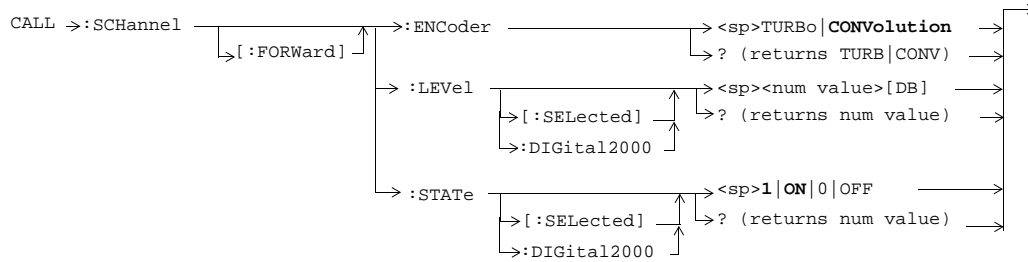


Note: Data rates used with the SElected keyword must be compatible with the currently selected radio configuration. If a data rate that does not belong to the selected radio configuration's rate set is used, an error message will be generated.

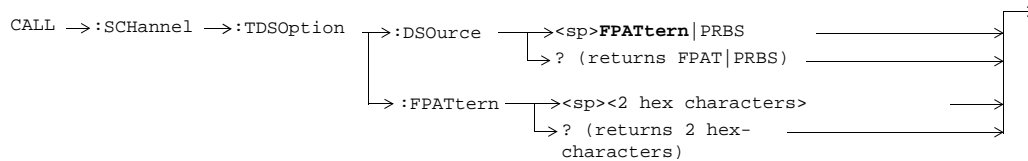


Note: Data rates used with the SElected keyword must be compatible with the currently selected radio configuration. If a data rate that does not belong to the selected radio configuration's rate set is used, an error message will be generated.

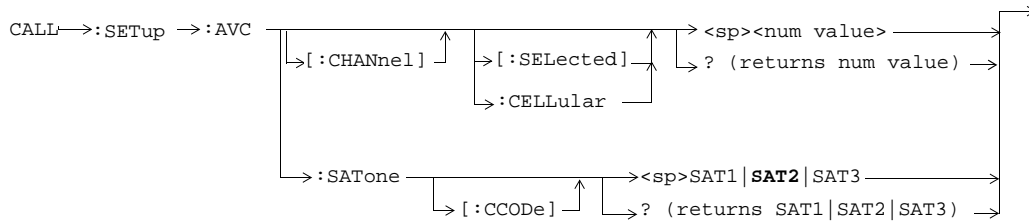
Diagram Conventions



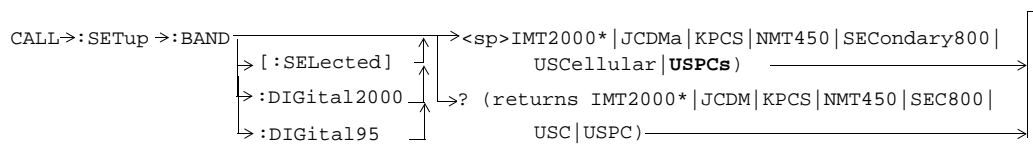
Note: Data rates used with the SElected keyword must be compatible with the currently selected radio configuration. If a data rate that does not belong to the selected radio configuration's rate set is used, an error message will be generated.



CALL:SETup:AVC

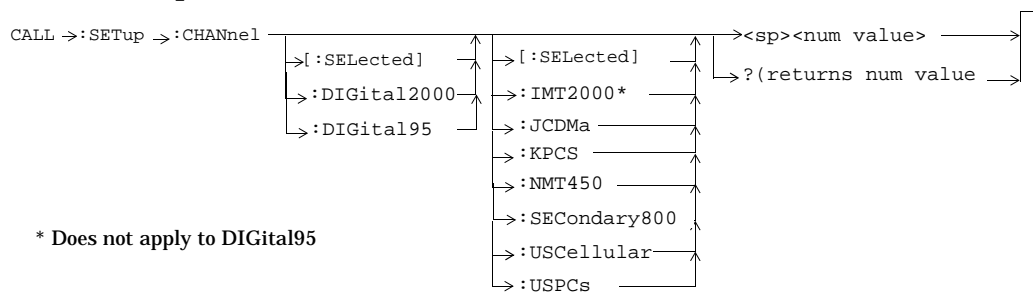


CALL:SETup:BAND



* Does not apply to DIGital95

CALL:SETup:CHANnel



* Does not apply to DIGital95

CALL:SETup:HANDoff

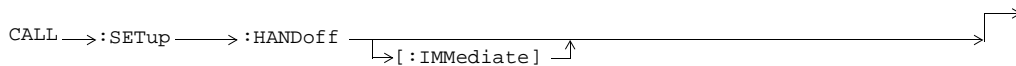
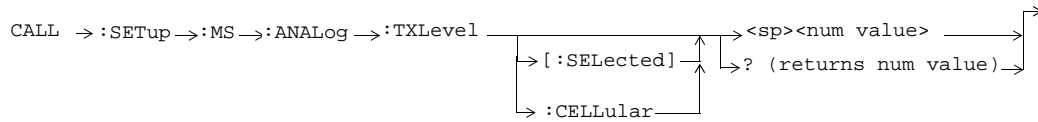
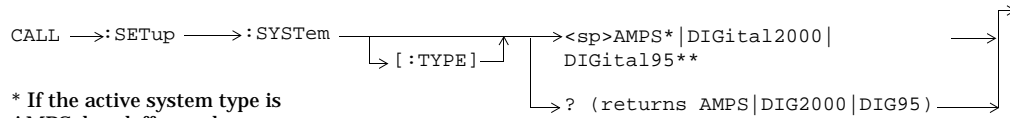


Diagram Conventions

CALL:SETup:MS:ANALog



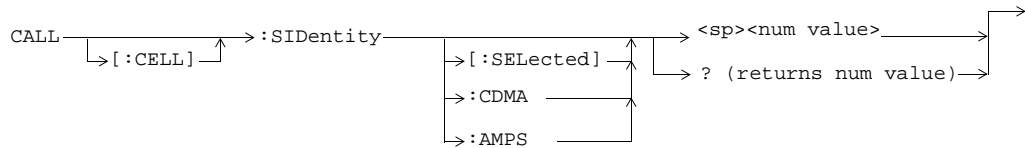
CALL:SETup:SYSTEM



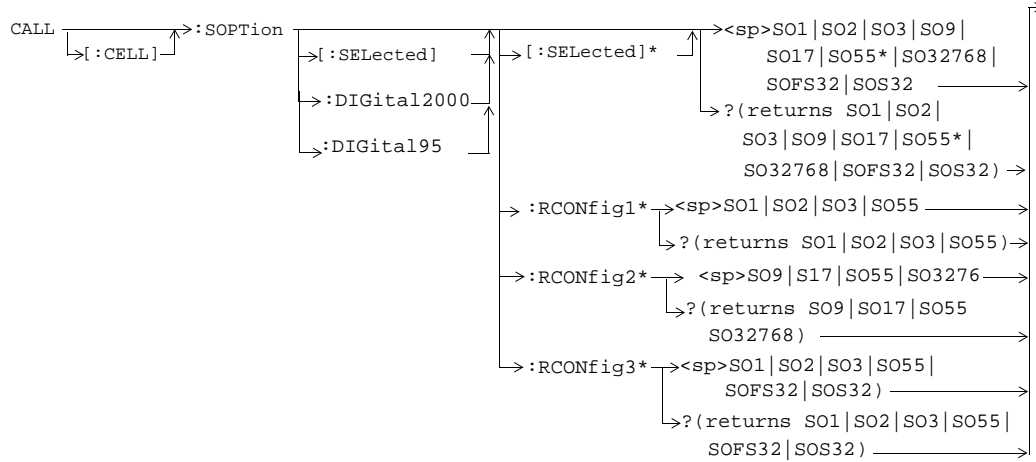
* If the active system type is AMPS, handoffs to other system types are not allowed.

** If the active system type is DIGital2000, handoffs to DIGital95 are not allowed.

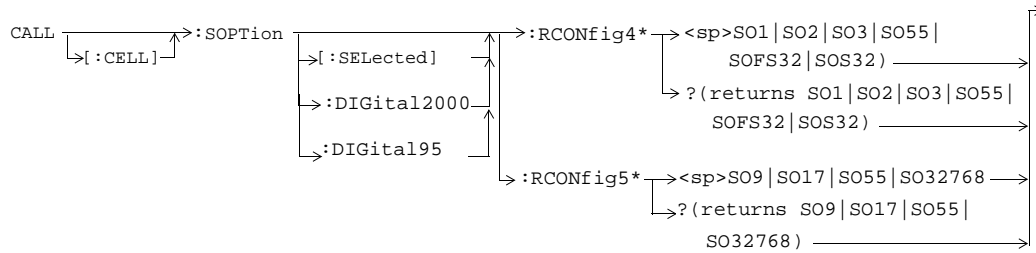
CALL[:CELL]:SIDentity



CALL[:CELL]:SOPTION



* Does not apply to DIGital95



* Does not apply to DIGital95

Diagram Conventions

CALL:STATUS

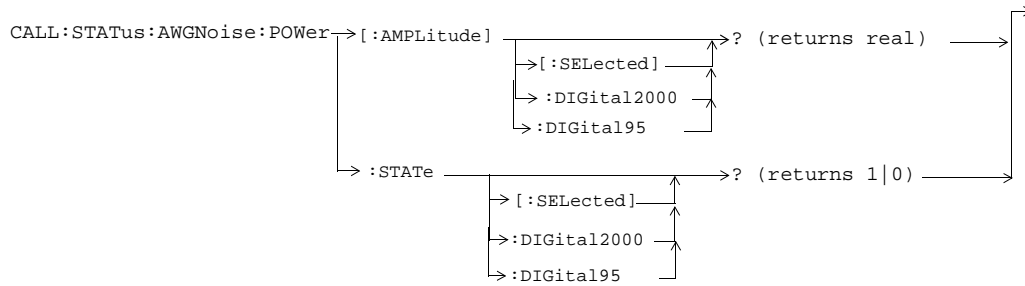
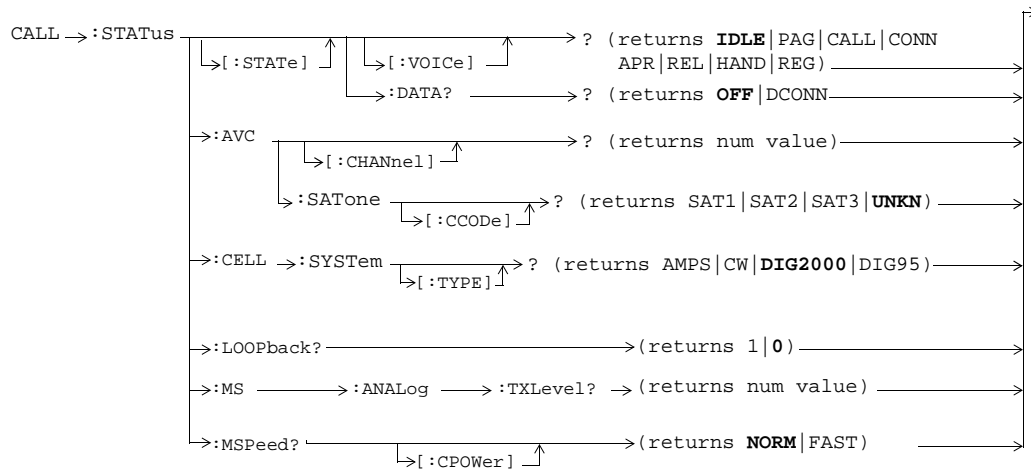


Diagram Conventions

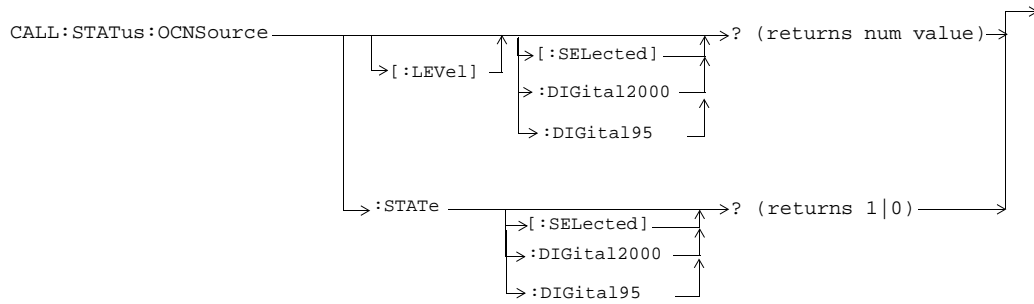
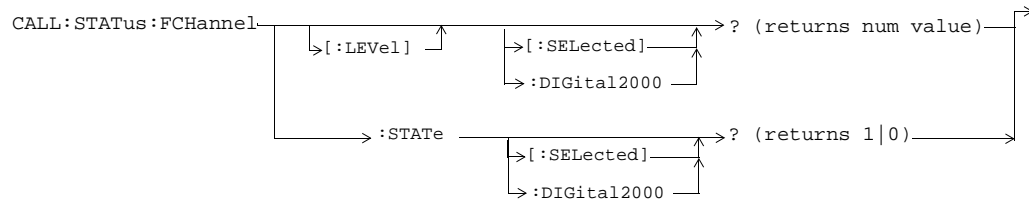
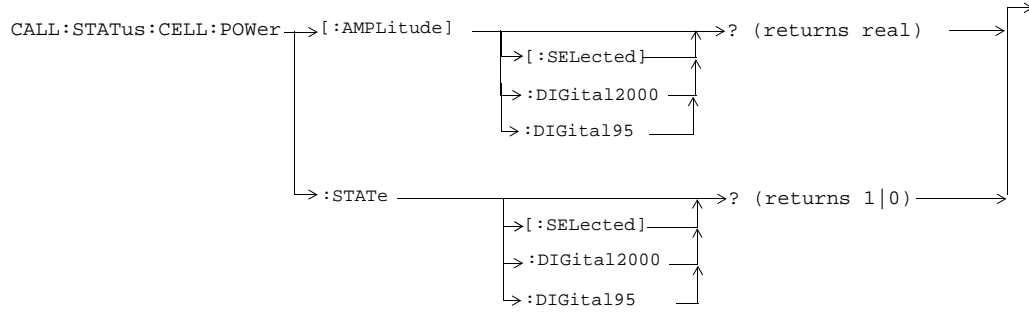


Diagram Conventions

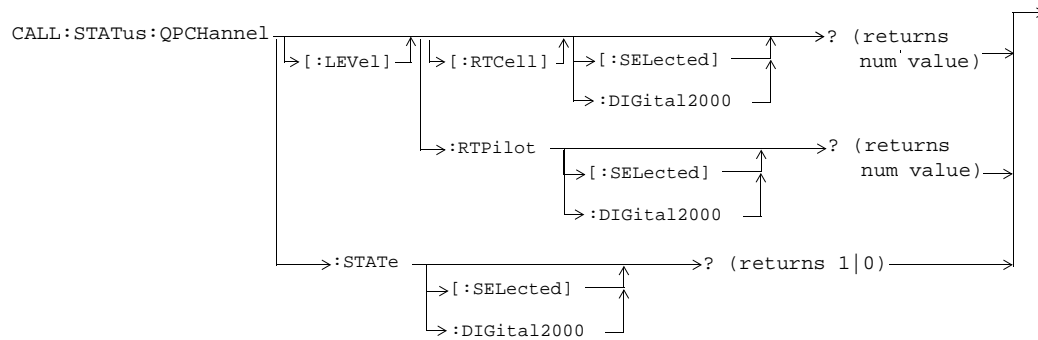
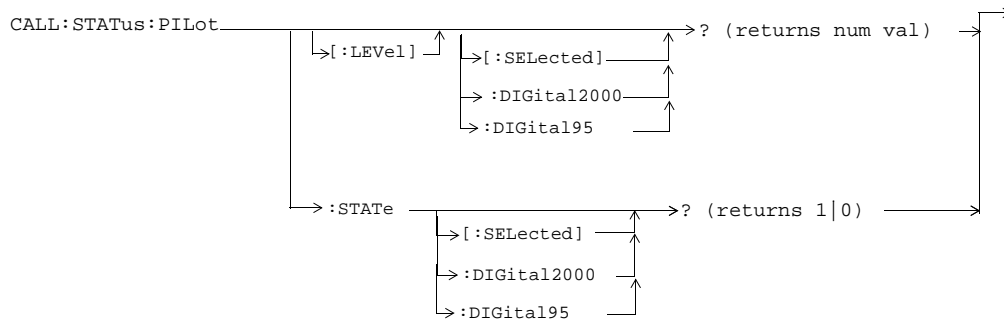
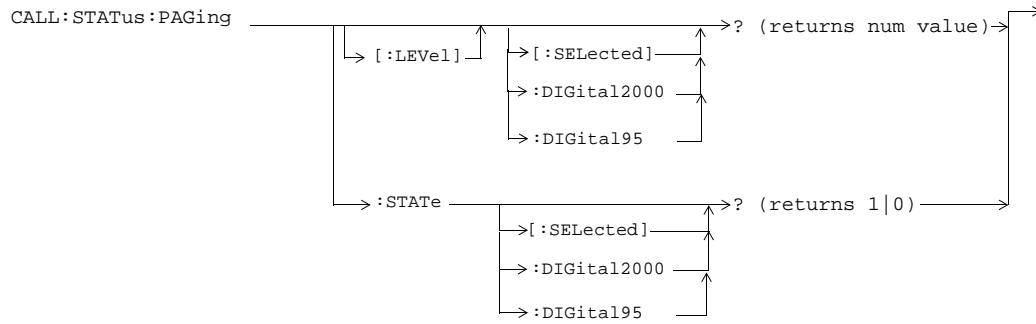


Diagram Conventions

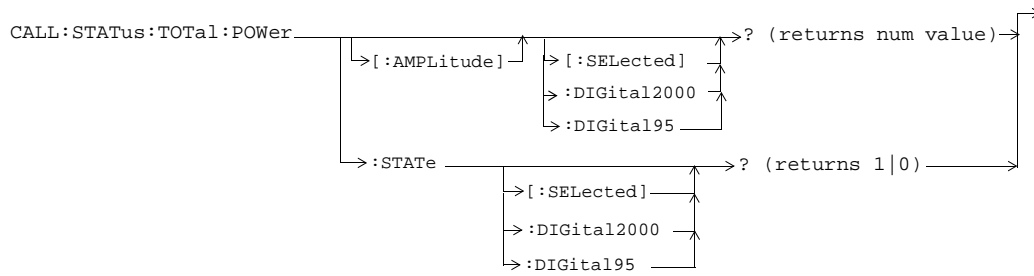
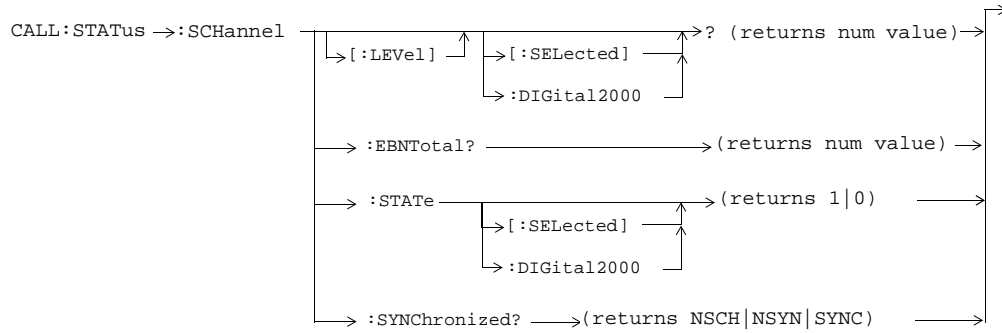
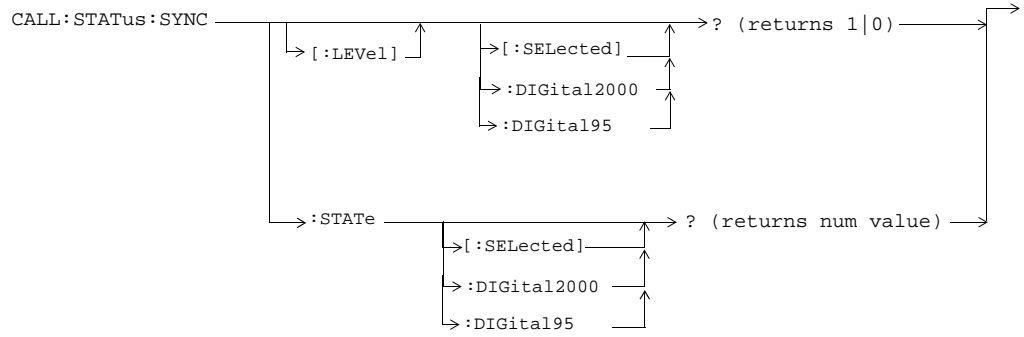
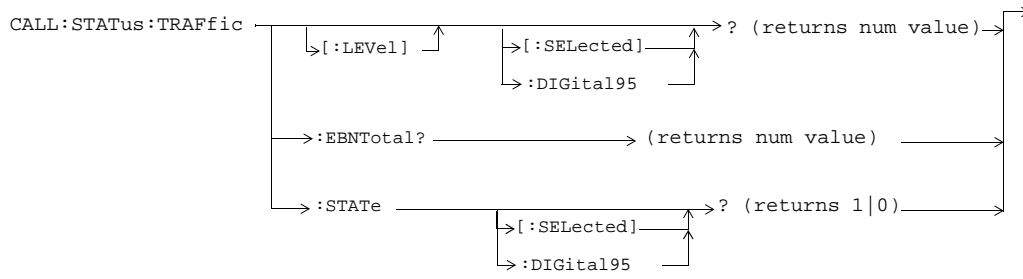
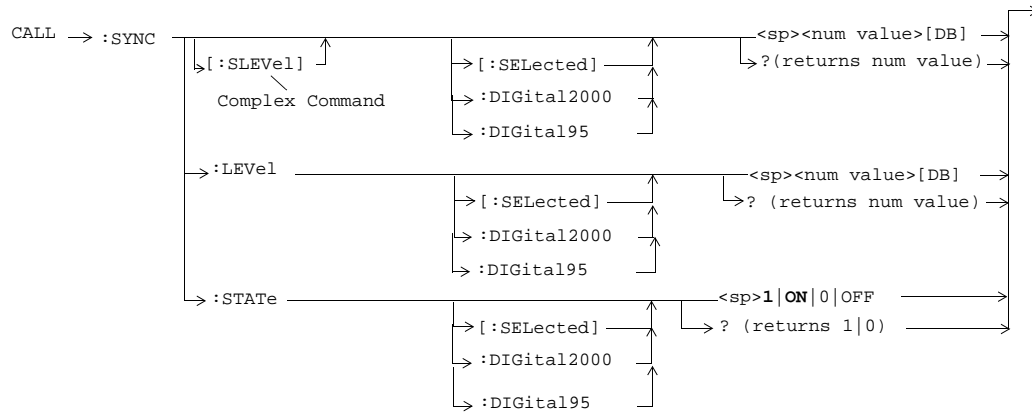


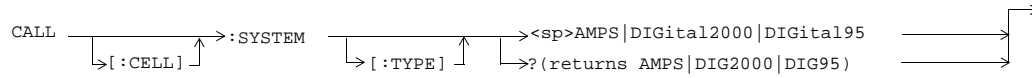
Diagram Conventions



CALL:SYNC



CALL[:CELL]:SYSTEM



CALL:TOTAl:POWer

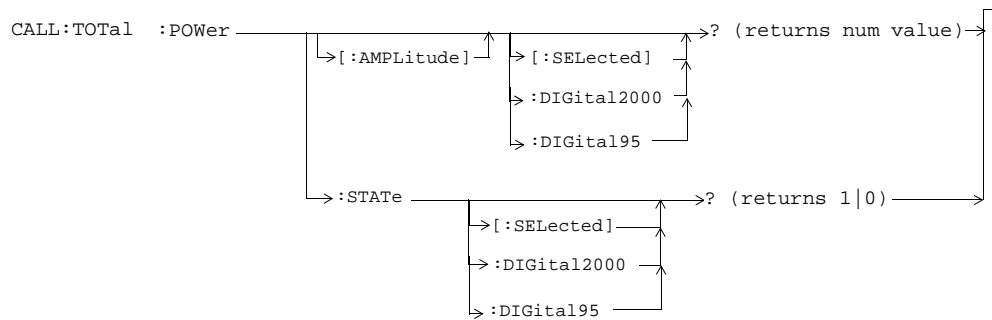
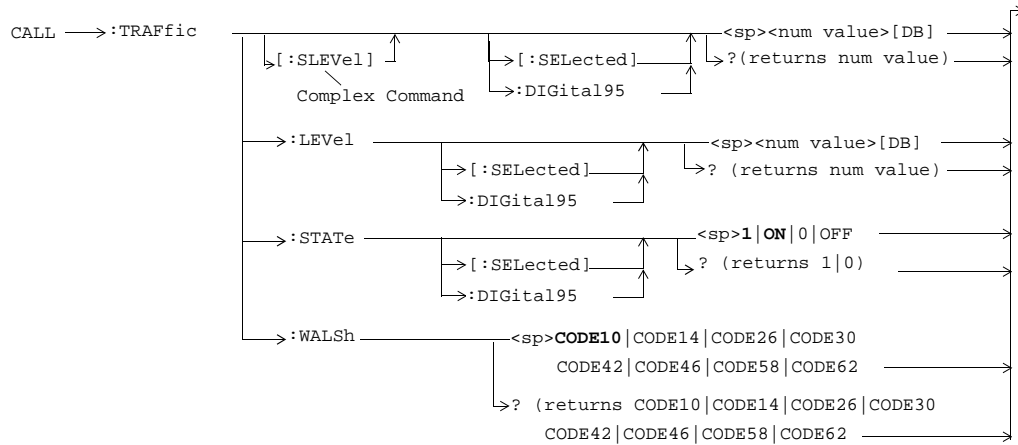
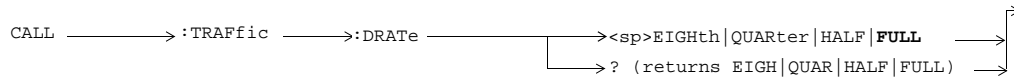
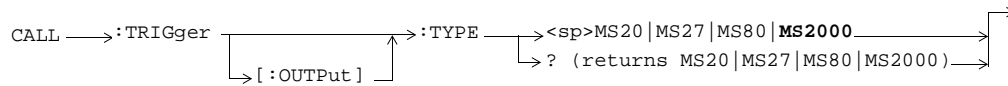


Diagram Conventions

CALL:TRAFfic



CALL:TRIGger:TYPE



DISPlay

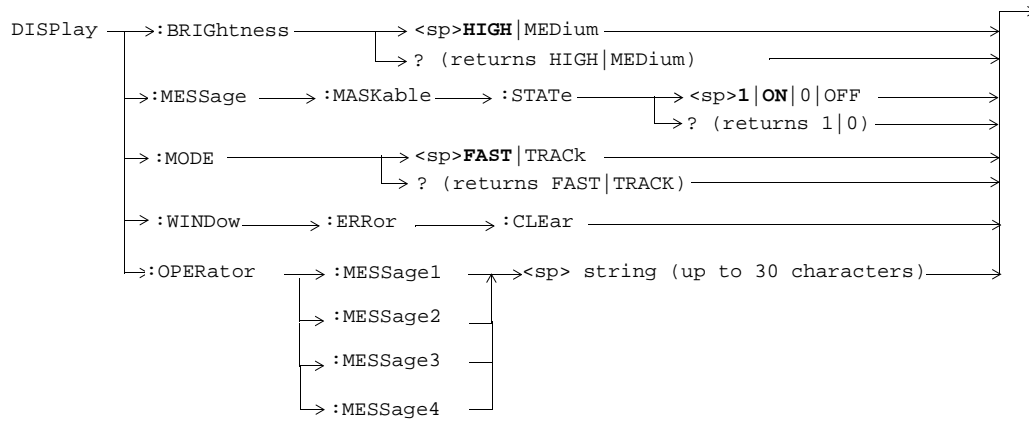


Diagram Conventions

FETCh:AFANalyzer

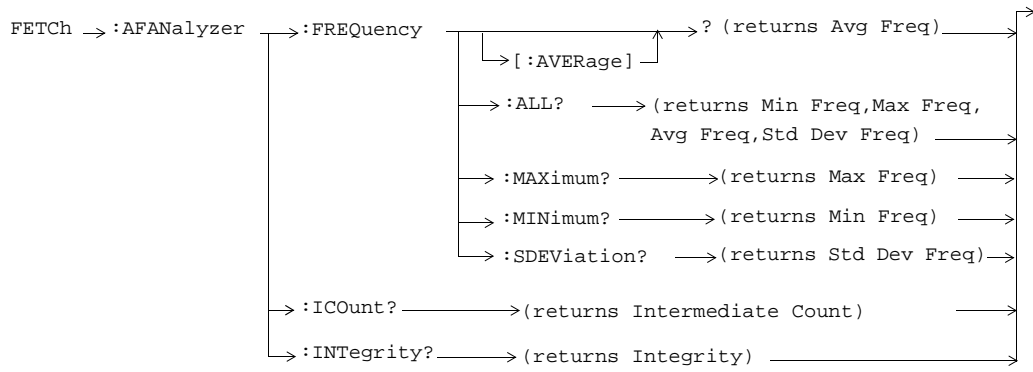
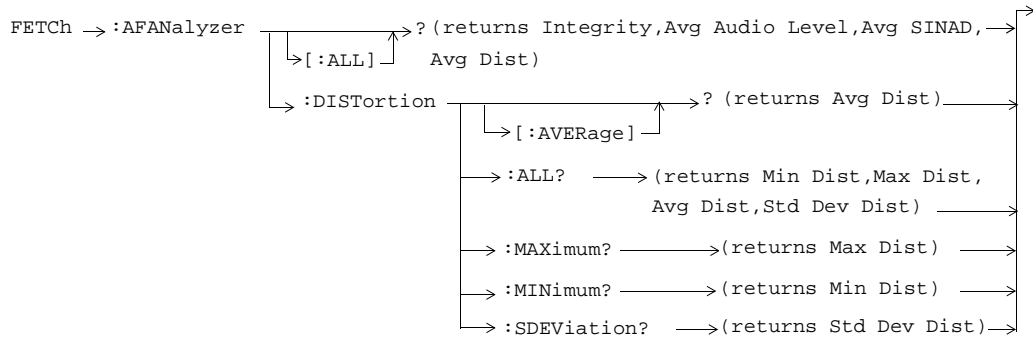


Diagram Conventions

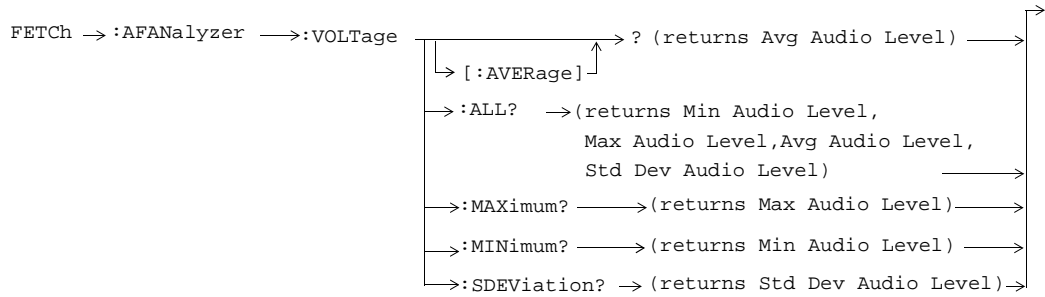
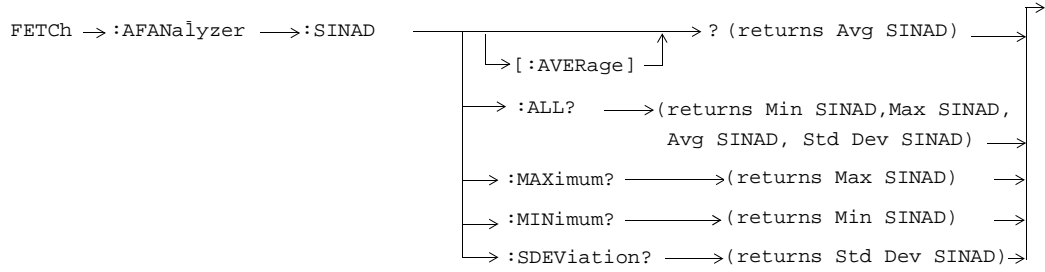
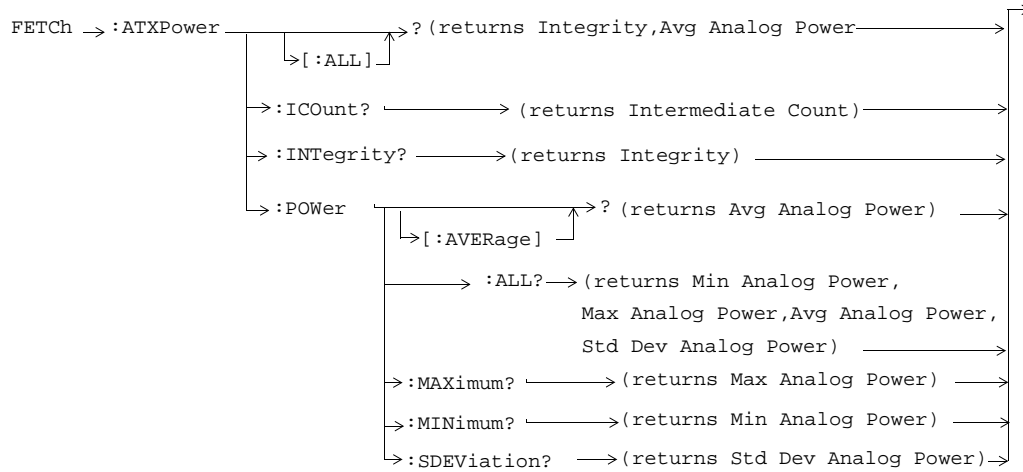
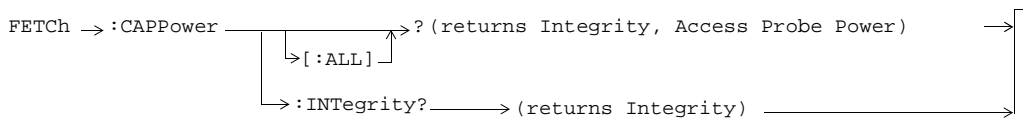


Diagram Conventions

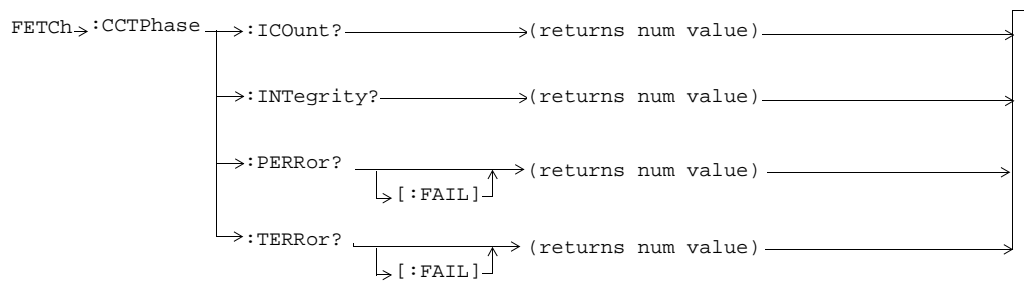
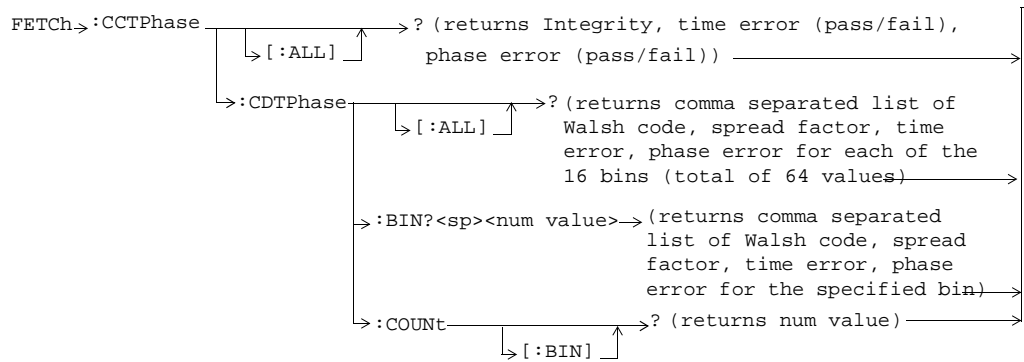
FETCH:ATXPower



FETCH:CAPPower



FEtCh:CCTPhase



FEtCh:CFERror

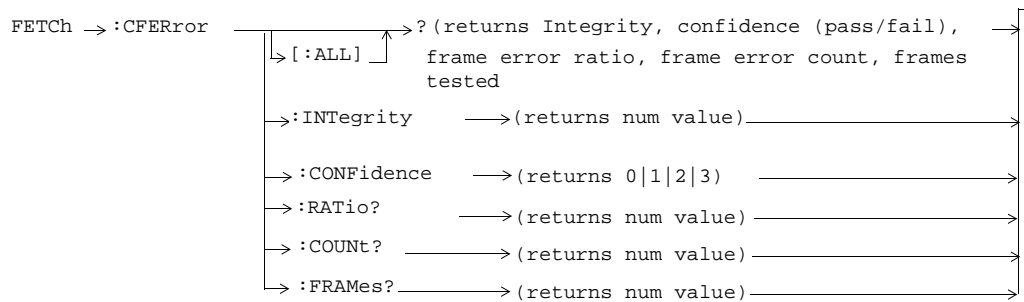
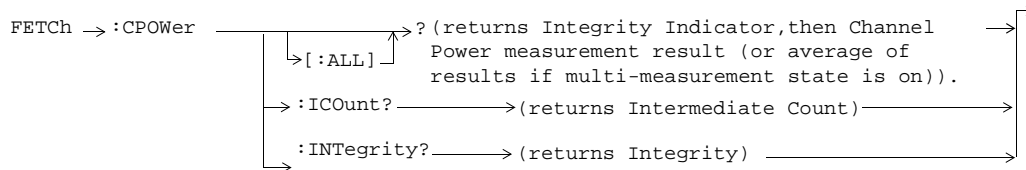
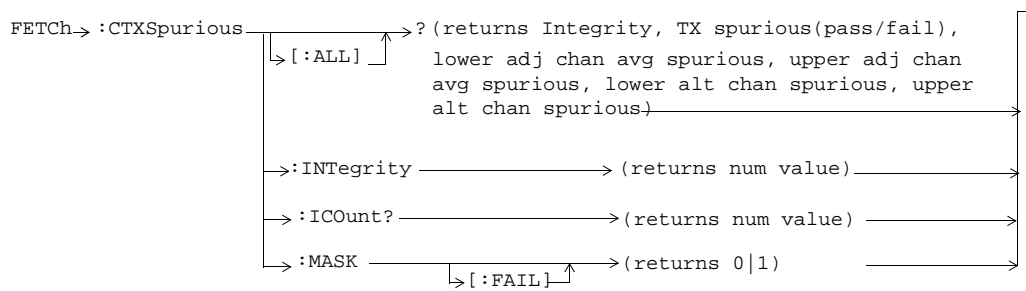


Diagram Conventions

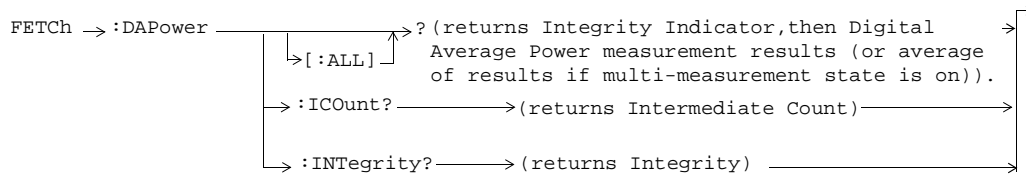
FETCH:CPOWer



FETCH:CTXSpurious



FETCH:DAPower



FETCH:FM

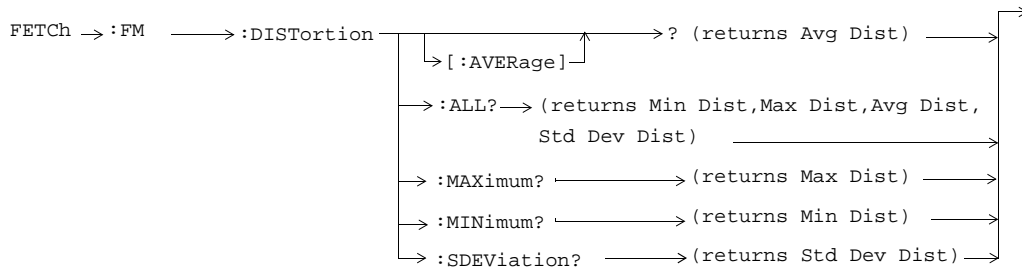
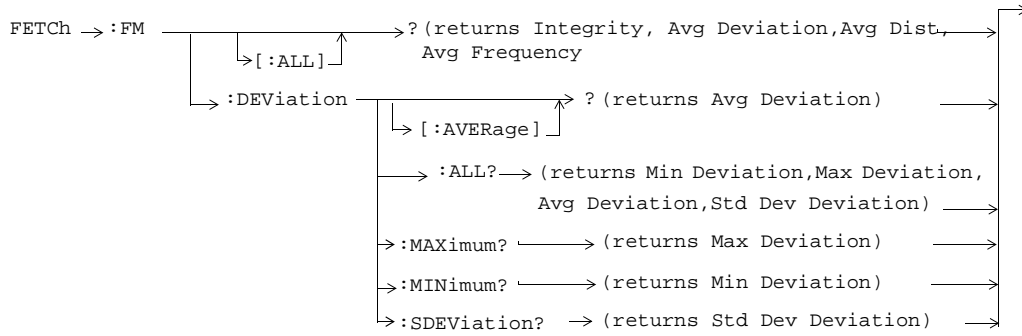
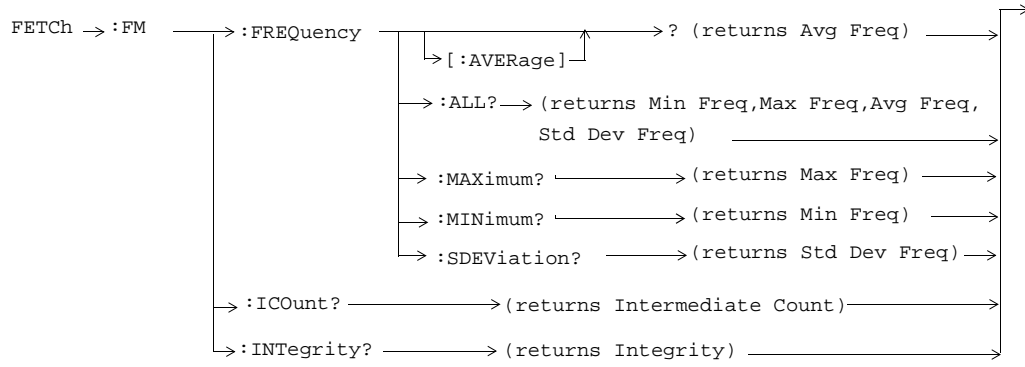


Diagram Conventions



FETCH:FSTability

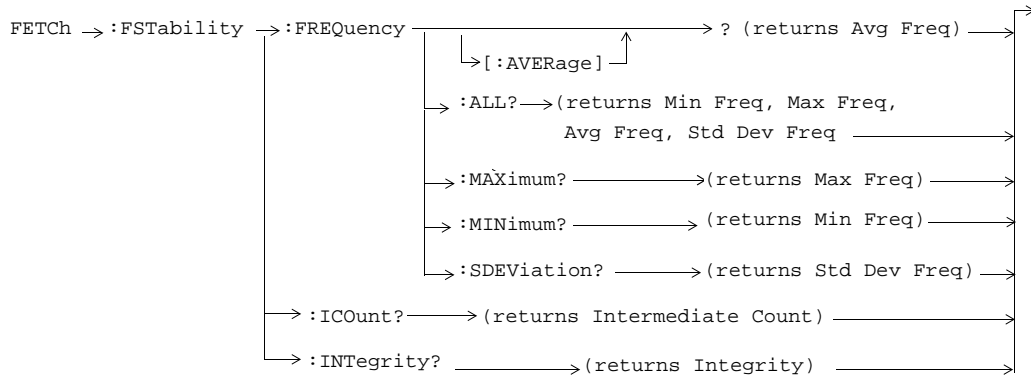
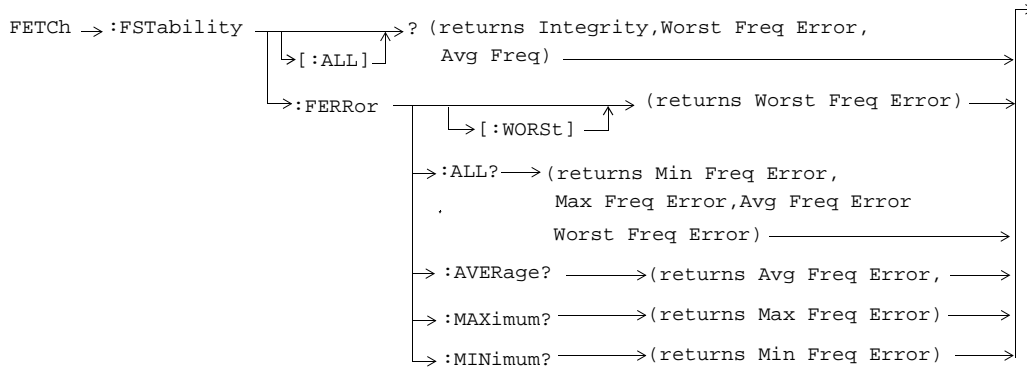
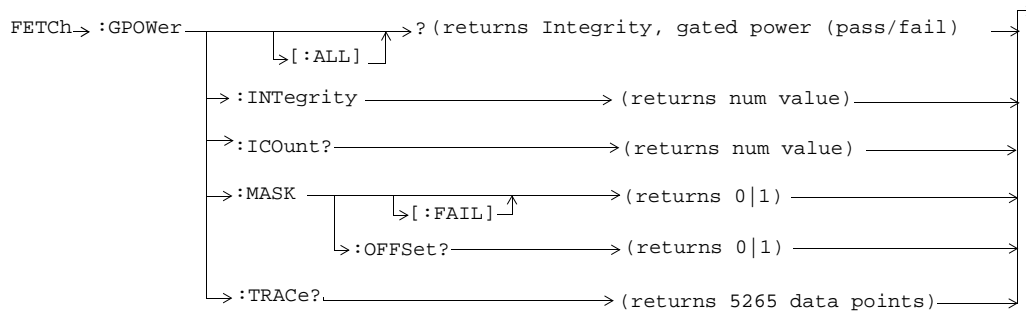
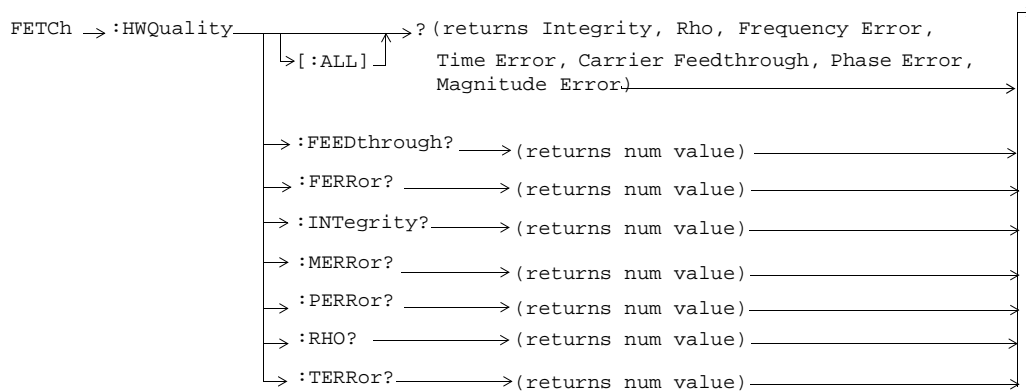


Diagram Conventions

FETCH:GPOWer



FETCH:HWQuality



FETCH:SAudio

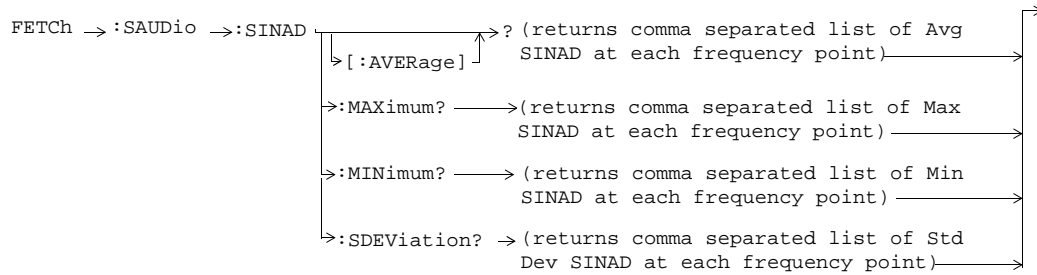
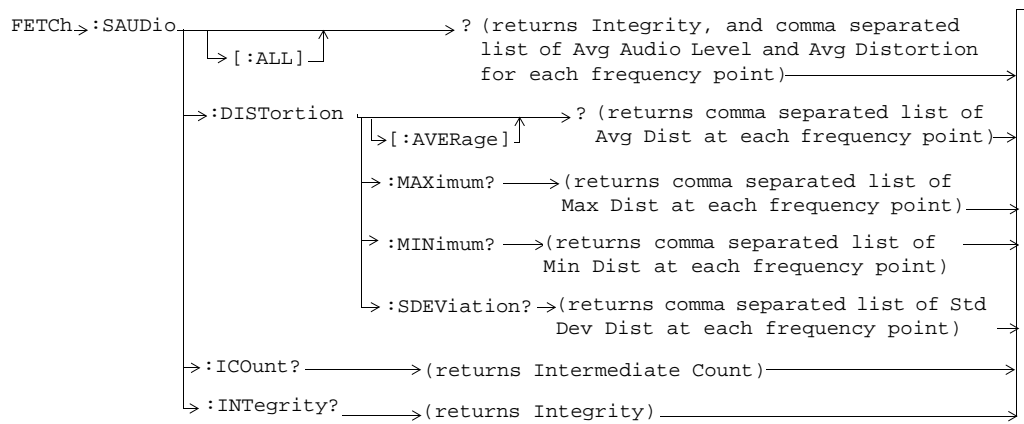
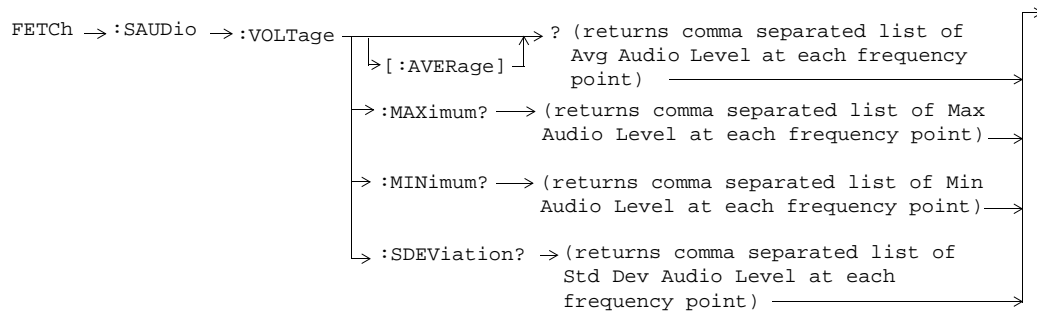


Diagram Conventions



FETCH:TFERror

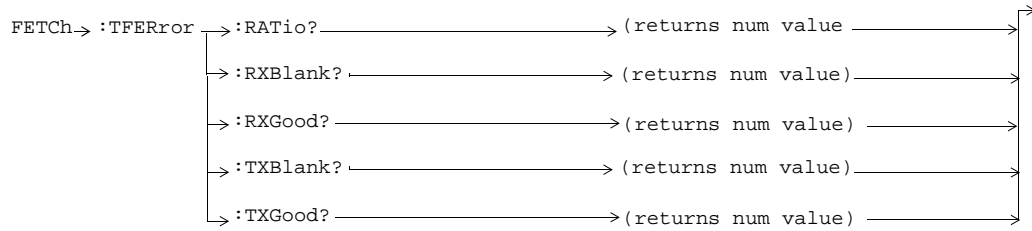
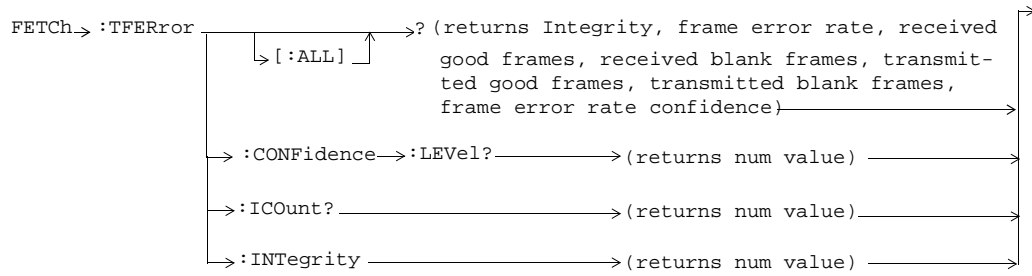


Diagram Conventions

FETCH:WQuality

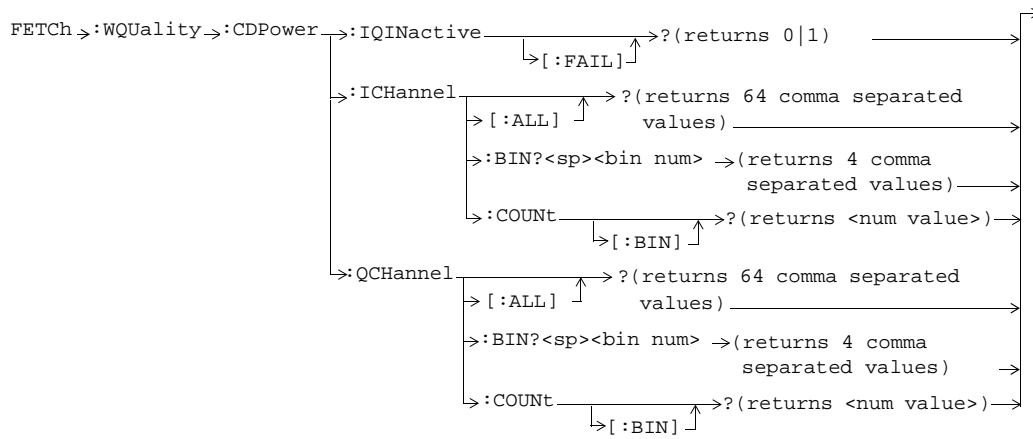
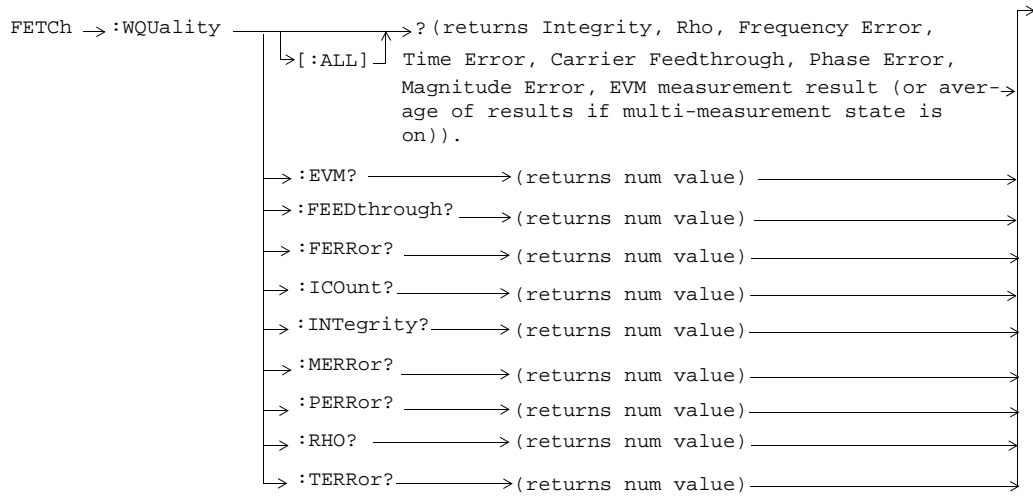


Diagram Conventions

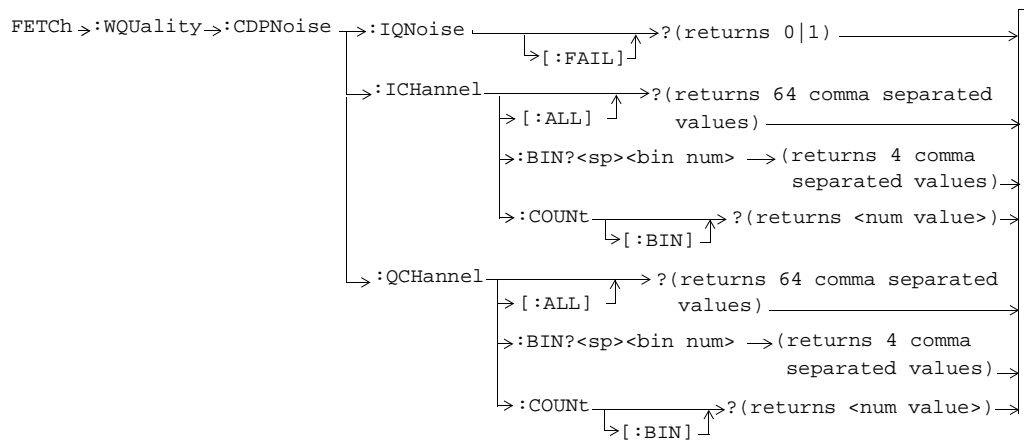


Diagram Conventions

INITiate

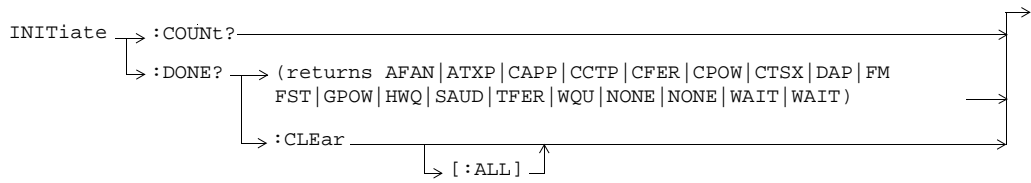
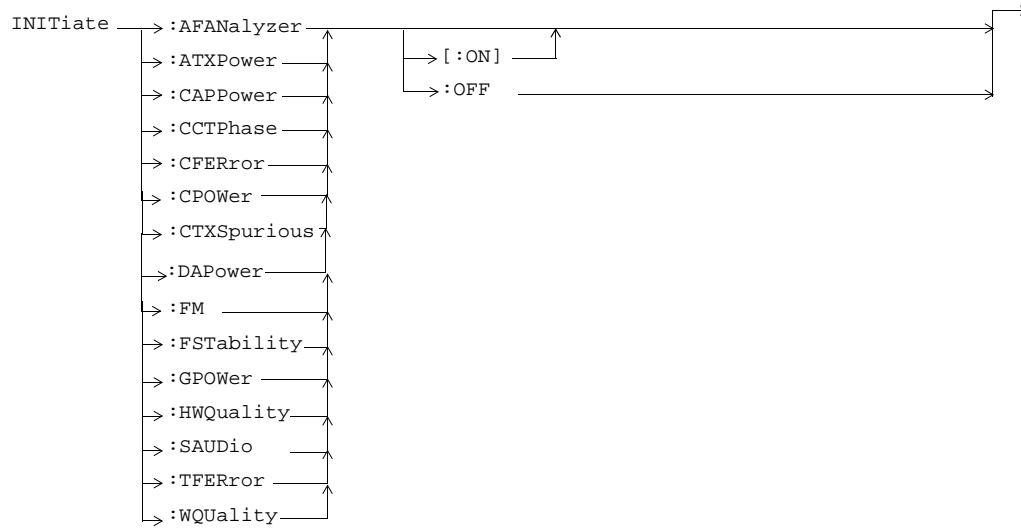
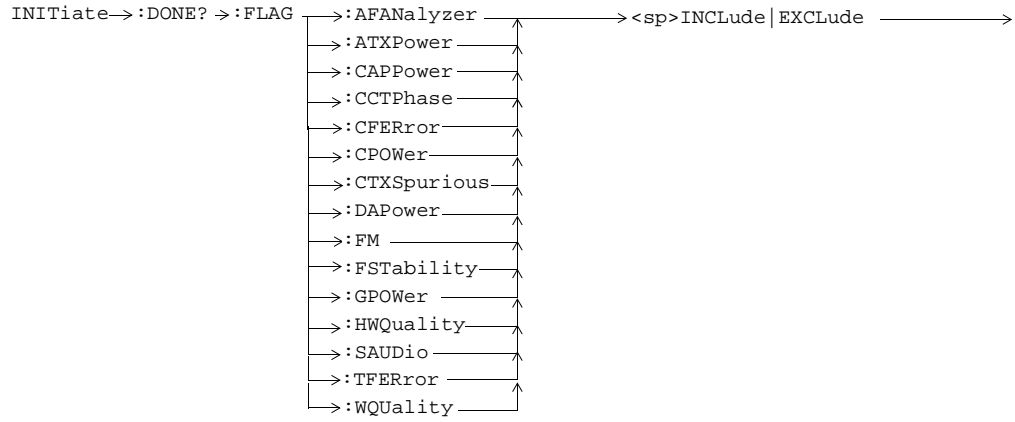


Diagram Conventions



`INITiate`:ON? → (returns comma-separated list of AFAN|ATXP|CAPP|CCTP|CFER|CPOW|CTSX|DAP|FM|FST|GPOW|HWQ|SAUD|TFER|WQU|NONE)



Diagram Conventions

READ

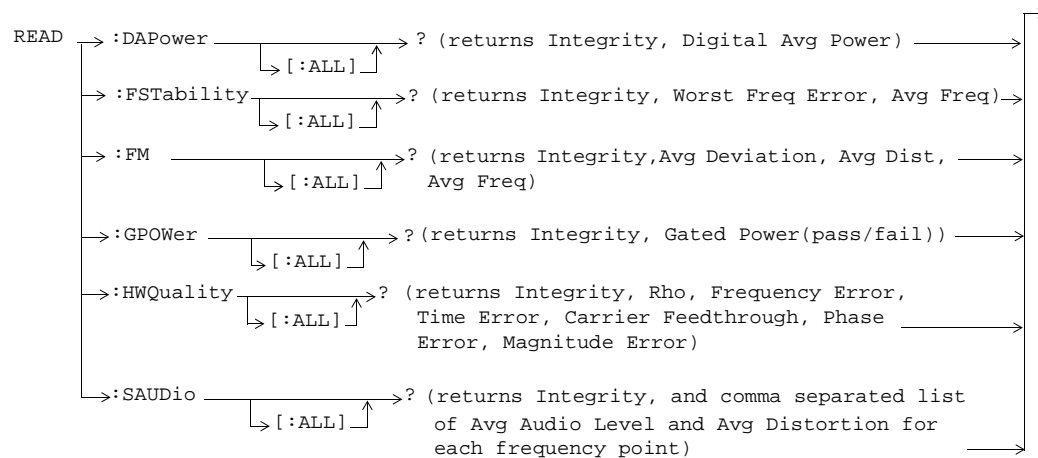
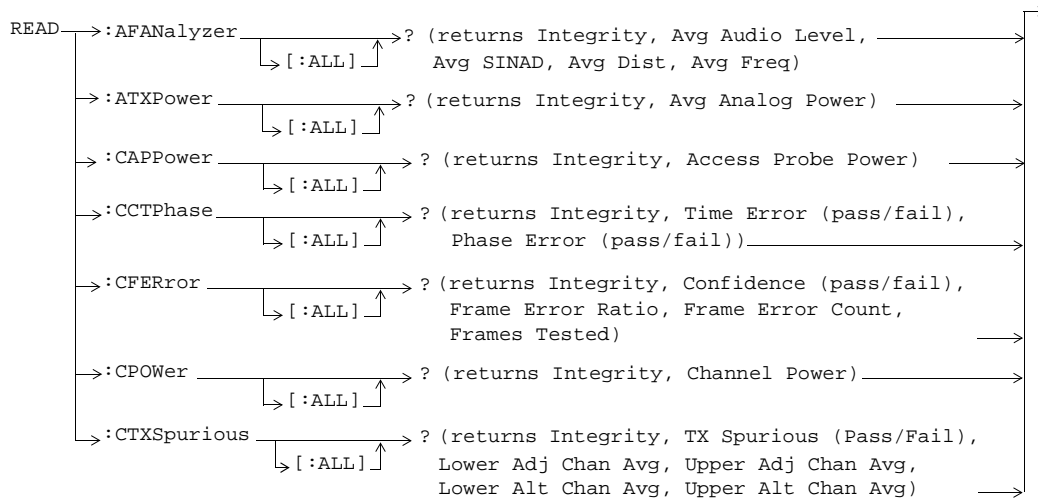


Diagram Conventions

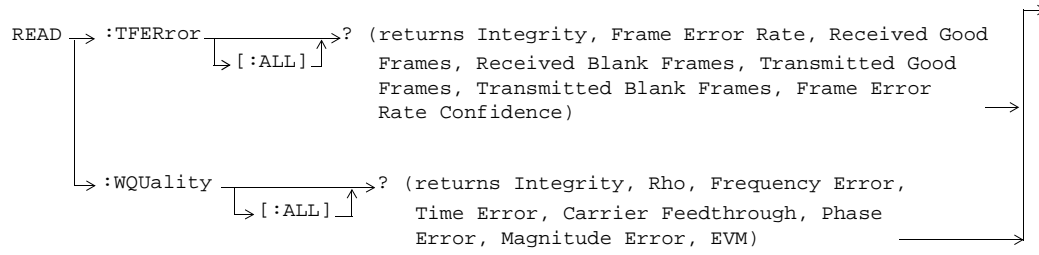
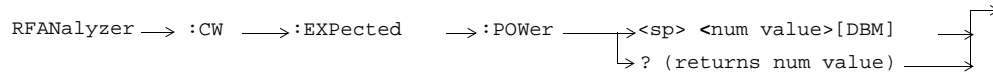
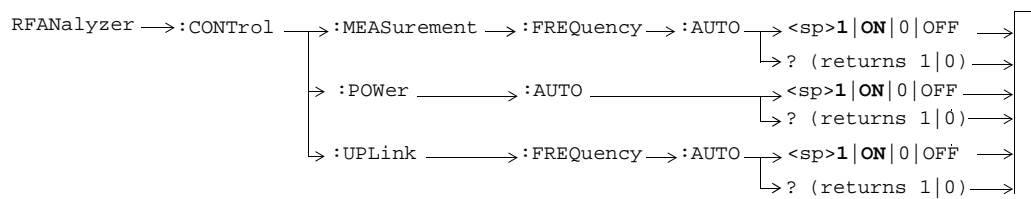
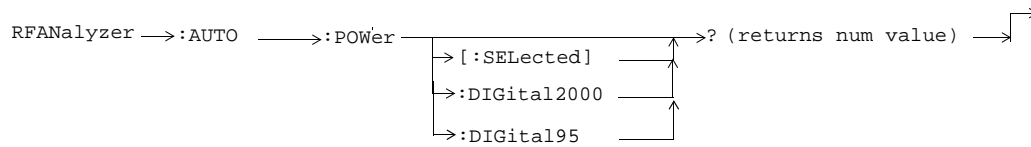
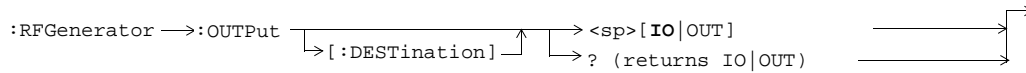


Diagram Conventions

RFAnalyzer



RFGenerator:OUTPut



SETup:AFANalyzer

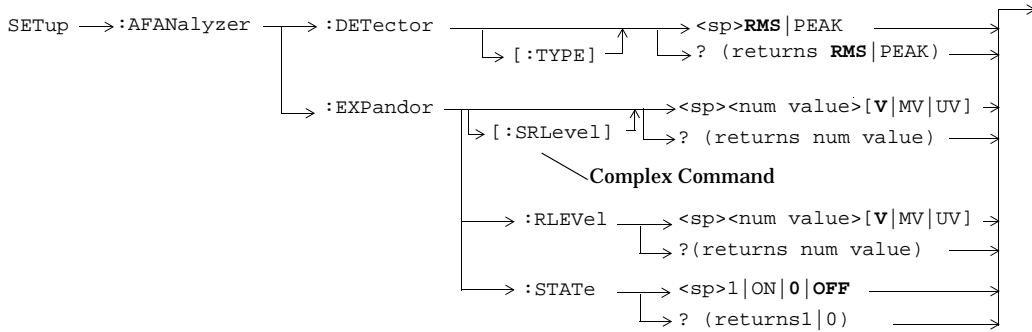
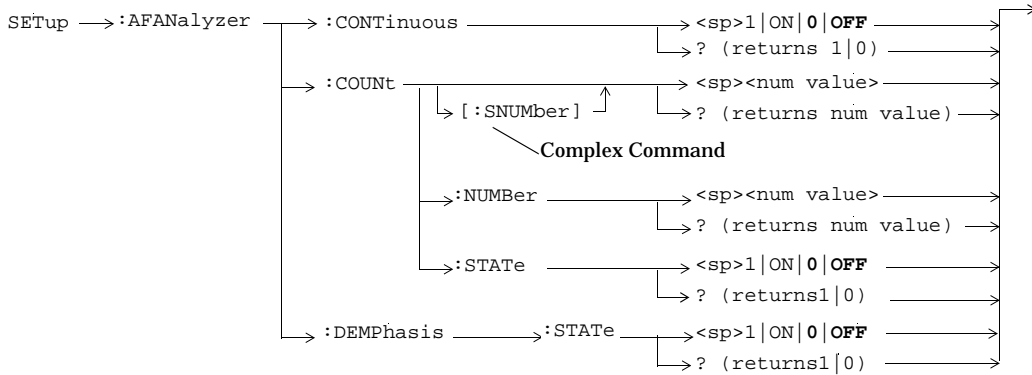
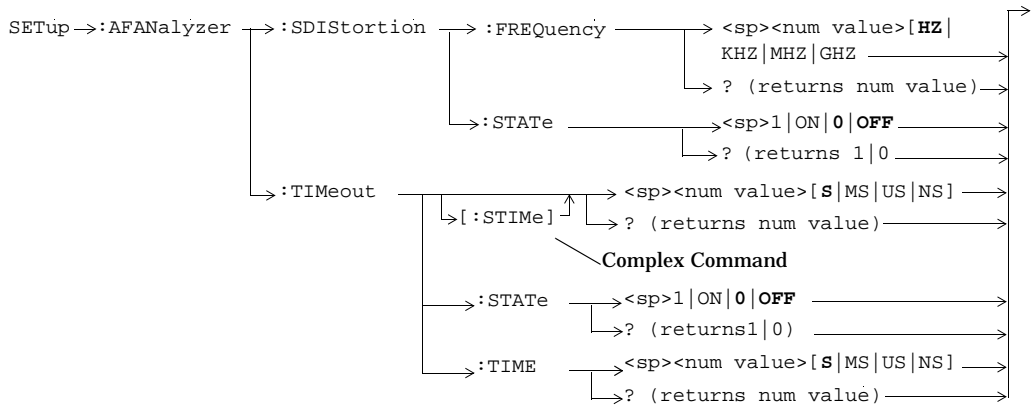
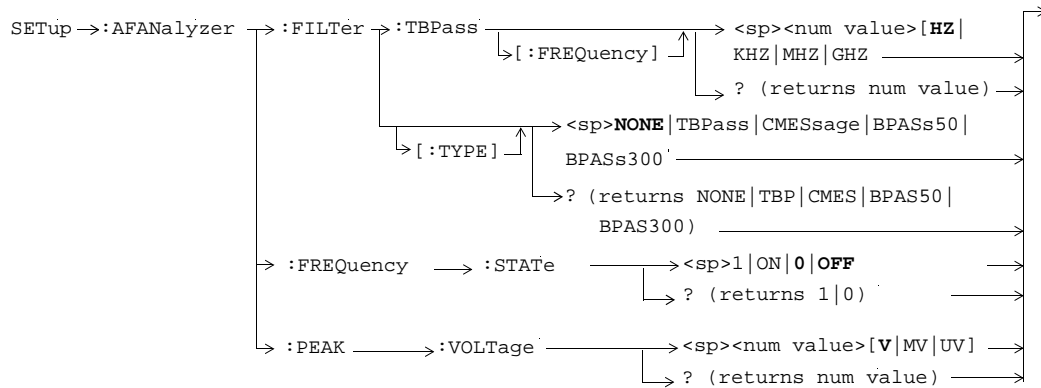
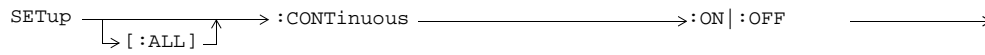


Diagram Conventions



SETup[:ALL]:CONTinuous



SETup:ATXPower

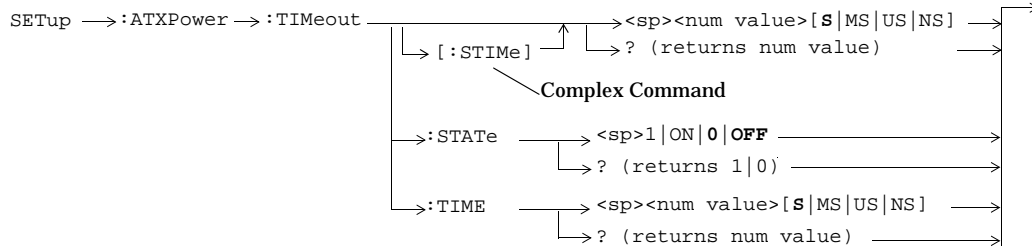
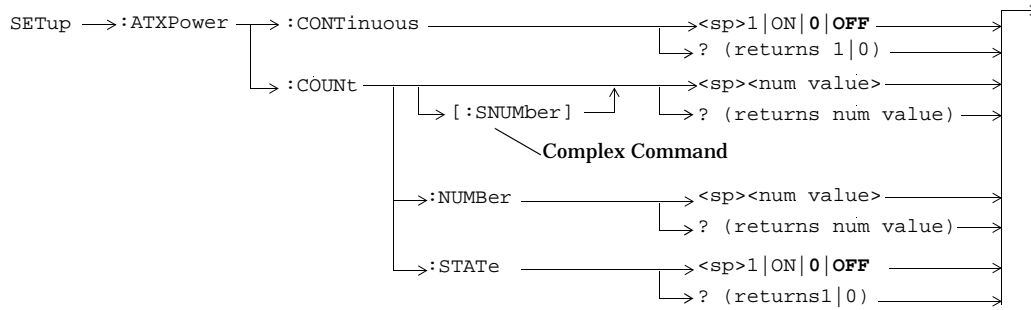


Diagram Conventions

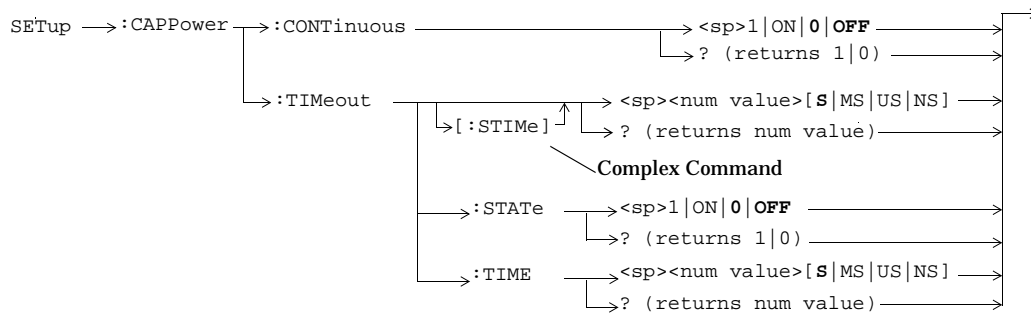
SETup:CAPower

NOTE The following three parameters directly affect access probe power measurements:

- Power Step
- Number of Steps
- Maximum Request Sequence

GPIB commands for these three parameters can be found in the “CALL[:CELL]:APARAmeter” syntax diagrams.

To limit a call attempt to the access attempt state, see “CALL:CONNected[:STATe]”



SETup:CCTPhase

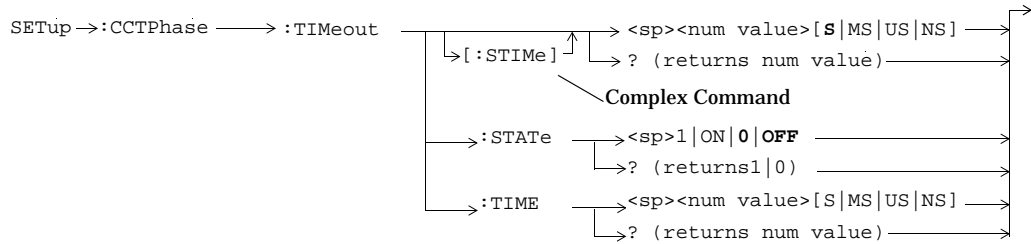
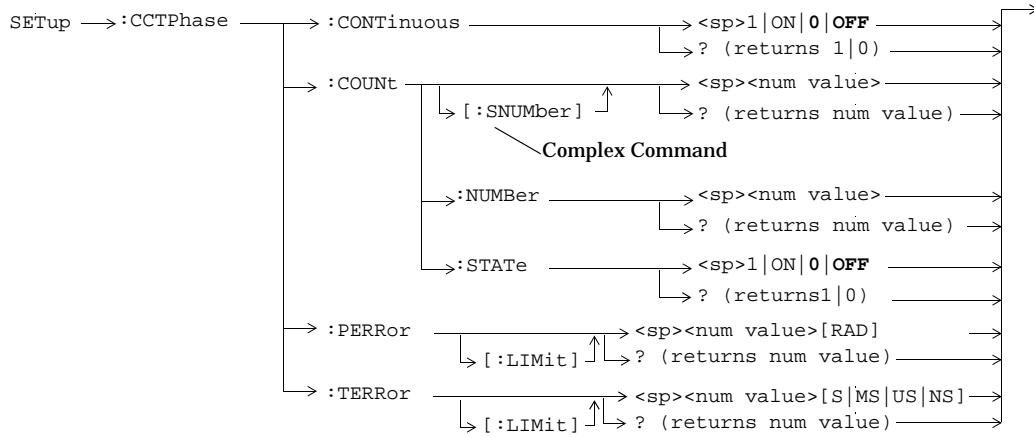
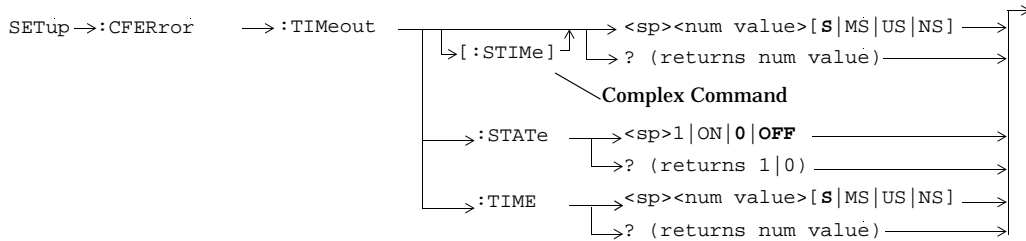
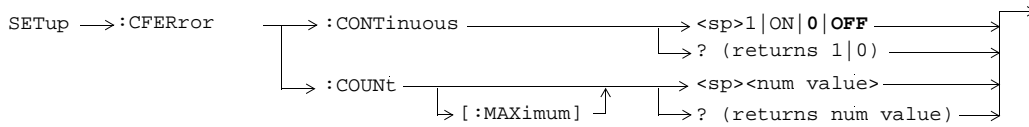
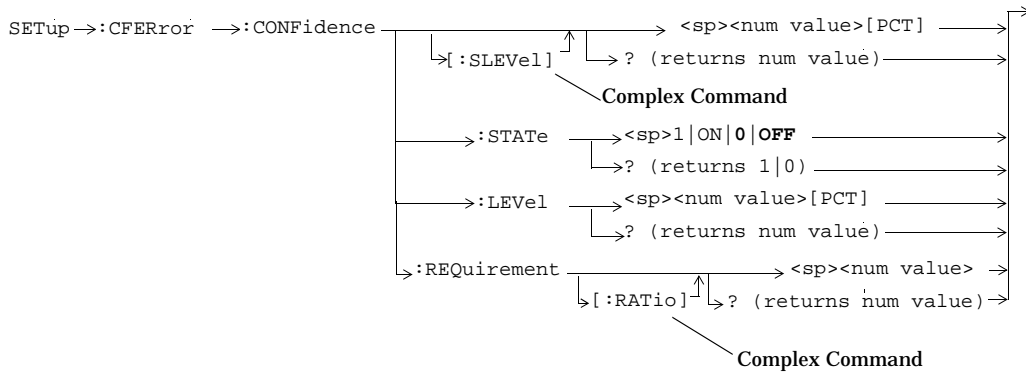


Diagram Conventions

SETup:CFERror



SETup:CPOWer

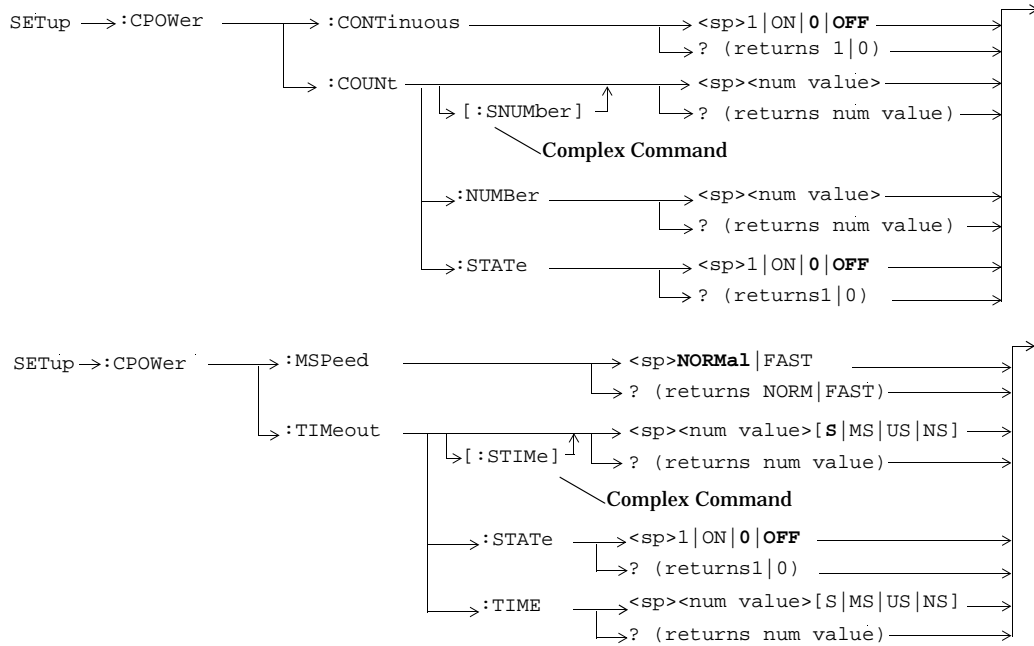
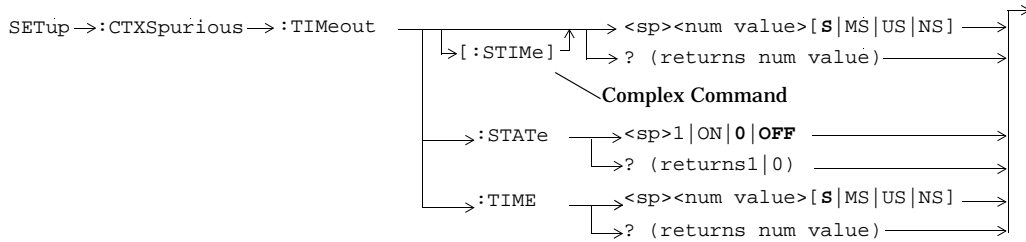
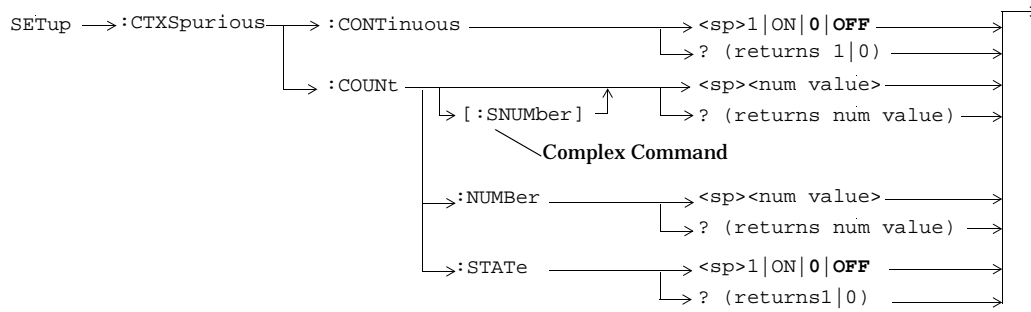


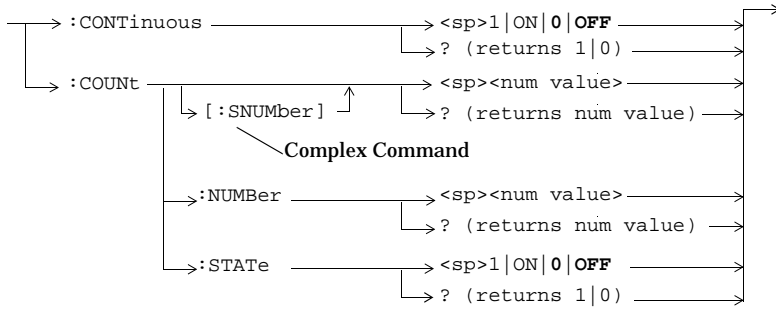
Diagram Conventions

SETup:CTXSpurious



SETup:DAPower

SETup → :DAPower



SETup → :DAPower

→ :TIMEout

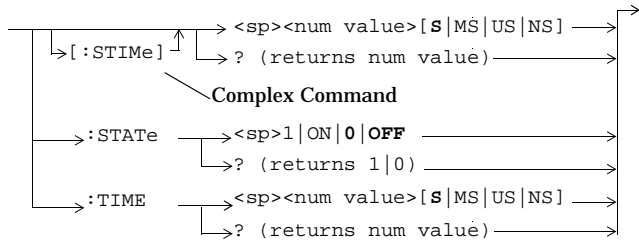


Diagram Conventions

SETup:FM

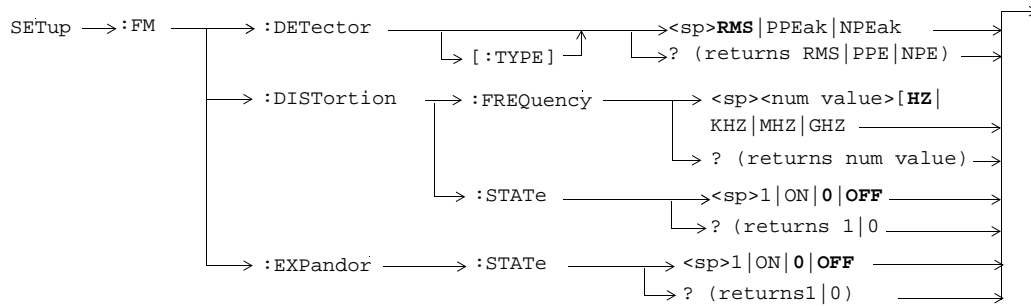
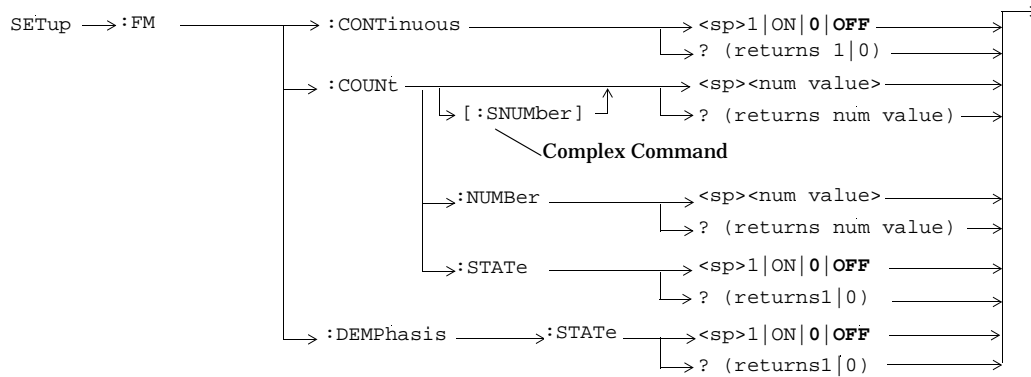


Diagram Conventions

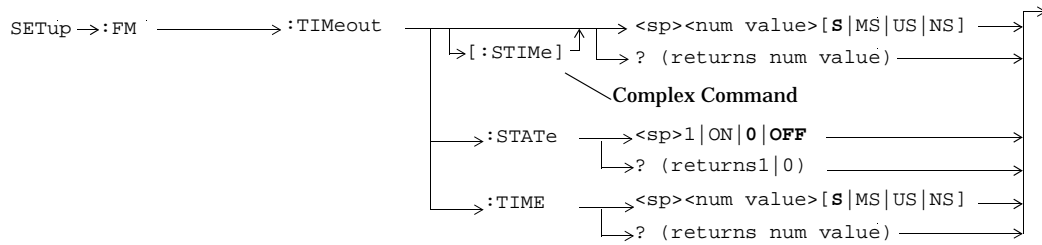
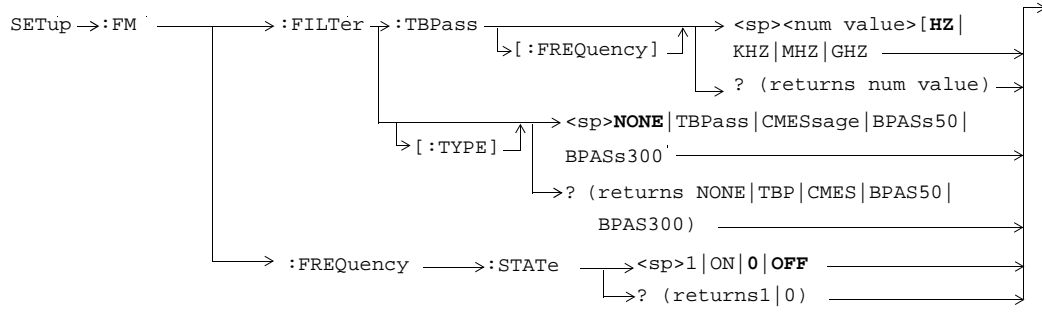
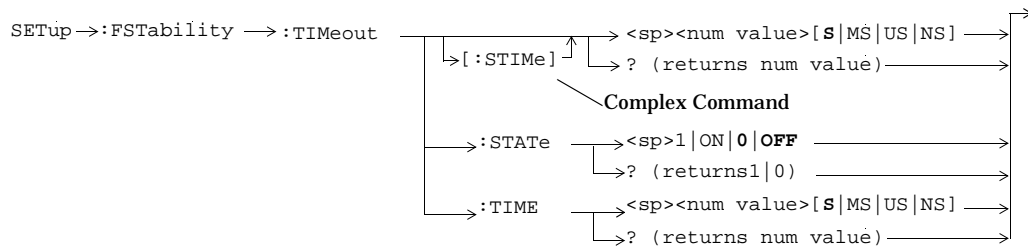
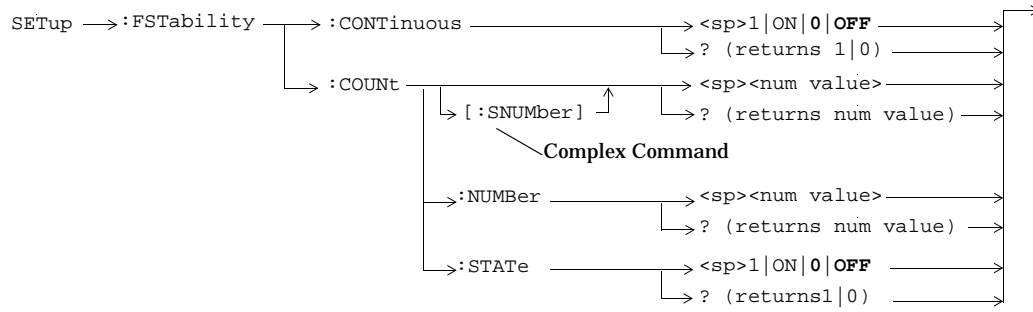
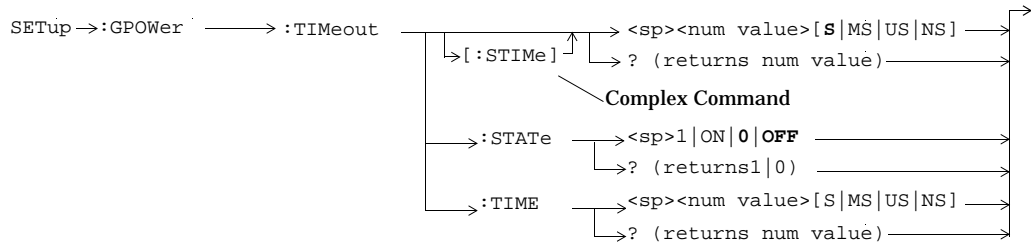
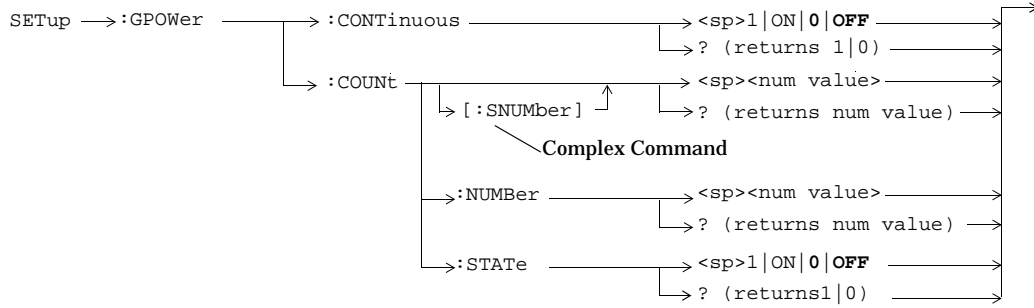


Diagram Conventions

SETup:FSTability



SETup:GPOWer



SETup:HWQuality

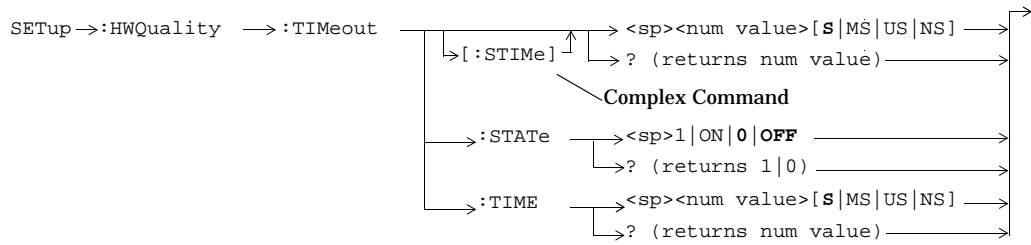


Diagram Conventions

SETup:SAudio

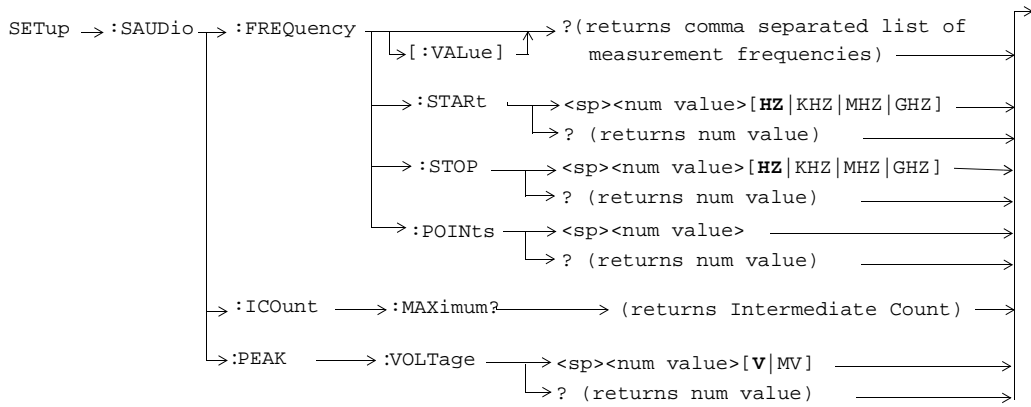
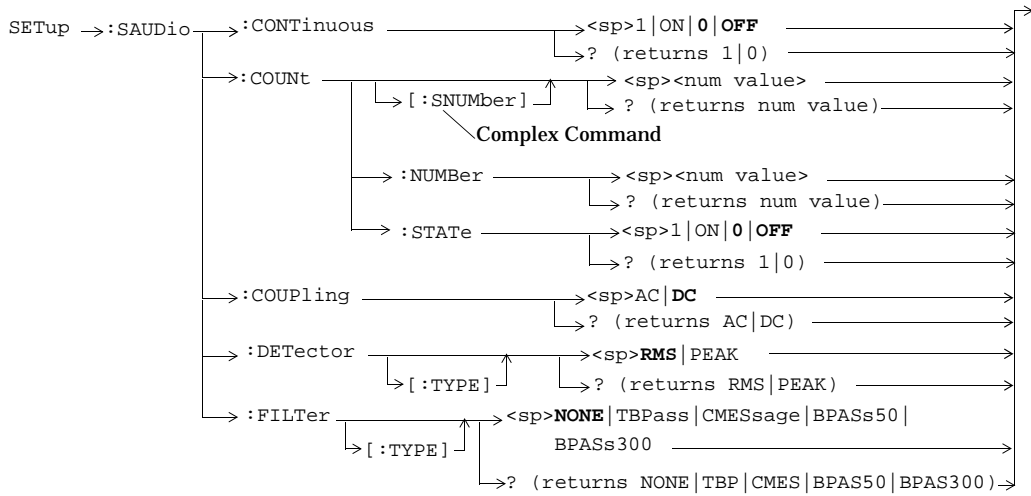


Diagram Conventions

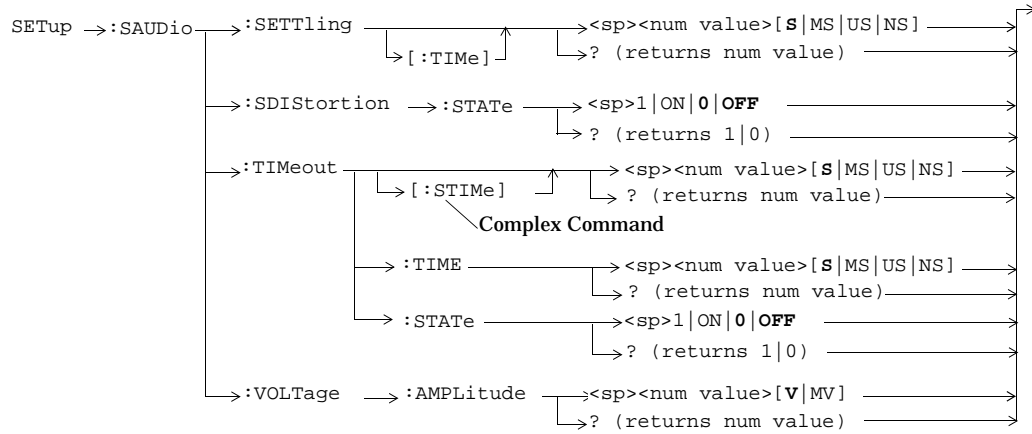
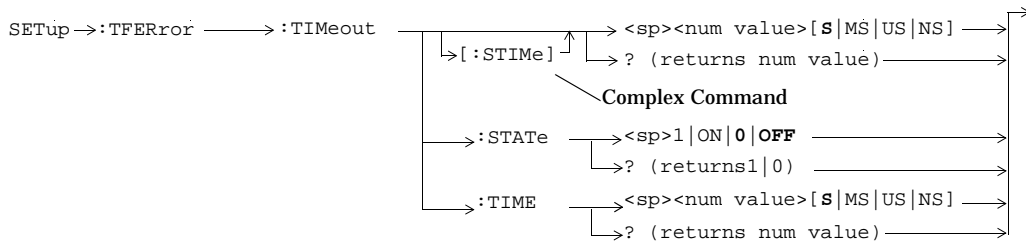
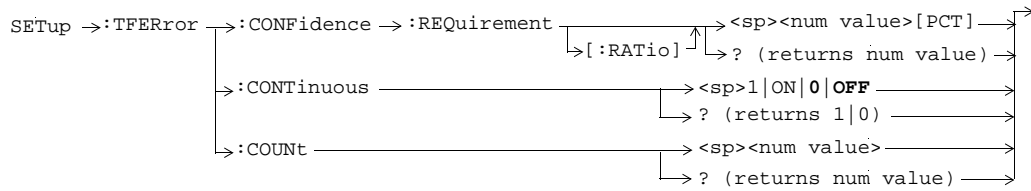


Diagram Conventions

SETup:TFERror



SETup:WQQuality

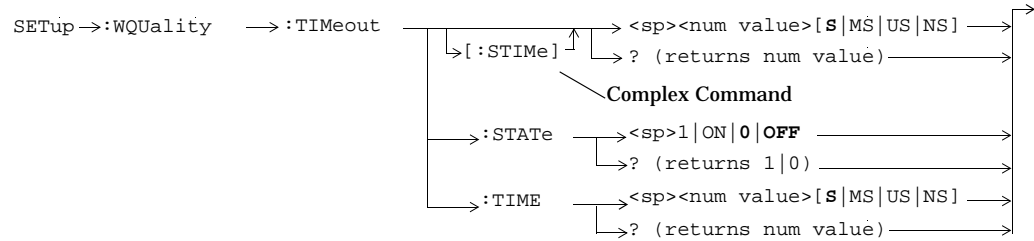
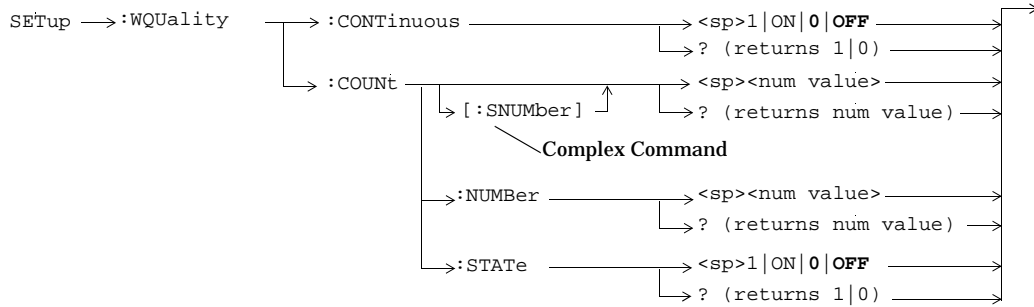
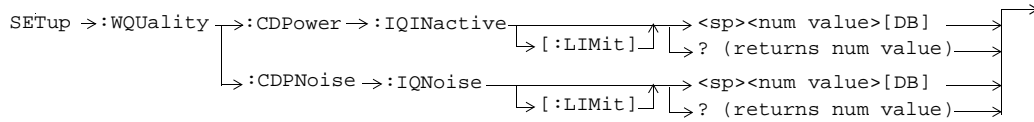


Diagram Conventions

STATUS:OPERation:

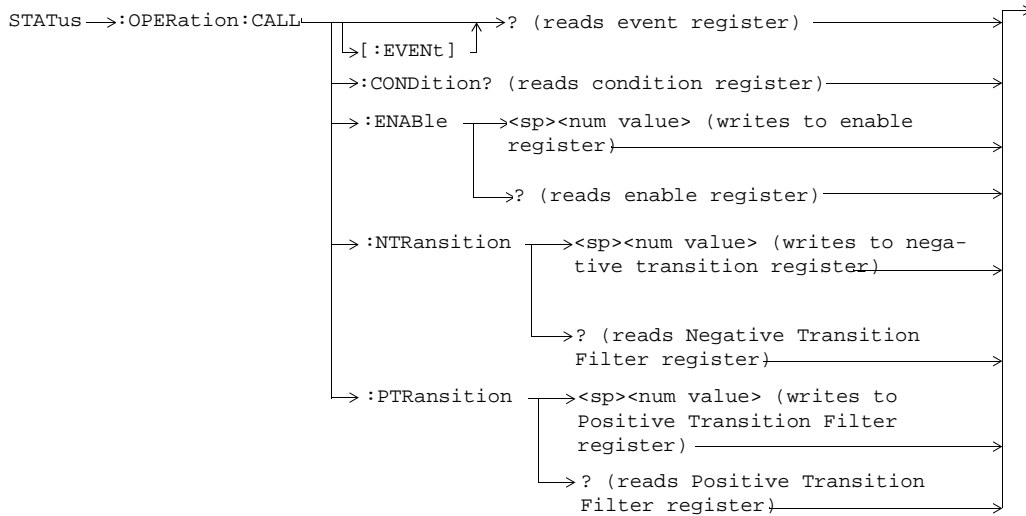
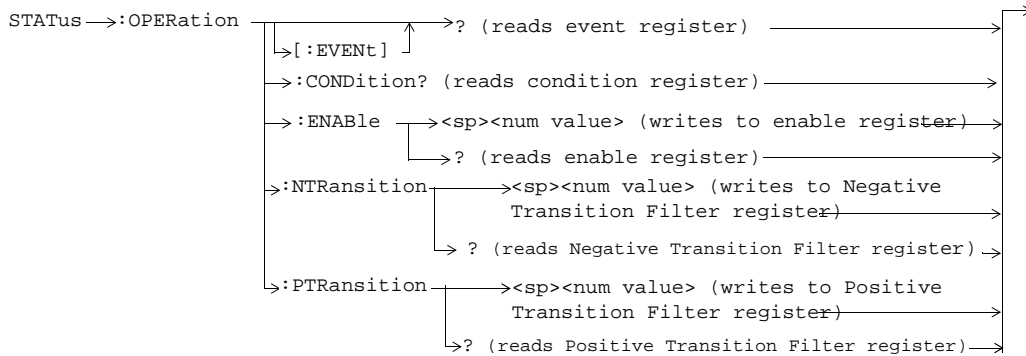


Diagram Conventions

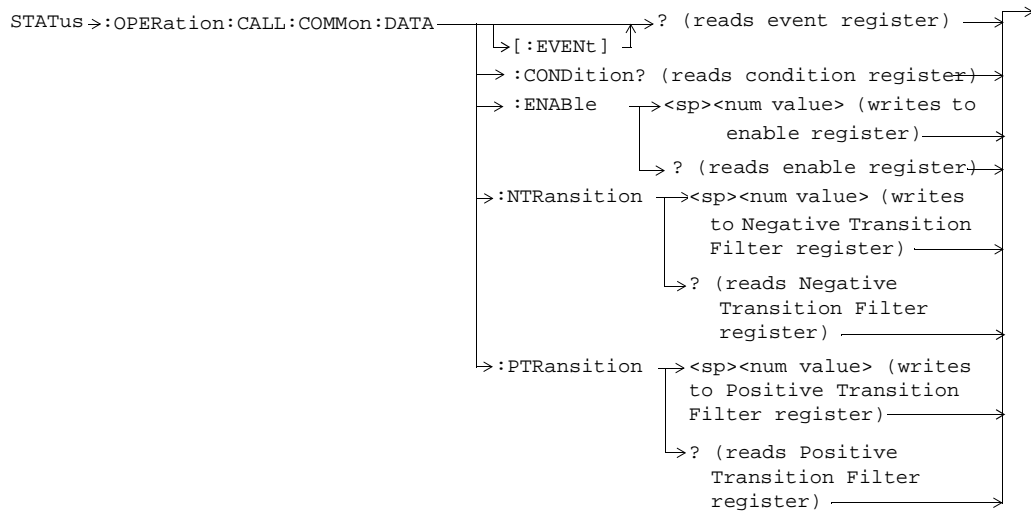
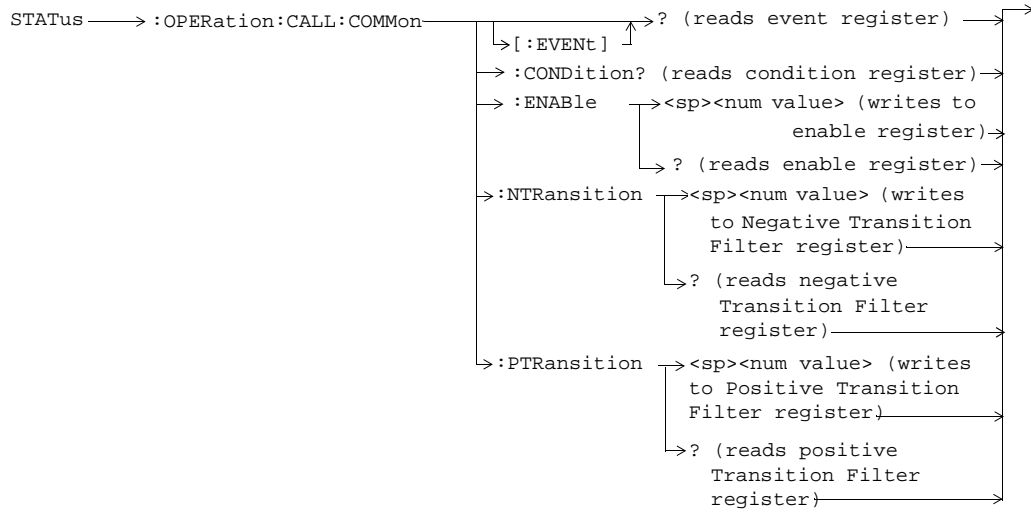


Diagram Conventions

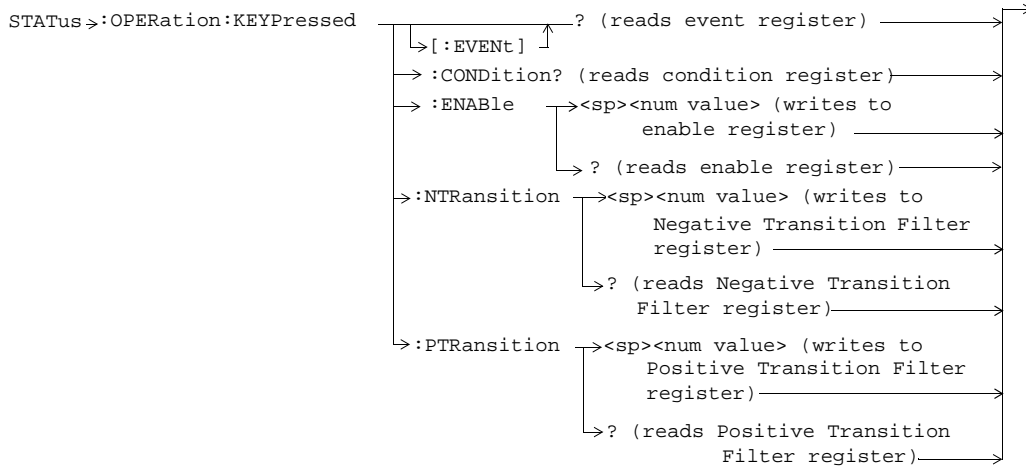
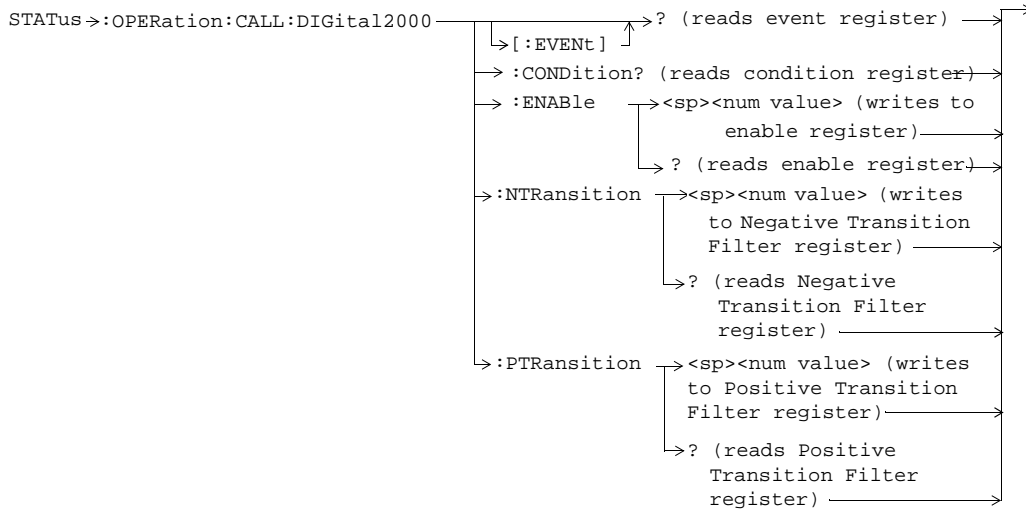


Diagram Conventions

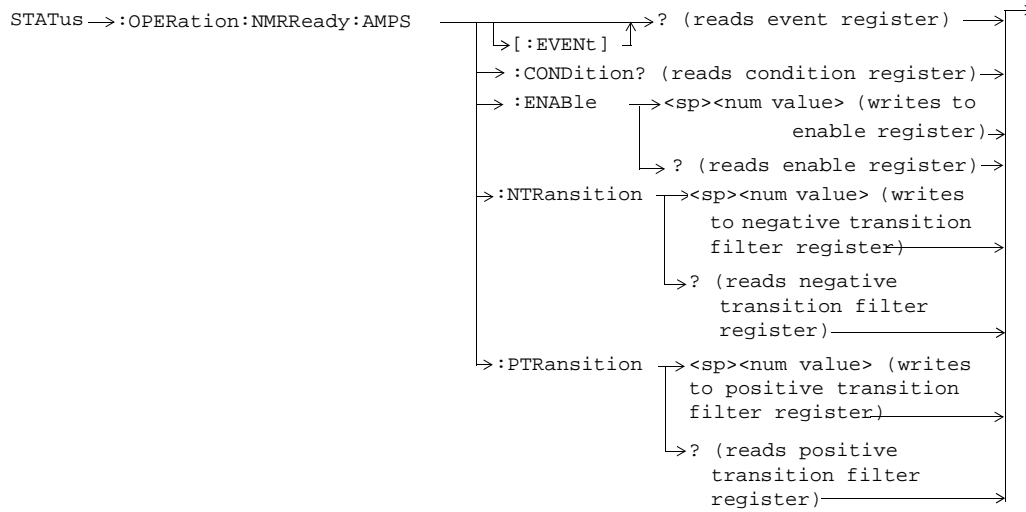
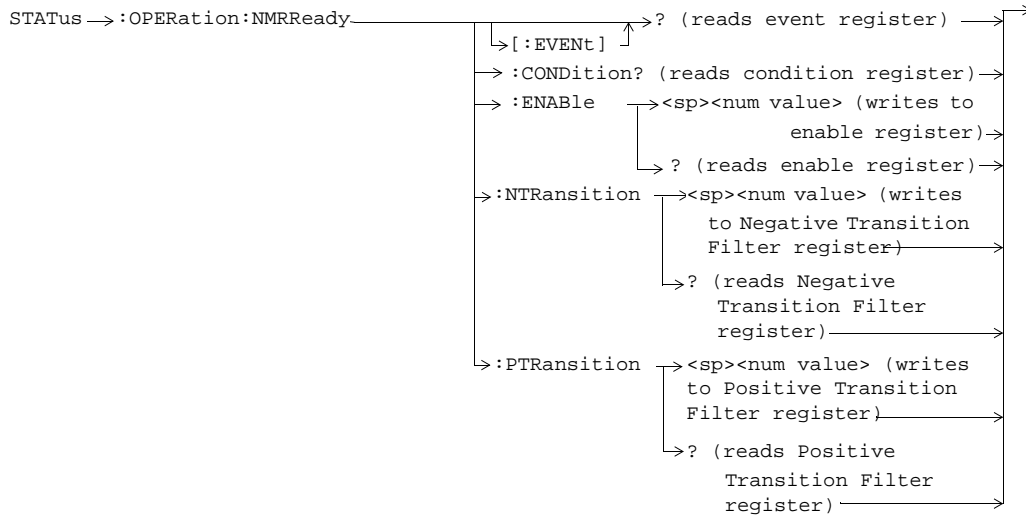


Diagram Conventions

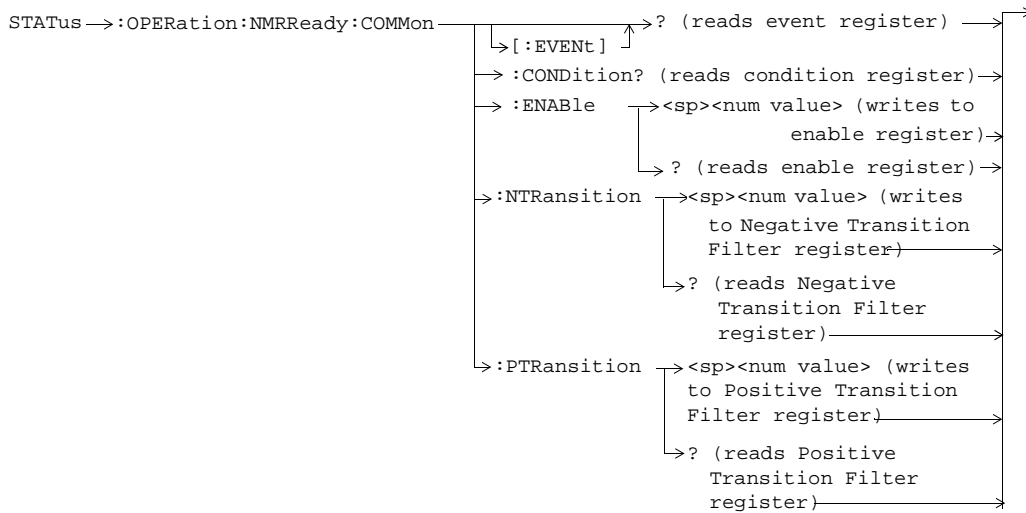
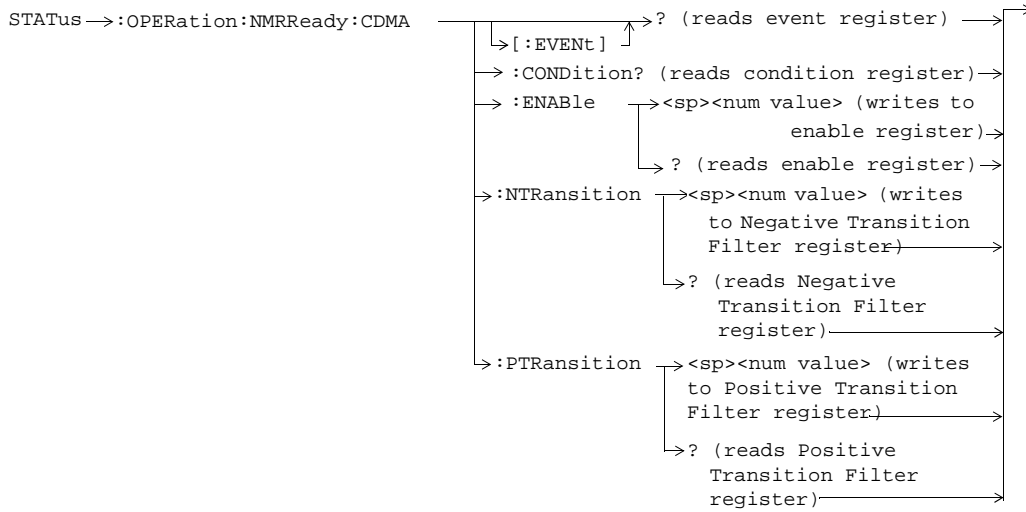


Diagram Conventions

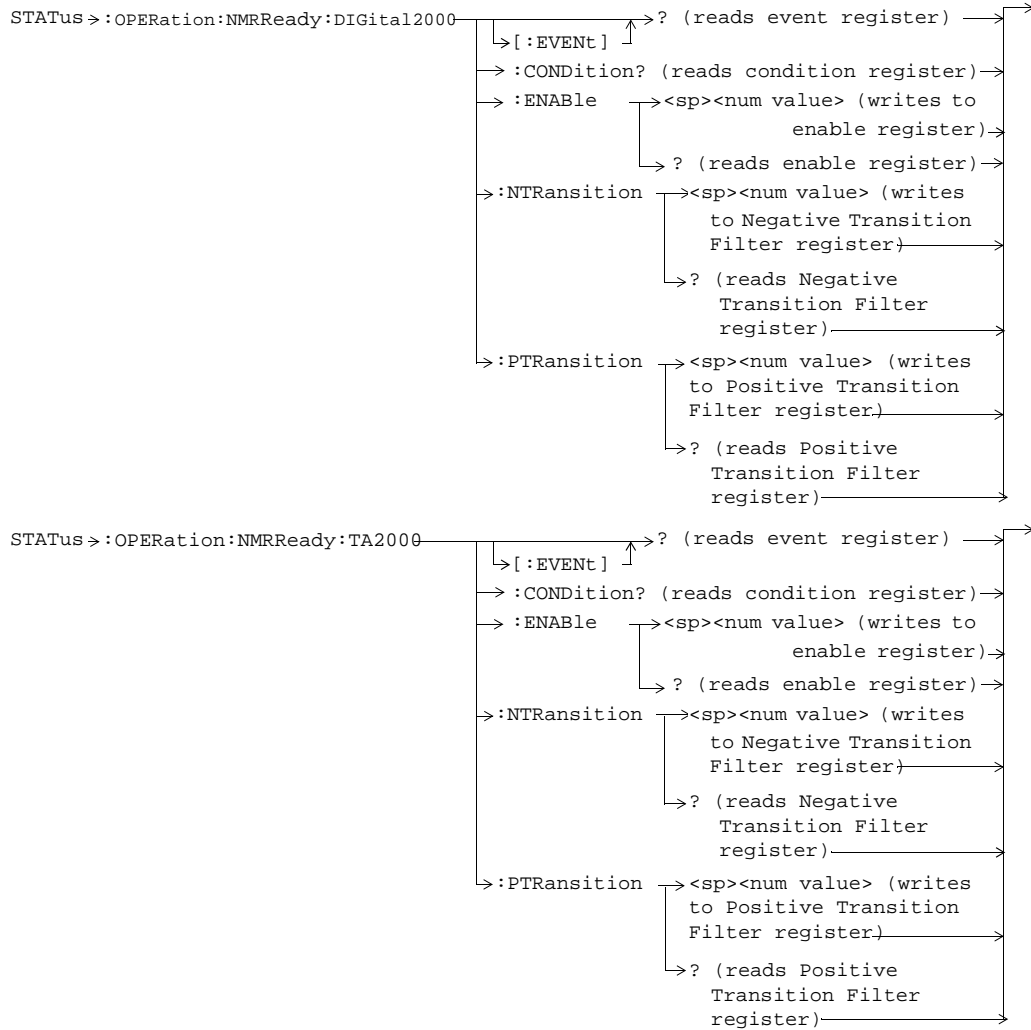
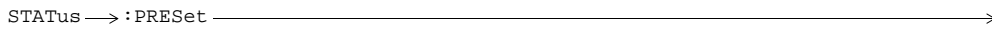


Diagram Conventions

STATUS:PRESet



STATUS:QUEStionable

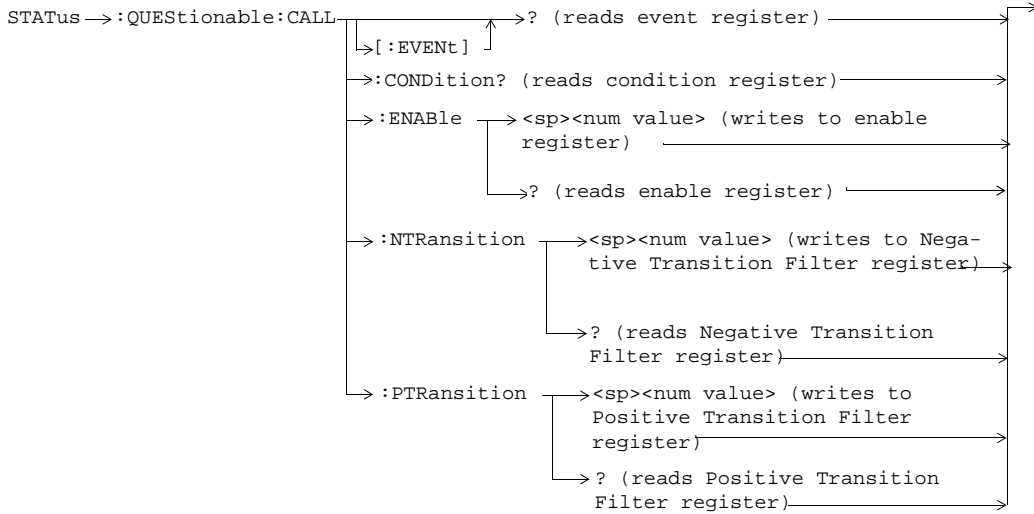
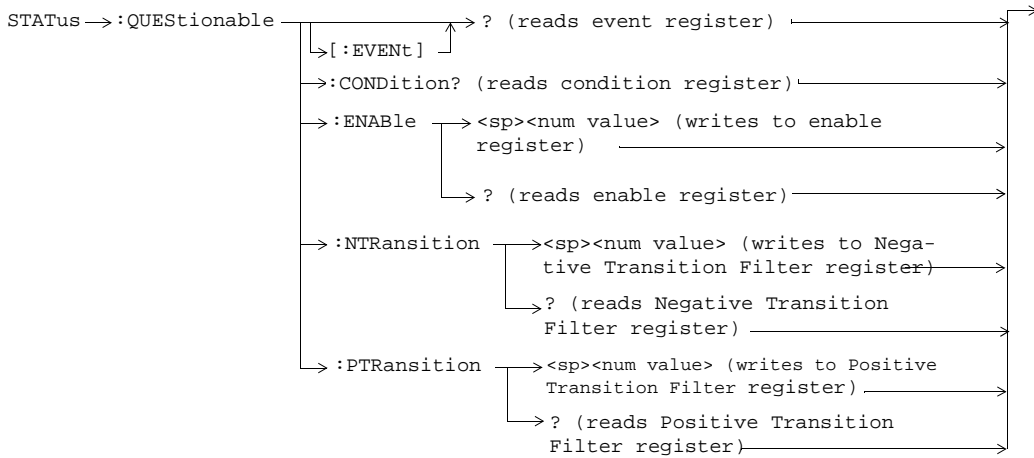


Diagram Conventions

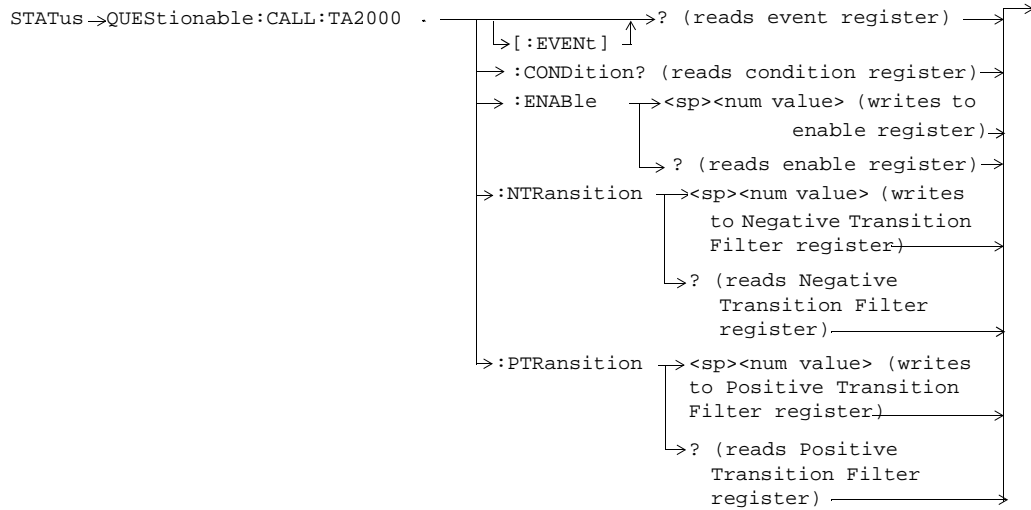
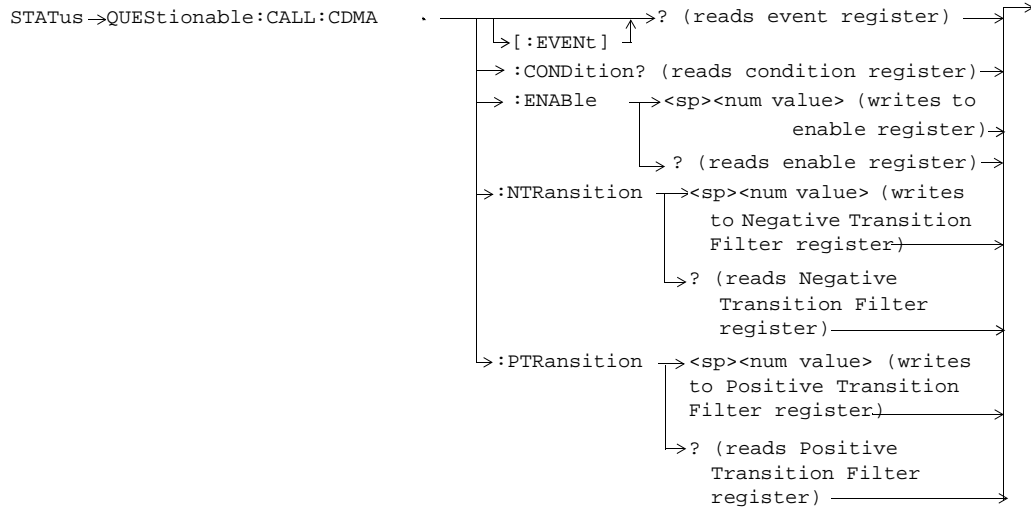


Diagram Conventions

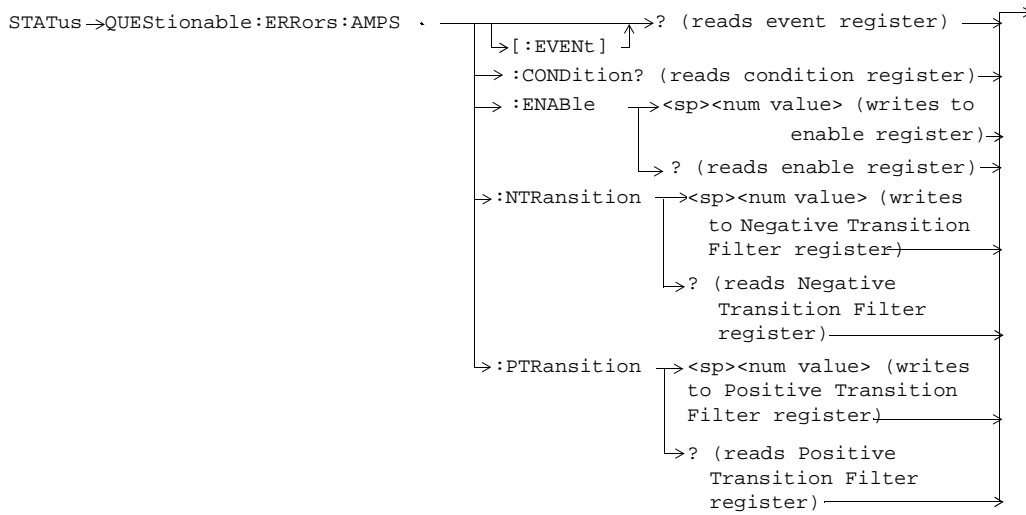
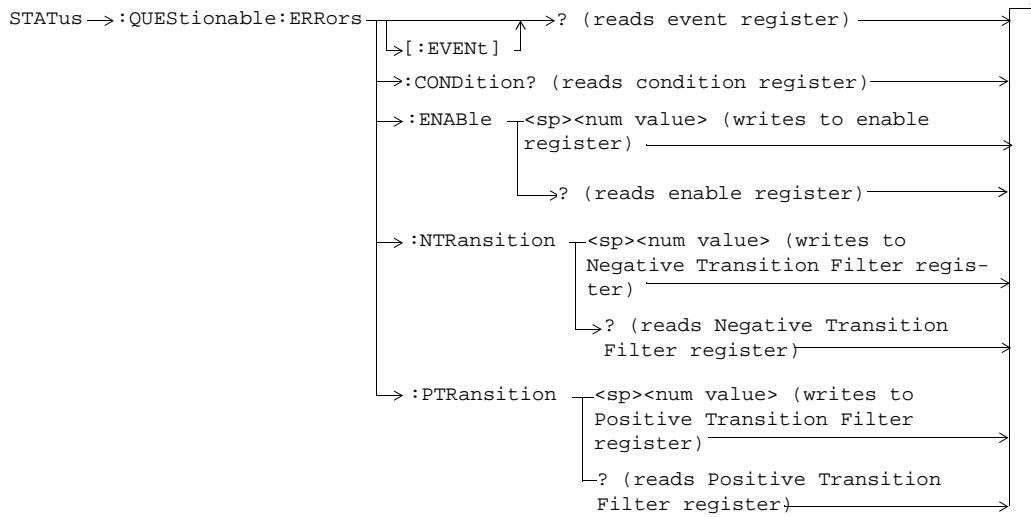


Diagram Conventions

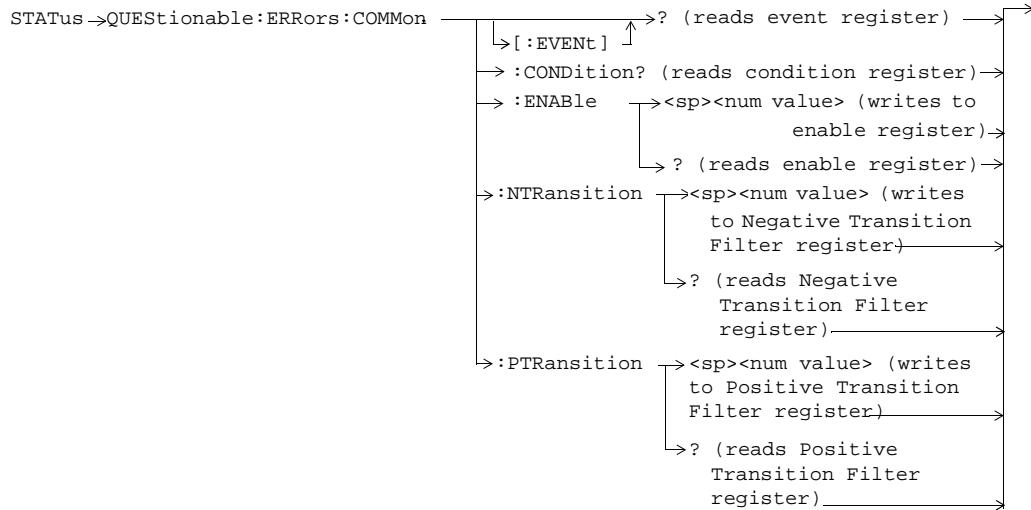
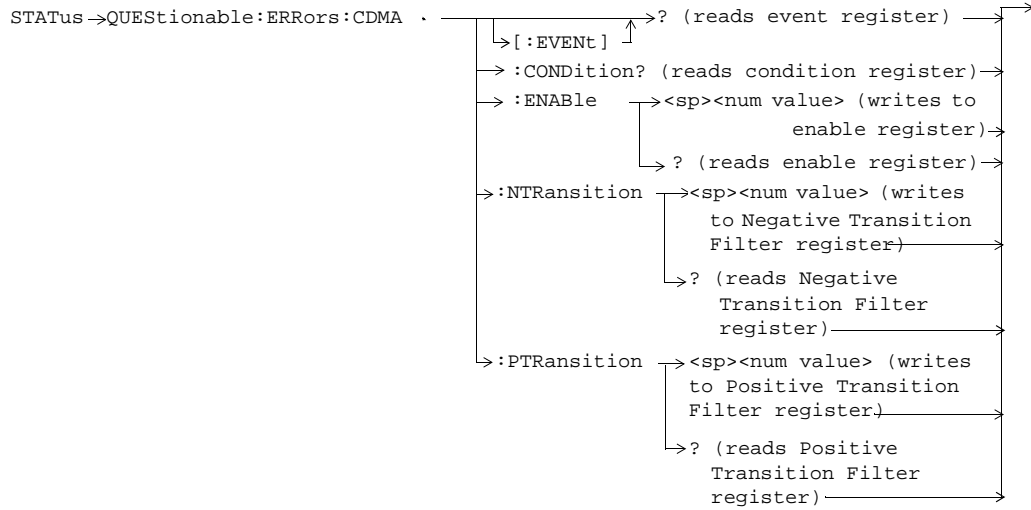


Diagram Conventions

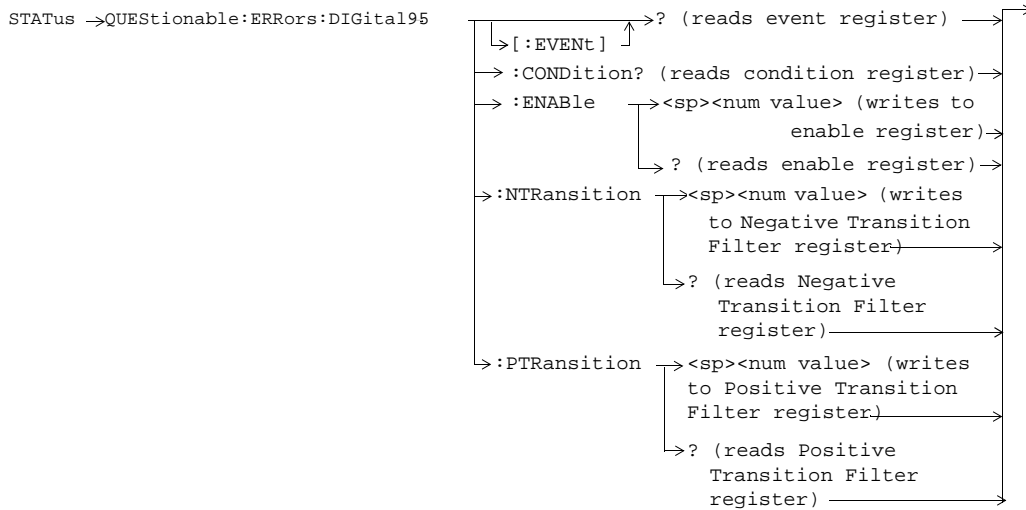
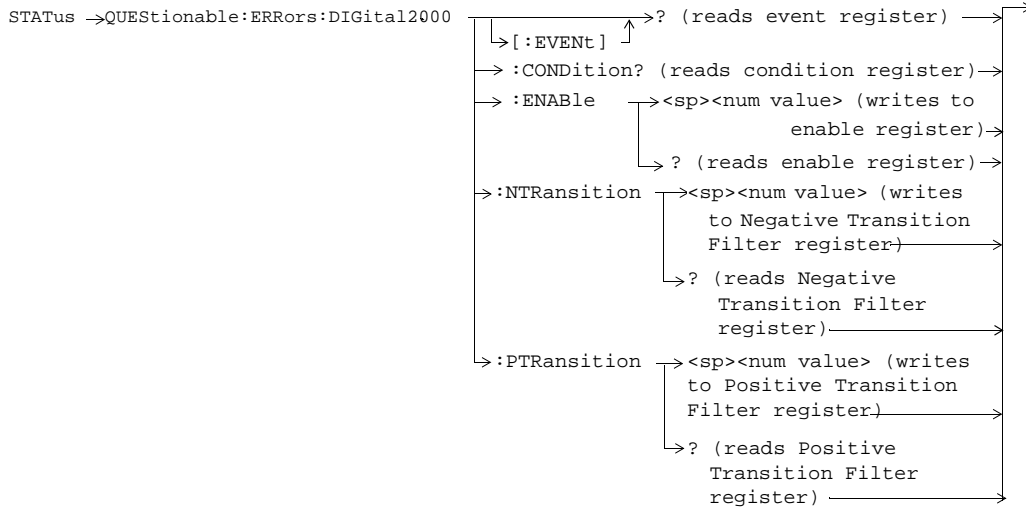
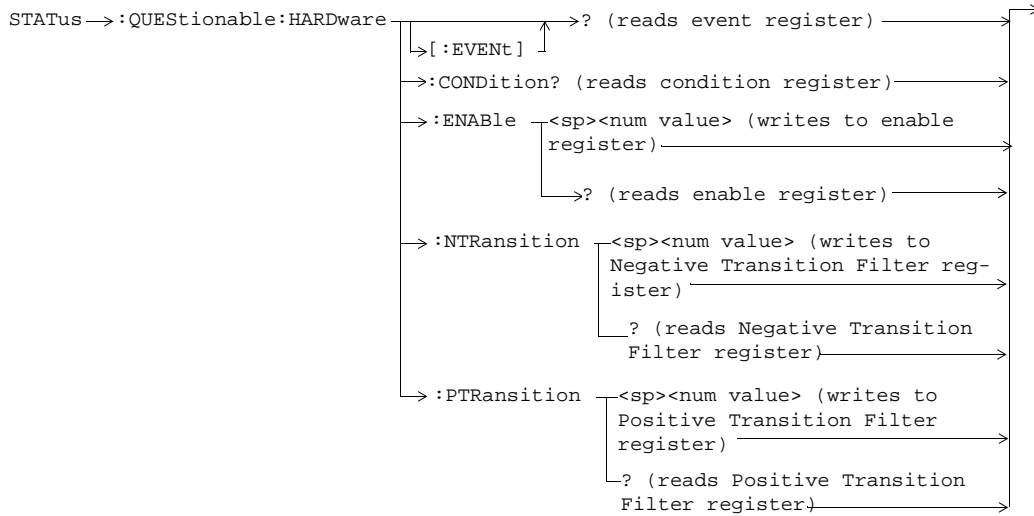
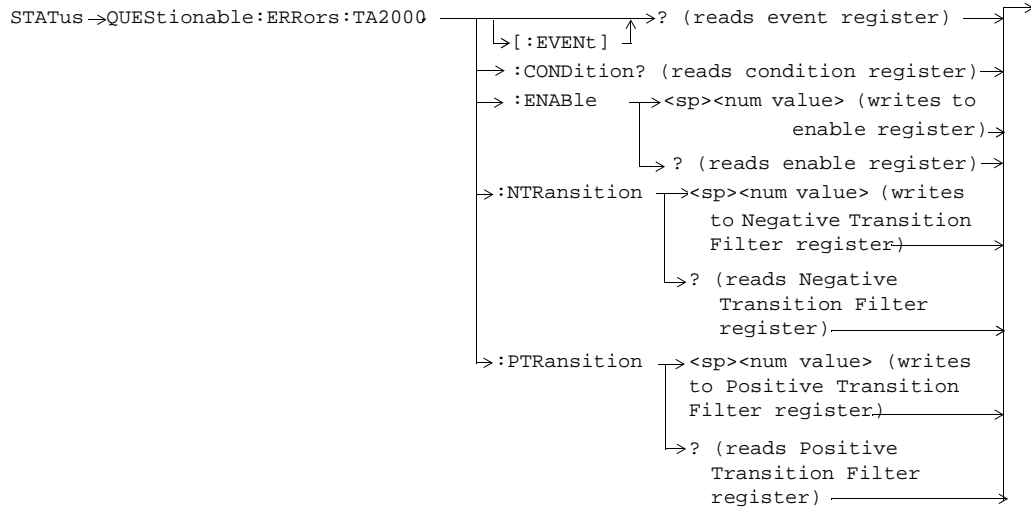


Diagram Conventions



“Diagram Conventions” on page 7

Diagram Conventions

STATUS:QUESTIONABLE Condition Register Bit Assignment

The STATUS:QUESTIONABLE register contains bits which give an indication that the data currently being acquired or generated is of questionable quality due to some condition affecting the functionality associated with that bit.

Bit Number	Binary Weighting	Condition	Description
15	32768	Not Used. Defined by SCPI.	This bit is always 0.
14	16384	Reserved for future use.	This bit is always 0
13	8192	Reserved for future use.	This bit is always 0.
12	4096	Reserved for future use.	This bit is always 0.
11	2048	QUESTIONABLE:HARDWARE summary	This bit is the summary message bit for the STATUS:QUESTIONABLE:HARDWARE register.
10	1024	QUESTIONABLE:CALL summary	This bit is the summary message bit for the STATUS:QUESTIONABLE:CALL register.
9	512	Reserved for future use.	This bit is always 0.
8	256	Reserved for future use.	This bit is always 0.
7	128	Reserved for future use.	This bit is always 0.
6	64	Reserved for future use.	This bit is always 0.
5	32	Reserved for future use.	This bit is always 0.
4	16	Reserved for future use.	This bit is always 0.
3	8	Reserved for future use.	This bit is always 0.
2	4	Reserved for future use.	This bit is always 0.
1	2	QUESTIONABLE:ERRORS summary	This bit is the summary message bit for the STATUS:QUESTIONABLE:ERRORS register.
0	1	Reserved for future use.	This bit is always 0.

Program Example - STATUS:QUESTIONABLE Register Bit Assignments

```

OUTPUT 714;"STATUS:QUESTIONABLE:EVENT?" !Queries and clears the Questionable Event
!Register
OUTPUT 714;"STATUS:QUESTIONABLE:CONDITION?" !Queries and clears the Questionable Condition
!Register
OUTPUT 714;"STATUS:QUESTIONABLE:ENABLE 1024" !Sets the Questionable Enable Register
!for bit 10
OUTPUT 714;"STATUS:QUESTIONABLE:NTRANSITION 2" !Sets the Questionable Negative
!Transition Filter Register for bit 1
OUTPUT 714;"STATUS:QUESTIONABLE:PTRANSITION 2" !Sets the Questionable Positive
!Transition Filter Register for bit 1

```

STATUS:QUESTIONABLE:CALL Condition Register Bit Assignment

The STATUS:QUESTIONABLE:CALL registers will contain information about which event(s) occurred during call processing that indicate what call processing procedure failed

Bit Number	Binary Weighting	Condition	Description
15	32768	Not Used. Defined by SCPI.	This bit is always 0.
14	16384	Reserved for future use.	This bit is always 0.
13	8192	Reserved for future use.	This bit is always 0.
12	4096	Reserved for future use.	This bit is always 0.
11	2048	Reserved for future use.	This bit is always 0.
10	1024	Reserved for future use.	This bit is always 0.
9	512	TA2000 Summary bit	This bit is the summary message bit for the STATUS:QUESTIONABLE:CALL:TA2000 register.
8	256	CDMA Summary bit	This bit is the summary message bit for the STATUS:QUESTIONABLE:CALL:CDMA register.
7	128	DIGital2000 Summary bit	This bit is the summary message bit for the STATUS:QUESTIONABLE:ERRORS:CALL:DIGital2000 register.
6	64	DIGital95 Summary bit	This bit is the summary message bit for the STATUS:QUESTIONABLE:ERRORS:CALL:DIGital95 register.
5	32	TA136 Summary bit	This bit is the summary message bit for the STATUS:QUESTIONABLE:CALL:TA136 register.
4	16	DIGital136 Summary bit	This bit is the summary message bit for the STATUS:QUESTIONABLE:CALL:DIGital136 register.
3	8	AMPS Summary bit	This bit is the summary message bit for the STATUS:QUESTIONABLE:CALL:AMPS register.
2	4	GSM Summary bit	This bit is the summary message bit for the STATUS:QUESTIONABLE:CALL:GSM register.
1	2	COMMOn Summary bit	This bit is the summary message bit for the STATUS:QUESTIONABLE:CALL:COMMOn register.
0	1	Extension Bit	This bit is always 0.

Diagram Conventions

Program Example - STATUS:QUESTIONABLE:CALL Condition Register Bit Assignment

```
OUTPUT 714;"STATUS:QUESTIONABLE:CALL:EVENT?" !Queries and clears the Questionable
!Call Event Register
OUTPUT 714;"STATUS:QUESTIONABLE:CALL:CONDITION?" !Queries and clears the Questionable
!Call Condition Register
OUTPUT 714;"STATUS:QUESTIONABLE:CALL:ENABLE 1024" !Sets the Questionable
!Call Enable Register for bit 10
OUTPUT 714;"STATUS:QUESTIONABLE:CALL:NTRANSITION 2" !Sets the Questionable Call
!Negative Transition Filter Register
!for bit 1
OUTPUT 714;"STATUS:QUESTIONABLE:CALL:PTRANSITION 2" !Sets the Questionable Call
!Positive Transition Filter Register
!for bit 1
```

STATUS:QUESTIONABLE:CALL:CDMA Condition Register Bit Assignment

The STATUS:QUESTIONABLE:CALL:CDMA registers will contain information about which event(s) occurred during call processing that indicate what call processing procedure failed

Bit Number	Binary Weighting	Condition	Description
15	32768	Not Used. Defined by SCPI.	This bit is always 0.
14	16384	MUI Maskable Message.	
13	8192	Reserved for future use.	This bit is always 0.
12	4096	Reserved for future use.	This bit is always 0.
11	2048	Reserved for future use.	This bit is always 0.
10	1024	Reserved for future use.	This bit is always 0.
9	512	Reserved for future use.	This bit is always 0.
8	256	Reserved for future use.	This bit is always 0.
7	128	Reserved for future use.	This bit is always 0.
6	64	Reserved for future use.	This bit is always 0.
5	32	Reserved for future use.	This bit is always 0.
4	16	Call drop timer timed out.	This bit is a 1 after 250 consecutive bad frames are counted indicating that the reverse link is lost
3	8	Service connect completion not received.	This bit is a 1 when the test set has not received a message from the MS that it has completed the change to a new Service Option or Radio Configuration.
2	4	Service Option or Radio Configuration rejected by MS.	This bit is a 1 when the MS receives a Service Option or a Radio Configuration that it does not support.
1	2	Traffic channel preamble not received.	This bit is a 1 if no preamble was received from the MS.
0	1	Extension Bit.	This bit is always 0.

Program Example - STATUS:QUESTIONABLE:CALL:CDMA Register Bit Assignment

```

OUTPUT 714;"STATUS:QUESTIONABLE:CALL:CDMA:EVENT?"
!Queries and clears the Questionable Call CDMA Event Register
OUTPUT 714;"STATUS:QUESTIONABLE:CALL:CDMA:CONDITION?"
!Queries and clears the Questionable Call CDMA Condition Register
OUTPUT 714;"STATUS:QUESTIONABLE:CALL:CDMA:ENABLE 1024"
!Sets the Questionable Call CDMA Enable Register for bit 10
OUTPUT 714;"STATUS:QUESTIONABLE:CALL:CDMA:NTRANSITION 2"
!Sets the Questionable Call CDMA Negative Transition Filter Register for bit 1
OUTPUT 714;"STATUS:QUESTIONABLE:CALL:CDMA:PTRANSITION 2"
!Sets the Questionable Call CDMA Positive Transition Filter Register for bit 1

```

Diagram Conventions

STATUS:QUESTIONABLE:CALL:TA2000 Condition Register Bit Assignment

The STATUS:QUESTIONABLE:CALL:TA2000 registers will contain information about which event(s) occurred during call processing that indicate what call processing procedure failed

Bit Number	Binary Weighting	Condition	Description
15	32768	Not Used. Defined by SCPI.	This bit is always 0.
14	16384	MUI Maskable Message.	
13	8192	Reserved for future use.	This bit is always 0.
12	4096	Reserved for future use.	This bit is always 0.
11	2048	Reserved for future use.	This bit is always 0.
10	1024	Reserved for future use.	This bit is always 0.
9	512	Reserved for future use.	This bit is always 0.
8	256	Reserved for future use.	This bit is always 0.
7	128	Reserved for future use.	This bit is always 0.
6	64	Reserved for future use.	This bit is always 0.
5	32	Reserved for future use.	This bit is always 0.
4	16	MS reject order received	This bit is a 1 if the Service Option or Radio Configuration change was rejected by the MS while connected.
3	8	Carrier not detected on new channel	This bit is a 1 when no power is detected by the test set after a handoff or when making a call.
2	4	Handoff completion not received	This bit is a 1 if the test set does not receive a handoff completion message from the MS.
1	2	Release order not received	This bit is a 1 if the MS does not send the call release to the test set.
0	1	Extension Bit	This bit is always 0.

Program Example - STATUS:QUESTIONABLE:CALL:TA2000 Register Bit Assignment

```
OUTPUT 714;"STATUS:QUESTIONABLE:CALL:TA2000:EVENT?"
!Queries and clears the Questionable Call Event Register
OUTPUT 714;"STATUS:QUESTIONABLE:CALL:TA2000:CONDITION?"
!Queries and clears the Questionable Call Condition Register
OUTPUT 714;"STATUS:QUESTIONABLE:CALL:TA2000:ENABLE 1024"
!Sets the Questionable Call Enable Register for bit 10
OUTPUT 714;"STATUS:QUESTIONABLE:CALL:TA2000:NTRANSITION 2"
!Sets the Questionable Call Negative Transition Filter Register for bit 1
OUTPUT 714;"STATUS:QUESTIONABLE:CALL:TA2000:PTRANSITION 2"
!Sets the Questionable Call Positive Transition Filter Register for bit 1
```

STATUS:QUESTIONable:ERRORs Condition Register Bit Assignment

The STATUS:QUESTIONable:ERRORs register bits will be used to indicate information about test set device-specific errors (positive error numbers).

Bit Number	Binary Weighting	Condition	Description
15	32768	Not Used. Defined by SCPI.	This bit is always 0.
14	16384	Reserved for future use.	This bit is always 0.
13	8192	Reserved for future use.	This bit is always 0.
12	4096	Reserved for future use.	This bit is always 0.
11	2048	Reserved for future use.	This bit is always 0.
10	1024	Reserved for future use.	This bit is always 0.
9	512	TA2000 Summary bit	This bit is the summary message bit for the STATUS:QUESTIONable:ERRORs:TA2000 register.
8	256	CDMA Summary bit	This bit is the summary message bit for the STATUS:QUESTIONable:ERRORs:CDMA register.
7	128	DIGital2000 Summary bit	This bit is the summary message bit for the STATUS:QUESTIONable:ERRORs:DIGital2000 register.
6	64	DIGital95 Summary bit	This bit is the summary message bit for the STATUS:QUESTIONable:ERRORs:DIGital95 register.
5	32	TA136 Summary bit	This bit is the summary message bit for the STATUS:QUESTIONable:ERRORs:TA136 register.
4	16	DIGital136 Summary bit	This bit is the summary message bit for the STATUS:QUESTIONable:ERRORs:DIGital136 register.
3	8	AMPS Summary bit	This bit is the summary message bit for the STATUS:QUESTIONable:ERRORs:AMPS register.
2	4	GSM Summary bit	This bit is the summary message bit for the STATUS:QUESTIONable:ERRORs:GSM register.
1	2	COMMon Summary bit	This bit is the summary message bit for the STATUS:QUESTIONable:CALL:ERRORs register.
0	1	Extension Bit	This bit is always 0.

Diagram Conventions

Program Example - STATUS:QUESTIONABLE:ERRORS Register Bit Assignments

```
OUTPUT 714;"STATUS:QUESTIONABLE:ERRORS:EVENT?" !Queries and clears the Questionable
          !Errors Event Register
OUTPUT 714;"STATUS:QUESTIONABLE:ERRORS:CONDITION?" !Queries and clears the Questionable
          !Errors Condition Register
OUTPUT 714;"STATUS:QUESTIONABLE:ERRORS:ENABLE 1024" !Sets the Questionable
          !Errors Enable
          !Register for bit 10
OUTPUT 714;"STATUS:QUESTIONABLE:ERRORS:NTRANSITION 2" !Sets the Questionable Errors
          !Negative Transition Filter Register
          !for bit 1
OUTPUT 714;"STATUS:QUESTIONABLE:ERRORS:PTRANSITION 2" !Sets the Questionable Errors
          !Positive Transition Filter Register
          !for bit 1
```


STATUS:QUESTIONable:ERRORs:AMPS Condition Register Bit Assignment

The STATUS:QUESTIONable:ERRORs:AMPS register bits will be used to indicate information about AMPS related test set device-specific errors (positive error numbers).

Bit Number	Binary Weighting	Condition	Description
15	32768	Not Used. Defined by SCPI.	This bit is always 0.
14	16384	MUI Maskable Messages	This bit is a 1 when a Maskable Message has occurred. Maskable Messages are not displayed on the test set display.
13	8192	Reserved for future use.	This bit is always 0.
12	4096	Reserved for future use.	This bit is always 0.
11	2048	Reserved for future use.	This bit is always 0.
10	1024	Reserved for future use.	This bit is always 0.
9	512	+900 Errors	The condition bit is pulsed to a 1 and immediately back to 0 if an error in the +900 to +999 range occurs. After setting the Positive Transition Filter and the Negative Transition Filter you can query the Event Register to find out which of these errors occurred.
8	256	+800 Errors	The condition bit is pulsed to a 1 and immediately back to 0 if an error in the +800 to +899 range occurs. After setting the Positive Transition Filter and the Negative Transition Filter you can query the Event Register to find out which of these errors occurred.
7	128	+700 Errors	The condition bit is pulsed to a 1 and immediately back to 0 if an error in the +700 to +799 range occurs. After setting the Positive Transition Filter and the Negative Transition Filter you can query the Event Register to find out which of these errors occurred.
6	64	+600 Errors	The condition bit is pulsed to a 1 and immediately back to 0 if an error in the +600 to +699 range occurs. After setting the Positive Transition Filter and the Negative Transition Filter you can query the Event Register to find out which of these errors occurred.
5	32	+500 Errors	The condition bit is pulsed to a 1 and immediately back to 0 if an error in the +500 to +599 range occurs. After setting the Positive Transition Filter and the Negative Transition Filter you can query the Event Register to find out which of these errors occurred.

Diagram Conventions

Bit Number	Binary Weighting	Condition	Description
4	16	+400 Errors	The condition bit is pulsed to a 1 and immediately back to 0 if an error in the +400 to +499 range occurs. After setting the Positive Transition Filter and the Negative Transition Filter you can query the Event Register to find out which of these errors occurred.
3	8	+300 Errors	The condition bit is pulsed to a 1 and immediately back to 0 if an error in the +300 to +399 range occurs. After setting the Positive Transition Filter and the Negative Transition Filter you can query the Event Register to find out which of these errors occurred.
2	4	+200 Errors	The condition bit is pulsed to a 1 and immediately back to 0 if an error in the +200 to +299 range occurs. After setting the Positive Transition Filter and the Negative Transition Filter you can query the Event Register to find out which of these errors occurred.
1	2	+100 Errors	The condition is be pulsed to a 1 and immediately back to 0 if an error in the +100 to +199 range occurs. After setting the Positive Transition Filter and the Negative Transition Filter you can query the Event Register to find out which of these errors occurred.
0	1	Extension Bit	This bit is always 0.

Program Example - STATUS:QUESTIONABLE:ERRORS:AMPS Condition Register Bit Assignment

```

OUTPUT 714;"STATUS:QUESTIONABLE:ERRORS:AMPS:EVENT?" !Queries and clears the Questionable
!Errors AMPS Event Register
OUTPUT 714;"STATUS:QUESTIONABLE:ERRORS:AMPS:CONDITION?" !Queries and clears the Questionable
!Errors AMPS Condition Register
OUTPUT 714;"STATUS:QUESTIONABLE:ERRORS:AMPS:ENABLE 1024" !Sets the Questionable
!Errors AMPS Enable
!Register for bit 10
OUTPUT 714;"STATUS:QUESTIONABLE:ERRORS:AMPS:NTRANSITION 2" !Sets the Questionable Errors
!AMPS Negative Transition
!Filter Register for bit 1
OUTPUT 714;"STATUS:QUESTIONABLE:ERRORS:AMPS:PTRANSITION 2" !Sets the Questionable Errors
!AMPS Positive Transition
!Filter Register for bit 1

```

STATUS:QUESTIONable:ERRORs:CDMA Register Bit Assignments

The STATUS:QUESTIONable:ERRORs:CDMA register bits will be used to indicate information about CDMA related test set device-specific errors (positive error numbers).

Bit Number	Binary Weighting	Condition	Description
15	32768	Not Used. Defined by SCPI.	This bit is always 0.
14	16384	MUI Maskable Message.	
13	8192	Reserved for future use.	This bit is always 0.
12	4096	Reserved for future use.	This bit is always 0.
11	2048	Reserved for future use.	This bit is always 0.
10	1024	Reserved for future use.	This bit is always 0.
9	512	+900 Errors	The condition bit is pulsed to a 1 and immediately back to 0 if an error in the +900 to +999 range occurs. After setting the Positive Transition Filter and the Negative Transition Filter you can query the Event Register to find out which of these errors occurred.
8	256	+800 Errors	The condition bit is pulsed to a 1 and immediately back to 0 if an error in the +800 to +899 range occurs. After setting the Positive Transition Filter and the Negative Transition Filter you can query the Event Register to find out which of these errors occurred.
7	128	+700 Errors	The condition bit will be pulsed to a 1 and immediately back to 0 if an error in the +700 to +799 range occurs. After setting the Positive Transition Filter and the Negative Transition Filter you may query the Event Register to find out which of these errors occurred.
6	64	+600 Errors	The condition bit is pulsed to a 1 and immediately back to 0 if an error in the +600 to +699 range occurs. After setting the Positive Transition Filter and the Negative Transition Filter you can query the Event Register to find out which of these errors occurred.
5	32	+500 Errors	The condition bit is pulsed to a 1 and immediately back to 0 if an error in the +500 to +599 range occurs. After setting the Positive Transition Filter and the Negative Transition Filter you can query the Event Register to find out which of these errors occurred.

Diagram Conventions

Bit Number	Binary Weighting	Condition	Description
4	16	+400 Errors	The condition bit is pulsed to a 1 and immediately back to 0 if an error in the +400 to +499 range occurs. After setting the Positive Transition Filter and the Negative Transition Filter you can query the Event Register to find out which of these errors occurred.
3	8	+300 Errors	The condition bit is pulsed to a 1 and immediately back to 0 if an error in the +300 to +399 range occurs. After setting the Positive Transition Filter and the Negative Transition Filter you can query the Event Register to find out which of these errors occurred.
2	4	+200 Errors	The condition bit is pulsed to a 1 and immediately back to 0 if an error in the +200 to +299 range occurs. After setting the Positive Transition Filter and the Negative Transition Filter you can query the Event Register to find out which of these errors occurred.
1	2	+100 Errors	The condition bit is pulsed to a 1 and immediately back to 0 if an error in the +100 to +199 range occurs. After setting the Positive Transition Filter and the Negative Transition Filter you can query the Event Register to find out which of these errors occurred.
0	1	Extension Bit	This bit is always 0.

Program Example - STATUS:QUESTIONABLE:ERRORS:CDMA Condition Register Bit Assignment

```

OUTPUT 714;"STATUS:QUESTIONABLE:ERRORS:CDMA:EVENT?" !Queries and clears the Questionable
!Errors CDMA Event Register
OUTPUT 714;"STATUS:QUESTIONABLE:ERRORS:CDMA:CONDITION?" !Queries and clears the Questionable
!Errors CDMA Condition Register
OUTPUT 714;"STATUS:QUESTIONABLE:ERRORS:CDMA:ENABLE 1024" !Sets the Questionable
!Errors CDMA Enable
!Register for bit 10
OUTPUT 714;"STATUS:QUESTIONABLE:ERRORS:CDMA:NTRANSITION 2" !Sets the Questionable Errors
!CDMA Negative Transition
!Filter Register for bit 1
OUTPUT 714;"STATUS:QUESTIONABLE:ERRORS:CDMA:PTRANSITION 2" !Sets the Questionable Errors
!CDMA Positive Transition
!Filter Register for bit 1

```

STATUS:QUESTIONable:ERRORs:COMMON Register Bit Assignments

The STATUS:QUESTIONable:ERRORs:COMMON register bits will be used to indicate information about the COMMON test set device-specific errors (positive error numbers).

Bit Number	Binary Weighting	Condition	Description
15	32768	Not Used. Defined by SCPI.	This bit is always 0.
14	16384	Reserved for future use.	This bit is always 0.
13	8192	Reserved for future use.	This bit is always 0.
12	4096	Reserved for future use.	This bit is always 0.
11	2048	Reserved for future use.	This bit is always 0.
10	1024	Reserved for future use.	This bit is always 0.
9	512	+900 Errors	The condition bit is pulsed to a 1 and immediately back to 0 if an error in the +900 to +999 range occurs. After setting the Positive Transition Filter and the Negative Transition Filter you can query the Event Register to find out which of these errors occurred.
8	256	+800 Errors	The condition bit is pulsed to a 1 and immediately back to 0 if an error in the +800 to +899 range occurs. After setting the Positive Transition Filter and the Negative Transition Filter you can query the Event Register to find out which of these errors occurred.
7	128	+700 Errors	The condition bit is pulsed to a 1 and immediately back to 0 if an error in the +700 to +799 range occurs. After setting the Positive Transition Filter and the Negative Transition Filter you can query the Event Register to find out which of these errors occurred.
6	64	+600 Errors	The condition bit is pulsed to a 1 and immediately back to 0 if an error in the +600 to +699 range occurs. After setting the Positive Transition Filter and the Negative Transition Filter you can query the Event Register to find out which of these errors occurred.
5	32	+500 Errors	The condition bit is pulsed to a 1 and immediately back to 0 if an error in the +500 to +599 range occurs. After setting the Positive Transition Filter and the Negative Transition Filter you can query the Event Register to find out which of these errors occurred.

Diagram Conventions

Bit Number	Binary Weighting	Condition	Description
4	16	+400 Errors	The condition bit is pulsed to a 1 and immediately back to 0 if an error in the +400 to +499 range occurs. After setting the Positive Transition Filter and the Negative Transition Filter you can query the Event Register to find out which of these errors occurred.
3	8	+300 Errors	The condition bit is pulsed to a 1 and immediately back to 0 if an error in the +300 to +399 range occurs. After setting the Positive Transition Filter and the Negative Transition Filter you can query the Event Register to find out which of these errors occurred.
2	4	+200 Errors	The condition bit is pulsed to a 1 and immediately back to 0 if an error in the +200 to +299 range occurs. After setting the Positive Transition Filter and the Negative Transition Filter you can query the Event Register to find out which of these errors occurred.
1	2	+100 Errors	The condition bit is pulsed to a 1 and immediately back to 0 if an error in the +100 to +199 range occurs. After setting the Positive Transition Filter and the Negative Transition Filter you can query the Event Register to find out which of these errors occurred.
0	1	Extension Bit	This bit is always 0.

Program Example - STATUS:QUESTIONABLE:ERRORS:COMMON Register Bit Assignments

```

OUTPUT 714;"STATUS:QUESTIONABLE:ERRORS:COMMON:EVENT?" !Queries and clears the Questionable
!Errors Common Event Register
OUTPUT 714;"STATUS:QUESTIONABLE:ERRORS:COMMON:CONDITION?" !Queries the and clears the
!Questionable Errors
!Common Condition Register
OUTPUT 714;"STATUS:QUESTIONABLE:ERRORS:COMMON:ENABLE 1024" !Sets the Questionable
!Errors Common Enable
!Register for bit 10
OUTPUT 714;"STATUS:QUESTIONABLE:ERRORS:COMMON:NTRANSITION 2" !Sets the Questionable Errors
!Common Negative Transition
!Register for bit 1
OUTPUT 714;"STATUS:QUESTIONABLE:ERRORS:COMMON:PTRANSITION 2" !Sets the Questionable Errors
!Common Positive Transition
!Register for bit 1

```

STATUS:QUESTIONable:ERRORs:DIGital2000Condition Register Bit Assignment

The STATUS:QUESTIONable:ERRORs:DIGital2000 register bits will be used to indicate information about DIGital2000 related test set device-specific errors (positive error numbers).

Bit Number	Binary Weighting	Condition	Description
15	32768	Not Used. Defined by SCPI.	This bit is always 0.
14	16384	MUI Maskable Message.	
13	8192	Reserved for future use.	This bit is always 0.
12	4096	Reserved for future use.	This bit is always 0.
11	2048	Reserved for future use.	This bit is always 0.
10	1024	Reserved for future use.	This bit is always 0.
9	512	+900 Errors	The condition bit is pulsed to a 1 and immediately back to 0 if an error in the +900 to +999 range occurs. After setting the Positive Transition Filter and the Negative Transition Filter you can query the Event Register to find out which of these errors occurred.
8	256	+800 Errors	The condition bit is pulsed to a 1 and immediately back to 0 if an error in the +800 to +899 range occurs. After setting the Positive Transition Filter and the Negative Transition Filter you can query the Event Register to find out which of these errors occurred.
7	128	+700 Errors	The condition bit is pulsed to a 1 and immediately back to 0 if an error in the +700 to +799 range occurs. After setting the Positive Transition Filter and the Negative Transition Filter you can query the Event Register to find out which of these errors occurred.
6	64	+600 Errors	The condition bit is pulsed to a 1 and immediately back to 0 if an error in the +600 to +699 range occurs. After setting the Positive Transition Filter and the Negative Transition Filter you can query the Event Register to find out which of these errors occurred.
5	32	+500 Errors	The condition bit is pulsed to a 1 and immediately back to 0 if an error in the +500 to +599 range occurs. After setting the Positive Transition Filter and the Negative Transition Filter you can query the Event Register to find out which of these errors occurred.

Diagram Conventions

Bit Number	Binary Weighting	Condition	Description
4	16	+400 Errors	The condition bit is pulsed to a 1 and immediately back to 0 if an error in the +400 to +499 range occurs. After setting the Positive Transition Filter and the Negative Transition Filter you can query the Event Register to find out which of these errors occurred.
3	8	+300 Errors	The condition bit is pulsed to a 1 and immediately back to 0 if an error in the +300 to +399 range occurs. After setting the Positive Transition Filter and the Negative Transition Filter you can query the Event Register to find out which of these errors occurred.
2	4	+200 Errors	The condition bit is pulsed to a 1 and immediately back to 0 if an error in the +200 to +299 range occurs. After setting the Positive Transition Filter and the Negative Transition Filter you can query the Event Register to find out which of these errors occurred.
1	2	+100 Errors	The condition bit is pulsed to a 1 and immediately back to 0 if an error in the +100 to +199 range occurs. After setting the Positive Transition Filter and the Negative Transition Filter you can query the Event Register to find out which of these errors occurred.
0	1	Extension Bit	This bit will always be 0.

Program Example - STATUS:QUESTIONABLE:ERRORS:DIGITAL2000 Condition Register Bit Assignment

```

OUTPUT 714;"STATUS:QUESTIONABLE:ERRORS:DIGITAL2000:EVENT?" !Queries and clears the
!Questionable Errors
!DIGITAL2000 Event Register
OUTPUT 714;"STATUS:QUESTIONABLE:ERRORS:DIGITAL2000:CONDITION?" !Queries and clears the
!Questionable Errors
!DIGITAL2000 Condition Register
OUTPUT 714;"STATUS:QUESTIONABLE:ERRORS:DIGITAL2000:ENABLE 1024" !Sets the Questionable
!Errors DIGITAL2000 Enable
!Register for bit 10
OUTPUT 714;"STATUS:QUESTIONABLE:ERRORS:DIGITAL2000:NTRANSITION 2" !Sets the Questionable
!Errors DIGITAL2000 Negative
!Transition Filter
!Register for bit 1
OUTPUT 714;"STATUS:QUESTIONABLE:ERRORS:DIGITAL2000:PTRANSITION 2" !Sets the Questionable
!Errors DIGITAL2000 Positive
!Transition Filter
!Register for bit 1

```


STATUS:QUESTIONable:ERRORs:DIGital95 Condition Register Bit Assignment

The STATUS:QUESTIONable:ERRORs:DIGital95 register bits will be used to indicate information about DIGital95 related test set device-specific errors (positive error numbers).

Bit Number	Binary Weighting	Condition	Description
15	32768	Not Used. Defined by SCPI.	This bit is always 0.
14	16384	MUI Maskable Message.	
13	8192	Reserved for future use.	This bit is always 0.
12	4096	Reserved for future use.	This bit is always 0.
11	2048	Reserved for future use.	This bit is always 0.
10	1024	Reserved for future use.	This bit is always 0.
9	512	+900 Errors	The condition bit is pulsed to a 1 and immediately back to 0 if an error in the +900 to +999 range occurs. After setting the Positive Transition Filter and the Negative Transition Filter you can query the Event Register to find out which of these errors occurred.
8	256	+800 Errors	The condition bit is pulsed to a 1 and immediately back to 0 if an error in the +800 to +899 range occurs. After setting the Positive Transition Filter and the Negative Transition Filter you can query the Event Register to find out which of these errors occurred.
7	128	+700 Errors	The condition bit is pulsed to a 1 and immediately back to 0 if an error in the +700 to +799 range occurs. After setting the Positive Transition Filter and the Negative Transition Filter you can query the Event Register to find out which of these errors occurred.
6	64	+600 Errors	The condition bit is pulsed to a 1 and immediately back to 0 if an error in the +600 to +699 range occurs. After setting the Positive Transition Filter and the Negative Transition Filter you can query the Event Register to find out which of these errors occurred.
5	32	+500 Errors	The condition bit is pulsed to a 1 and immediately back to 0 if an error in the +500 to +599 range occurs. After setting the Positive Transition Filter and the Negative Transition Filter you can query the Event Register to find out which of these errors occurred.

Diagram Conventions

Bit Number	Binary Weighting	Condition	Description
4	16	+400 Errors	The condition bit is pulsed to a 1 and immediately back to 0 if an error in the +400 to +499 range occurs. After setting the Positive Transition Filter and the Negative Transition Filter you can query the Event Register to find out which of these errors occurred.
3	8	+300 Errors	The condition bit is pulsed to a 1 and immediately back to 0 if an error in the +300 to +399 range occurs. After setting the Positive Transition Filter and the Negative Transition Filter you can query the Event Register to find out which of these errors occurred.
2	4	+200 Errors	The condition bit is pulsed to a 1 and immediately back to 0 if an error in the +200 to +299 range occurs. After setting the Positive Transition Filter and the Negative Transition Filter you can query the Event Register to find out which of these errors occurred.
1	2	+100 Errors	The condition bit is pulsed to a 1 and immediately back to 0 if an error in the +100 to +199 range occurs. After setting the Positive Transition Filter and the Negative Transition Filter you can query the Event Register to find out which of these errors occurred.
0	1	Extension Bit	This bit will always be 0.

Program Example - STATUS:QUESTIONABLE:ERRORS:DIGITAL95 Condition Register Bit Assignment

```

OUTPUT 714;"STATUS:QUESTIONABLE:ERRORS:DIGITAL95:EVENT?" !Queries and clears the
                                     !Questionable Errors
                                     !DIGITAL95 Event Register
OUTPUT 714;"STATUS:QUESTIONABLE:ERRORS:DIGITAL95:CONDITION?" !Queries and clears the
                                     !Questionable Errors
                                     !DIGITAL95 Condition Register
OUTPUT 714;"STATUS:QUESTIONABLE:ERRORS:DIGITAL95:ENABLE 1024" !Sets the Questionable
                                     !Errors DIGITAL95 Enable
                                     !Register for bit 10
OUTPUT 714;"STATUS:QUESTIONABLE:ERRORS:DIGITAL95:NTRANSITION 2" !Sets the Questionable
                                     !Errors DIGITAL95 Negative
                                     !Transition Filter
                                     !Register for bit 1
OUTPUT 714;"STATUS:QUESTIONABLE:ERRORS:DIGITAL95:PTRANSITION 2" !Sets the Questionable
                                     !Errors DIGITAL95 Positive
                                     !Transition Filter
                                     !Register for bit 1

```

STATUS:QUESTIONable:ERRORs:TA2000 Register Bit Assignments

The STATUS:QUESTIONable:ERRORs:TA2000 register bits will be used to indicate information about TA2000 related test set device-specific errors (positive error numbers).

Bit Number	Binary Weighting	Condition	Description
15	32768	Not Used. Defined by SCPI.	This bit is always 0.
14	16384	MUI Maskable Message.	
13	8192	Reserved for future use.	This bit is always 0.
12	4096	Reserved for future use.	This bit is always 0.
11	2048	Reserved for future use.	This bit is always 0.
10	1024	Reserved for future use.	This bit is always 0.
9	512	+900 Errors	The condition bit is pulsed to a 1 and immediately back to 0 if an error in the +900 to +999 range occurs. After setting the Positive Transition Filter and the Negative Transition Filter you can query the Event Register to find out which of these errors occurred.
8	256	+800 Errors	The condition bit is pulsed to a 1 and immediately back to 0 if an error in the +800 to +899 range occurs. After setting the Positive Transition Filter and the Negative Transition Filter you can query the Event Register to find out which of these errors occurred.
7	128	+700 Errors	The condition bit is pulsed to a 1 and immediately back to 0 if an error in the +700 to +799 range occurs. After setting the Positive Transition Filter and the Negative Transition Filter you can query the Event Register to find out which of these errors occurred.
6	64	+600 Errors	The condition bit is pulsed to a 1 and immediately back to 0 if an error in the +600 to +699 range occurs. After setting the Positive Transition Filter and the Negative Transition Filter you can query the Event Register to find out which of these errors occurred.
5	32	+500 Errors	The condition bit is pulsed to a 1 and immediately back to 0 if an error in the +500 to +599 range occurs. After setting the Positive Transition Filter and the Negative Transition Filter you can query the Event Register to find out which of these errors occurred.

Diagram Conventions

Bit Number	Binary Weighting	Condition	Description
4	16	+400 Errors	The condition bit is pulsed to a 1 and immediately back to 0 if an error in the +400 to +499 range occurs. Query the Event Register to find out if one of these errors occurred.
3	8	+300 Errors	The condition bit is pulsed to a 1 and immediately back to 0 if an error in the +300 to +399 range occurs. After setting the Positive Transition Filter and the Negative Transition Filter you can query the Event Register to find out which of these errors occurred.
2	4	+200 Errors	The condition bit is pulsed to a 1 and immediately back to 0 if an error in the +200 to +299 range occurs. After setting the Positive Transition Filter and the Negative Transition Filter you can query the Event Register to find out which of these errors occurred.
1	2	+100 Errors	The condition bit is pulsed to a 1 and immediately back to 0 if an error in the +100 to +199 range occurs. After setting the Positive Transition Filter and the Negative Transition Filter you can query the Event Register to find out which of these errors occurred.
0	1	Extension Bit	This bit is always 0.

Program Example - STATUS:QUESTIONABLE:ERRORS:TA2000 Condition Register Bit Assignment

```

OUTPUT 714;"STATUS:QUESTIONABLE:ERRORS:TA2000:EVENT?" !Queries and clears the
                                     !Questionable Errors
                                     !TA2000 Event Register
OUTPUT 714;"STATUS:QUESTIONABLE:ERRORS:TA2000:CONDITION?" !Queries and clears the
                                     !Questionable Errors
                                     !TA2000 Condition Register
OUTPUT 714;"STATUS:QUESTIONABLE:ERRORS:TA2000:ENABLE 1024" !Sets the Questionable
                                     !Errors TA2000 Enable
                                     !Register for bit 10
OUTPUT 714;"STATUS:QUESTIONABLE:ERRORS:TA2000:NTRANSITION 2" !Sets the Questionable
                                     !Errors TA2000 Negative
                                     !Transition Filter
                                     !Register for bit 1
OUTPUT 714;"STATUS:QUESTIONABLE:ERRORS:TA2000:PTRANSITION 2" !Sets the Questionable
                                     !Errors TA2000 Positive
                                     !Transition Filter
                                     !Register for bit 1

```

STATUS:QUESTIONABLE:HARDWARE Condition Register Bit Assignment

The STATUS:QUESTIONABLE:HARDWARE register bits give an indication that the data/signals currently being acquired or generated are of questionable quality.

Bit Number	Binary Weighting	Condition	Description
15	32768	Not Used. Defined by SCPI.	This bit is always 0.
14	16384	Reserved for future use.	This bit is always 0.
13	8192	Reserved for future use.	This bit is always 0.
12	4096	Reserved for future use.	This bit will always be 0.
11	2048	Reserved for future use.	This bit will always be 0.
10	1024	Reserved for future use.	This bit will always be 0.
9	512	Reserved for future use.	This bit will always be 0.
8	256	Reserved for future use.	This bit will always be 0.
7	128	Reserved for future use.	This bit will always be 0.
6	64	Reserved for future use.	This bit will always be 0.
5	32	Reserved for future use.	This bit will always be 0.
4	16	Power-up Self Test(s) Failed	This bit will be a 1 if the power-up self tests failed.
3	8	Reserved for future use.	This bit will always be 0.
2	4	Reserved for future use.	This bit will always be 0.
1	2	Reserved for future use.	This bit will always be 0.
0	1	Extension Bit.	This bit will always be 0.

Program Example - STATUS:QUESTIONABLE:HARDWARE Condition Register Bit Assignment

```

OUTPUT 714;"STATUS:QUESTIONABLE:HARDWARE:EVENT?" !Queries and clears the Questionable
!Hardware Event Register
OUTPUT 714;"STATUS:QUESTIONABLE:HARDWARE:CONDITION?" !Queries and clears the Questionable
!Hardware Condition Register
OUTPUT 714;"STATUS:QUESTIONABLE:HARDWARE:ENABLE 1024" !Sets the Questionable
!Hardware Enable
!Register for bit 10
OUTPUT 714;"STATUS:QUESTIONABLE:HARDWARE:NTRANSITION 2" !Sets the Questionable
!Hardware Negative Transition Filter
!Register for bit 1
OUTPUT 714;"STATUS:QUESTIONABLE:HARDWARE:PTRANSITION 2" !Sets the Questionable
!Hardware Positive Transition Filter
!Register for bit 1

```

Diagram Conventions

Status Byte Register

*STB?

*STB? _____ ↘ →

NOTE The Status Byte Register can also be read with a serial poll. For example, the command “Status_byte = SPOLL(714)” would perform a serial poll of the Status Byte Register, returning and releasing RQS (bit 6).

Standard Event Status Register

***ESR?**

*ESR? —————> Reads and clears the Std Event Status Register. —————>

***ESE?**

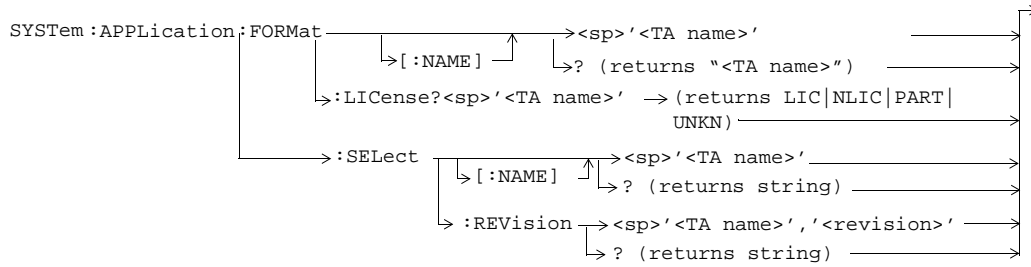
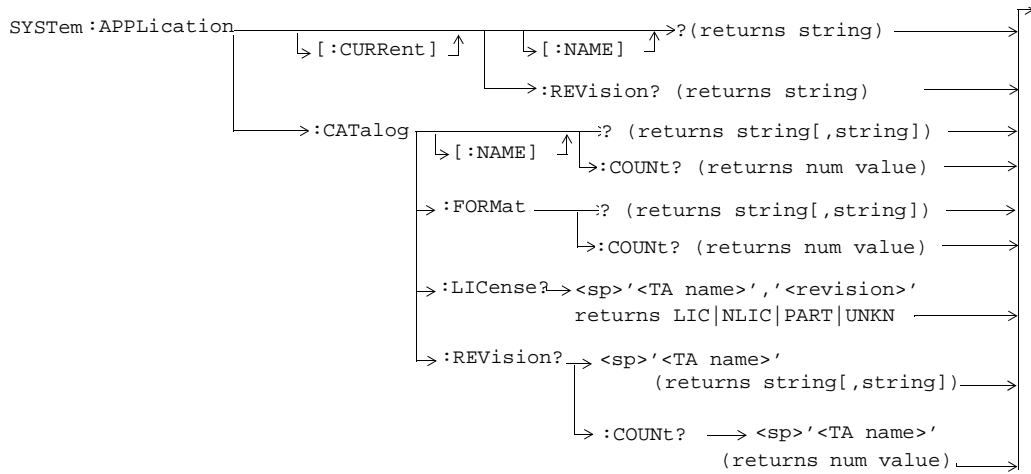
*ESE? —————> Reads the Std Event Status Register Enable Register —————>

***ESE**

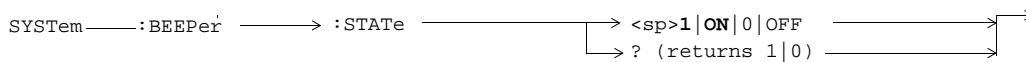
*ESE —————> Writes to the Std Event Status Register Enable Register —————>

Diagram Conventions

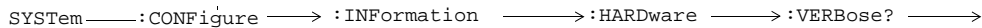
SYSTEM:APPLICATION



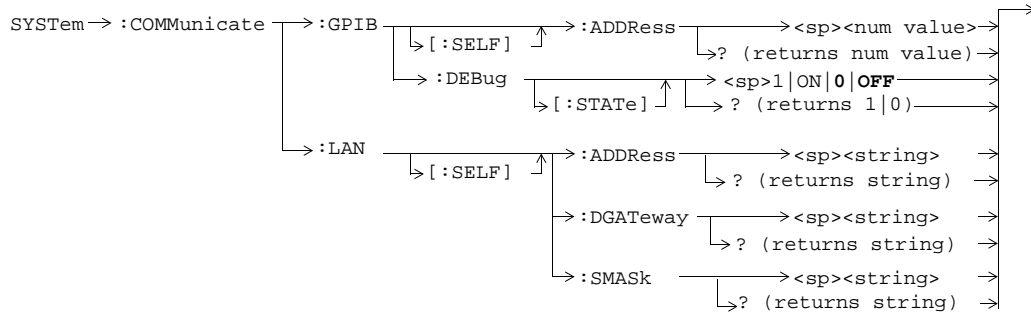
SYSTEM:BEEPER



SYSTEM:CONFigure:INFormation:HARDware:VERBose?



SYSTEM:COMMunicate



SYSTEM:CORRection

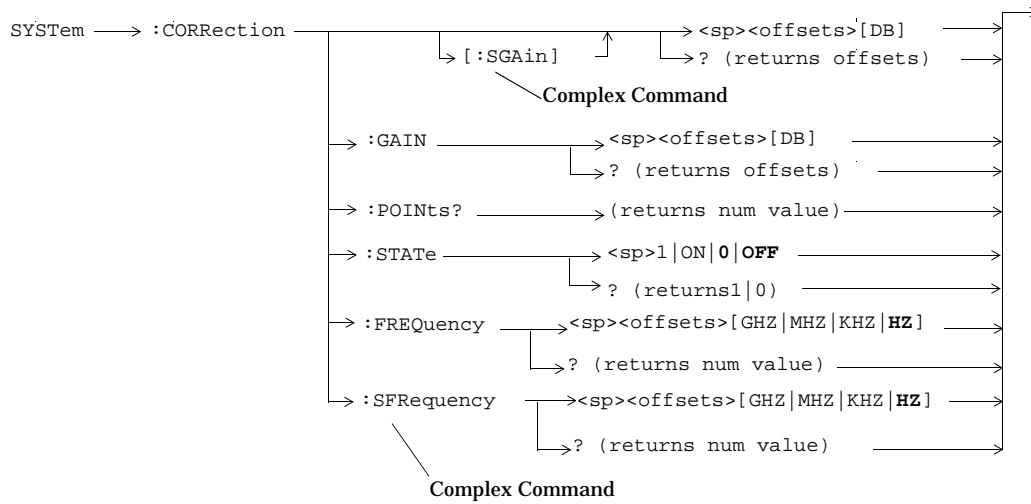
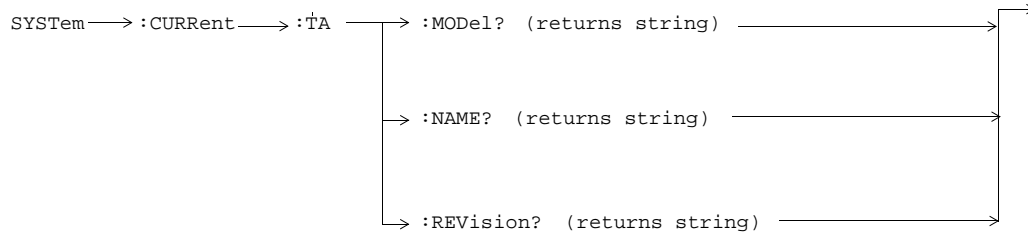
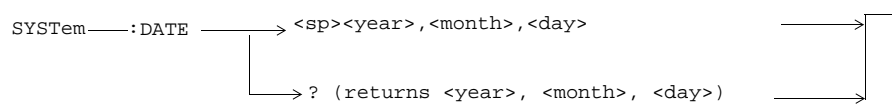


Diagram Conventions

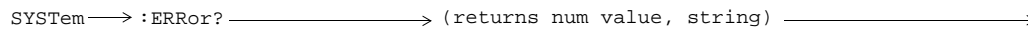
SYSTEM:CURRENT:TA



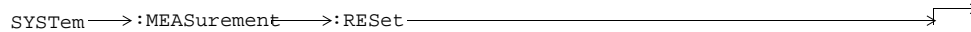
SYSTEM:DATE



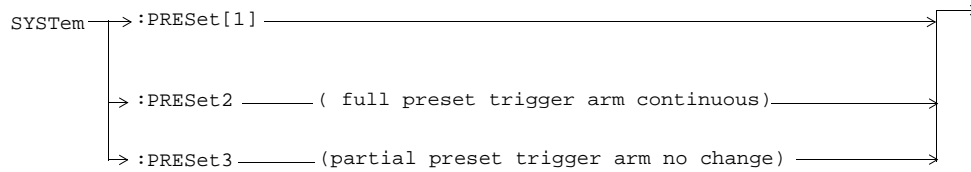
SYSTEM:ERROR?



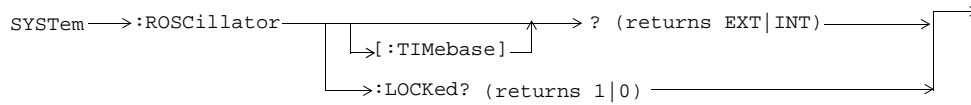
SYSTEM:MEASUREMENT:RESet



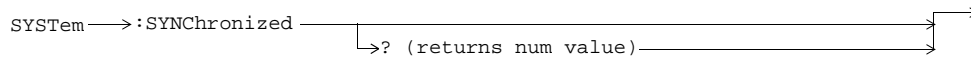
SYSTEM:PRESet



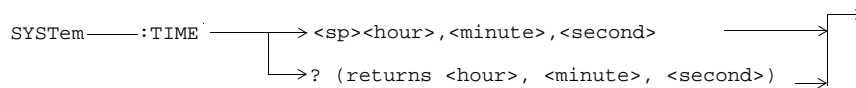
SYSTem:ROSCillator



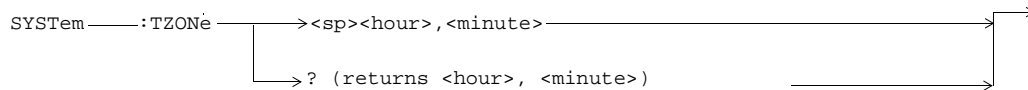
SYSTem:SYNChronized



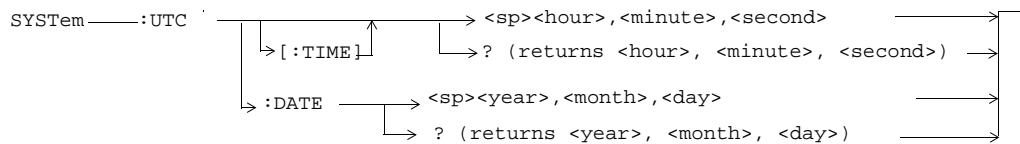
SYSTem:TIME



SYSTem:TZONE



SYSTem:UTC



IEEE 488.2 Common Commands

Description

*CLS

The *CLS, clear status command, is defined in "IEEE Std 488.2-1992", 10.3. This command will also clear and close the error message screen on the test set's display.

*ESE

The *ESE, standard event status enable command, is defined in "IEEE Std 488.2-1992", 10.10.

*ESE?

The *ESE?, standard event status enable query, is defined in "IEEE Std 488.2-1992", 10.11.

*ESR?

The *ESR?, standard event status register query, is defined in "IEEE Std 488.2-1992", 10.12.

*IDN?

The *IDN?, identification query, is defined in "IEEE Std 488.2-1992", 10.14. *IDN? is used to retrieve information about the test set in ASCII format.

*IDN?, returns ASCII codes 32 through 126 excluding comma and semicolon in four comma separated fields. Field 1 returns the manufacturer, field 2 returns the instrument model number, field 3 returns the serial number, field 4 returns 0.

*OPC

The *OPC, operation complete command, is defined in "IEEE 488.2-1992", 10.18. *OPC causes the test set to continuously sense the No Operation Pending flag. When the No Operation Pending flag becomes TRUE, the OPC event bit in the standard event status register (ESR) is set to indicate that the state of all pending operations is completed. The *OPC common command is not recommended for use as an overlapped command.

*OPC?

The *OPC?, operation complete query, is defined in "IEEE Std 488.2-1992", 10.19. The *OPC? query allows synchronization between the controller and the test set using either the message available (MAV) bit in the status byte, or a read of the output OPC?. The *OPC? query does not effect the OPC event bit in the Standard Event Status Register (ESR). The *OPC? common command is not recommended for use as an overlapped command.

*OPT?

The *OPT?, option identification query, is defined in "IEEE Std 488.2-1992", 10.20. Each option will have a unique name, that name will be returned with the query.

***RST**

The *RST, full preset command, is defined in “IEEE Std 488.2-1992”, 10.32. *RST is the recommended command when performing a full preset on the test set. A *RST restores the majority of settings to their default values.

- *RST sets trigger arm to single
- PRESet2 sets trigger arm to continuous

***SRE**

The *SRE, service request enable command, is defined in “IEEE Std 488.2-1992”, 10.34. The parameter range for this command is 0 through 255.

***SRE?**

The *SRE?, service request enable query, is defined in “IEEE Std 488.2-1992”, 10.35. Values returned by this query range from 0 through 255.

***STB?**

The *STB?, read status byte query, is defined in “IEEE Std 488.2-1992”, 10.36. Values returned by this query range from 0 through 255.

***WAI**

The *WAI, wait-to-continue command, is defined in “IEEE Std 488.2-1992”, 10.39. The *WAI command prevents the test set from executing any further commands or queries until all pending operation flags are false. The *WAI common command is not recommended for use as an overlapped command.

Diagram Conventions

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To find a syntax equivalent for a field on the Test Set's display.

1. Find the field name on the Test Set's display.
2. Look up the name in the alphabetical listing.
3. Turn to the page indicated.

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